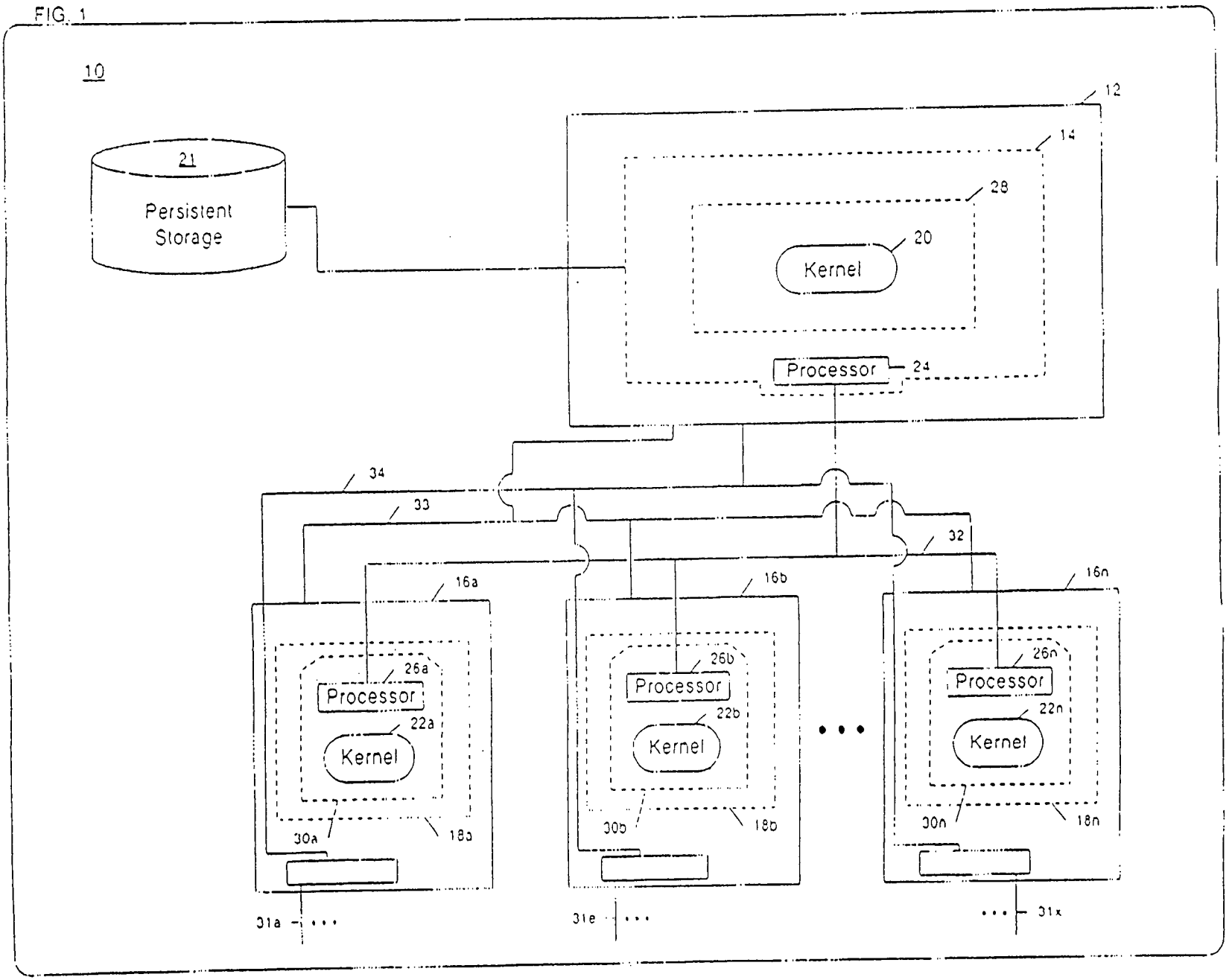
FIG. 1


10
12
14
28
20
Kernel
Processor
24
21
Persistent Storage
34
33
32
16a
16b
16n
26a
Processor
22a
Kernel
30a
18a
26b
Processor
22b
Kernel
30b
18b
26n
Processor
22n
Kernel
30n
18n
31a
31e
31x

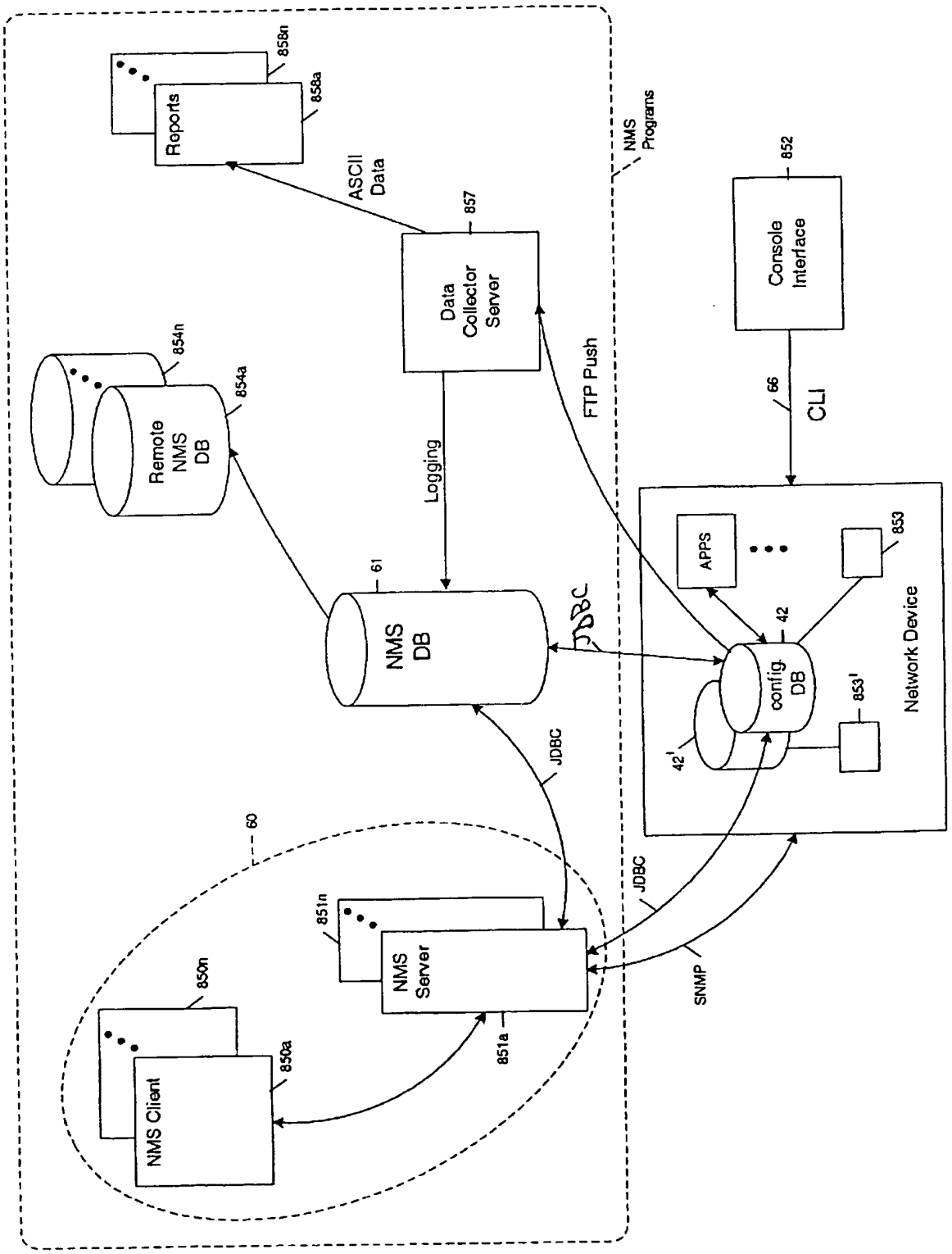

NMS Programs
Reports
858n
858a
ASCII Data
857
Data Collector Server
FTP Push
Console Interface
852
66
CLI
854n
854a
Remote NMS DB
Logging
61
NMS DB
JDBC
APPS
853
42
config DB
42'
853'
Network Device
60
851n
NMS Server
851a
850m
850a
NMS Client
JDBC
SNMP

Fig. 2a

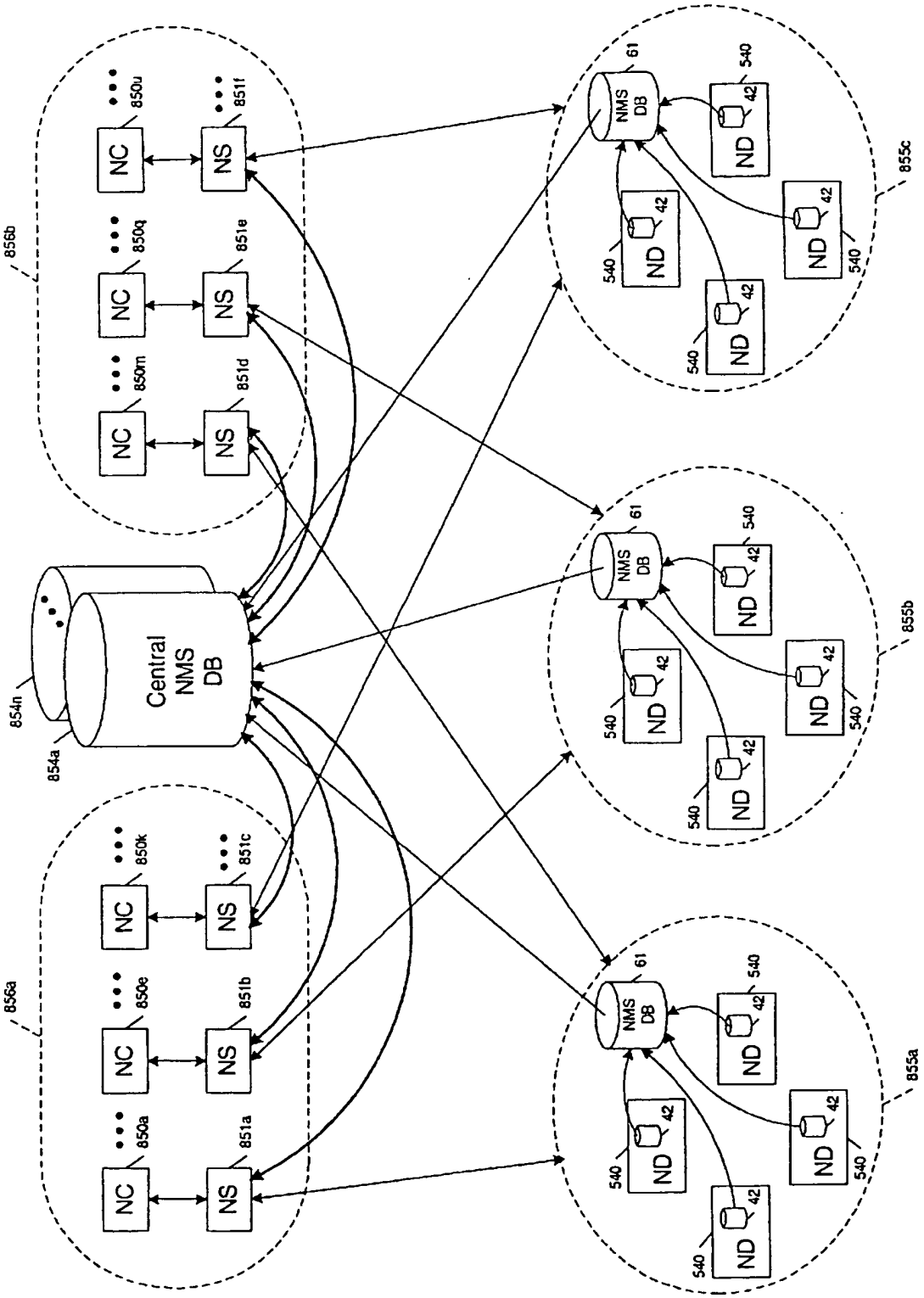

Central NMS DB
854n
854a
856a
856b
855a
855b
855c
NC
NS
ND
NMS DB
540
42
61

Fig. 2b

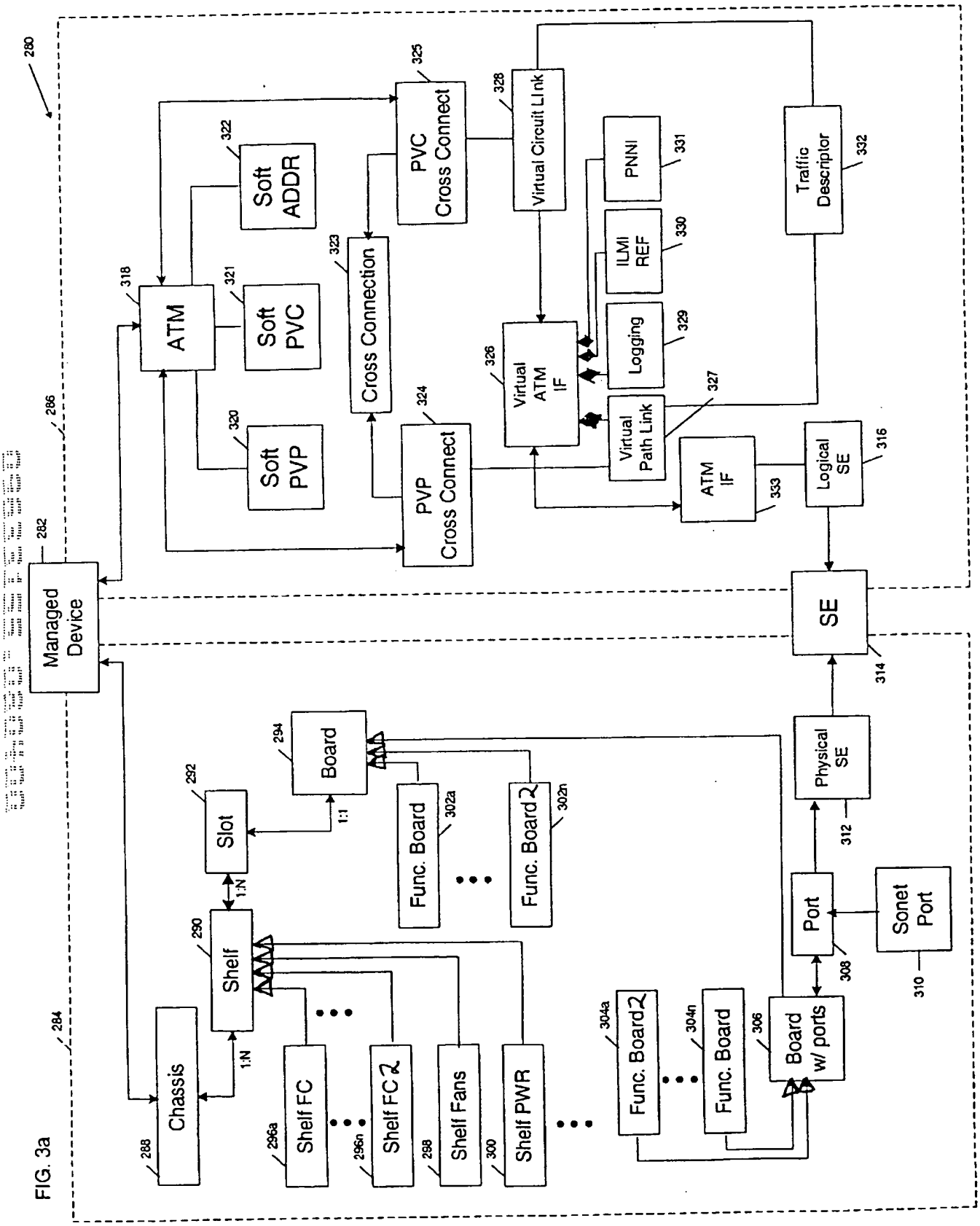

280
286
282
Managed Device
284
Chassis
288
Shelf
290
1:N
Slot
292
1:N
Board
294
1:1
Shelf FC
296a
Shelf FC 2
296n
Shelf Fans
298
Shelf PWR
300
Func. Board
302a
Func. Board 2
302n
Func. Board 2
304a
Func. Board
304n
Board w/ ports
306
Port
308
Sonet Port
310
Physical SE
312
SE
314
Logical SE
316
ATM
318
Soft PVP
320
Soft PVC
321
Soft ADDR
322
Cross Connection
323
PVP Cross Connect
324
PVC Cross Connect
325
Virtual ATM IF
326
Virtual Path Link
327
Virtual Circuit Link
328
Logging
329
ILMI REF
330
PNNI
331
Traffic Descriptor
332
ATM IF
333

FIG. 3a

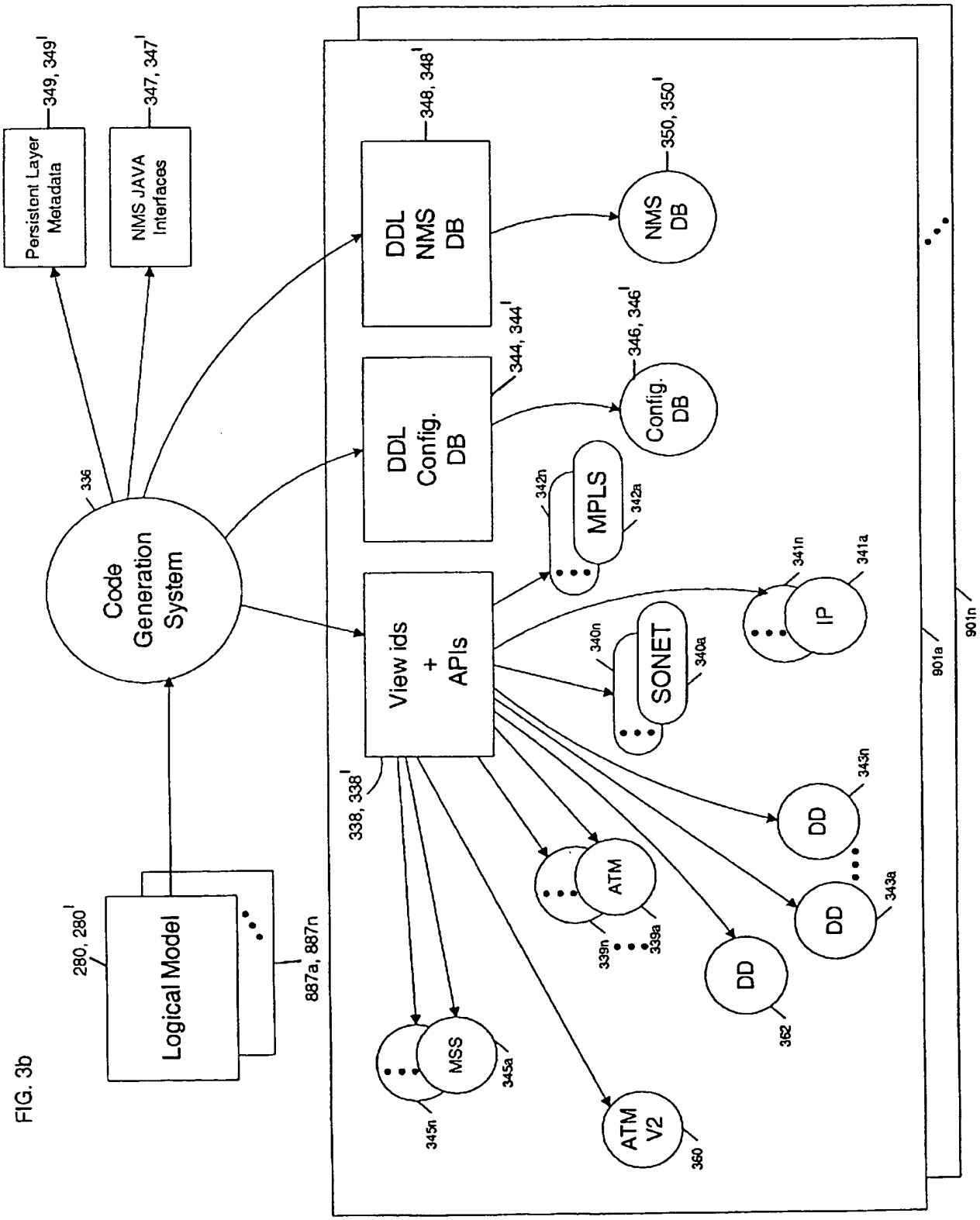

Persistent Layer Metadata
349, 349'
NMS JAVA Interfaces
347, 347'
336
Code Generation System
Logical Model
280, 280'
887a, 887n
DDL NMS DB
348, 348'
NMS DB
350, 350'
DDL Config. DB
344, 344'
Config. DB
346, 346'
MPLS
342n
342a
View ids + APIs
338, 338'
SONET
340n
340a
IP
341n
341a
ATM
339n
339a
DD
343n
DD
343a
DD
362
MSS
345n
345a
ATM V2
360
901a
901n

FIG. 3b

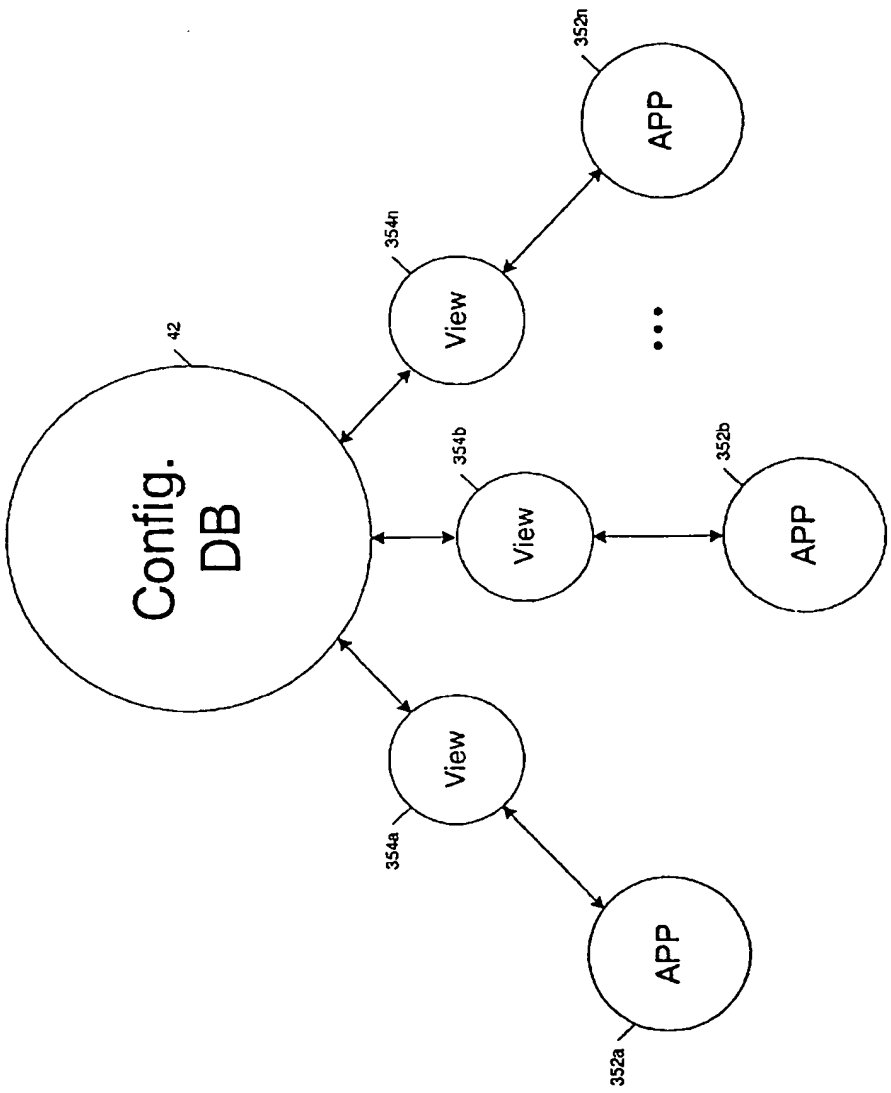
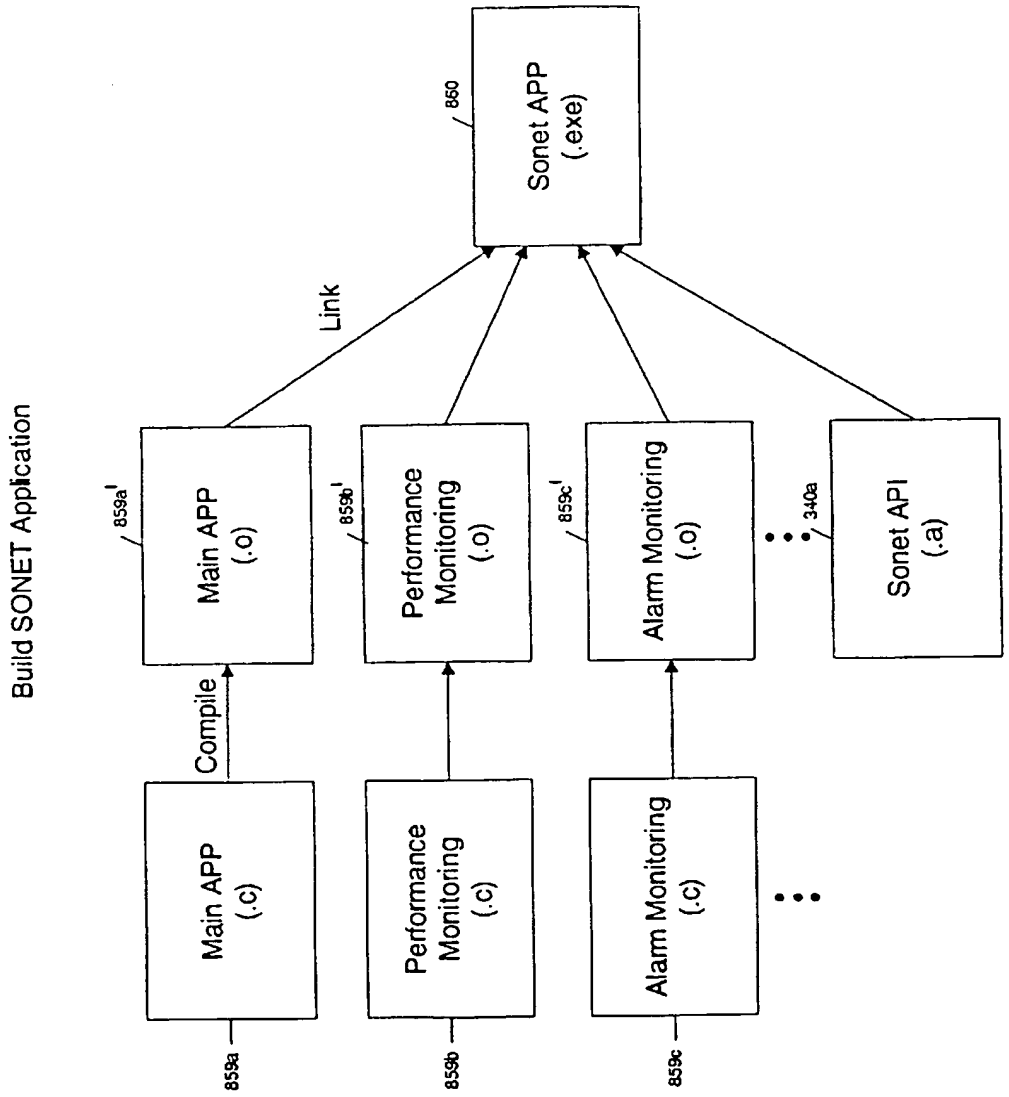

Config. DB
42
View
354n
View
354b
View
354a
APP
352n
APP
352b
APP
352a

FIG. 3c

Fig. 3d


Build SONET Application
Main APP (.c)
Compile
Main APP (.o)
Performance Monitoring (.c)
Performance Monitoring (.o)
Alarm Monitoring (.c)
Alarm Monitoring (.o)
Sonet API (.a)
Link
Sonet APP (.exe)
859a
859b
859c
859a'
859b'
859c'
340a
860

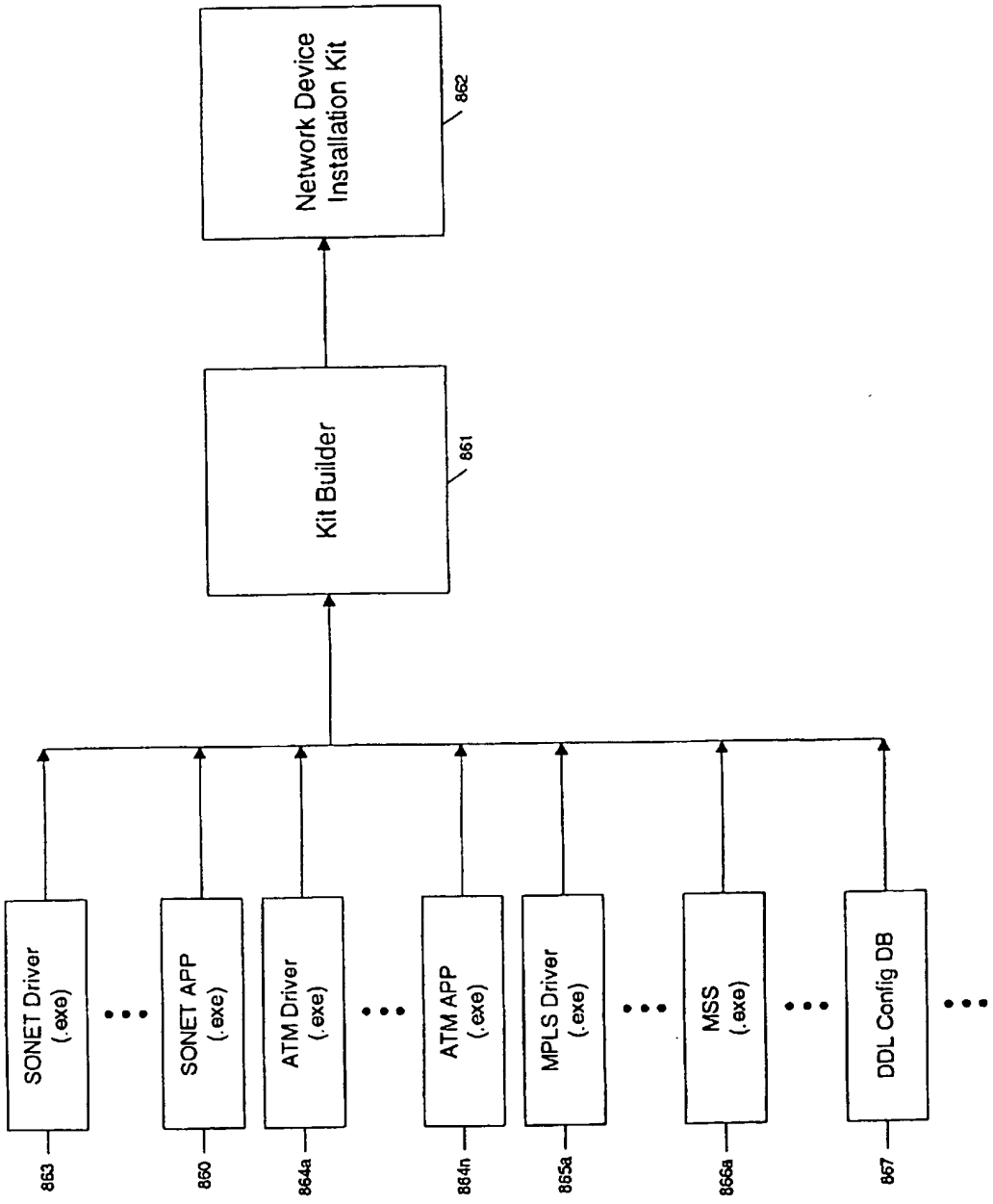
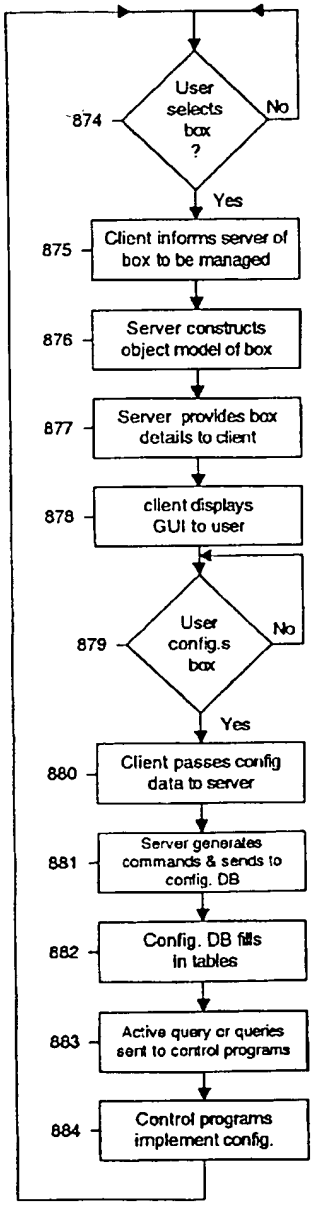

Network Device Installation Kit
862
Kit Builder
861
SONET Driver (.exe)
SONET APP (.exe)
ATM Driver (.exe)
ATM APP (.exe)
MPLS Driver (.exe)
MSS (.exe)
DDL Config DB
863
860
864a
864n
865a
866a
867

Fig. 3e

Fig. 3A

NMS Installation Kit
871
Kit Builder
861
DDL NMS DB
868
NMS JAVA Interfaces
869
Persistent Layer Metadata
870
NMS Server
885
NMS Client
886

Fig. 3g



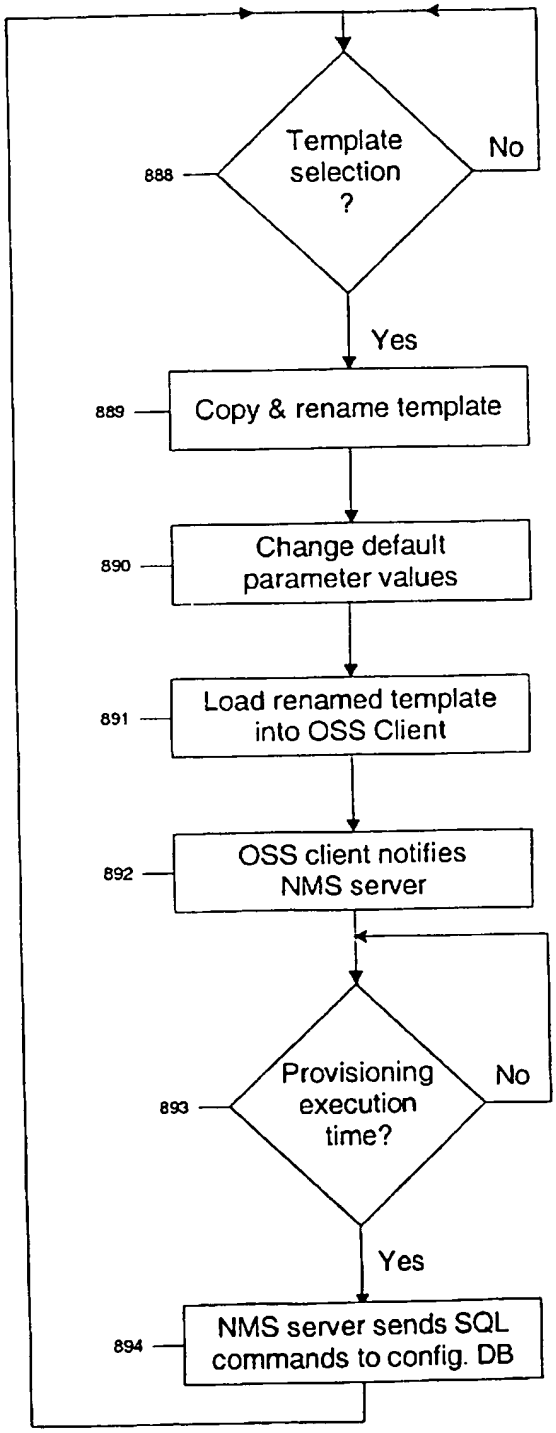


Fig. 3h

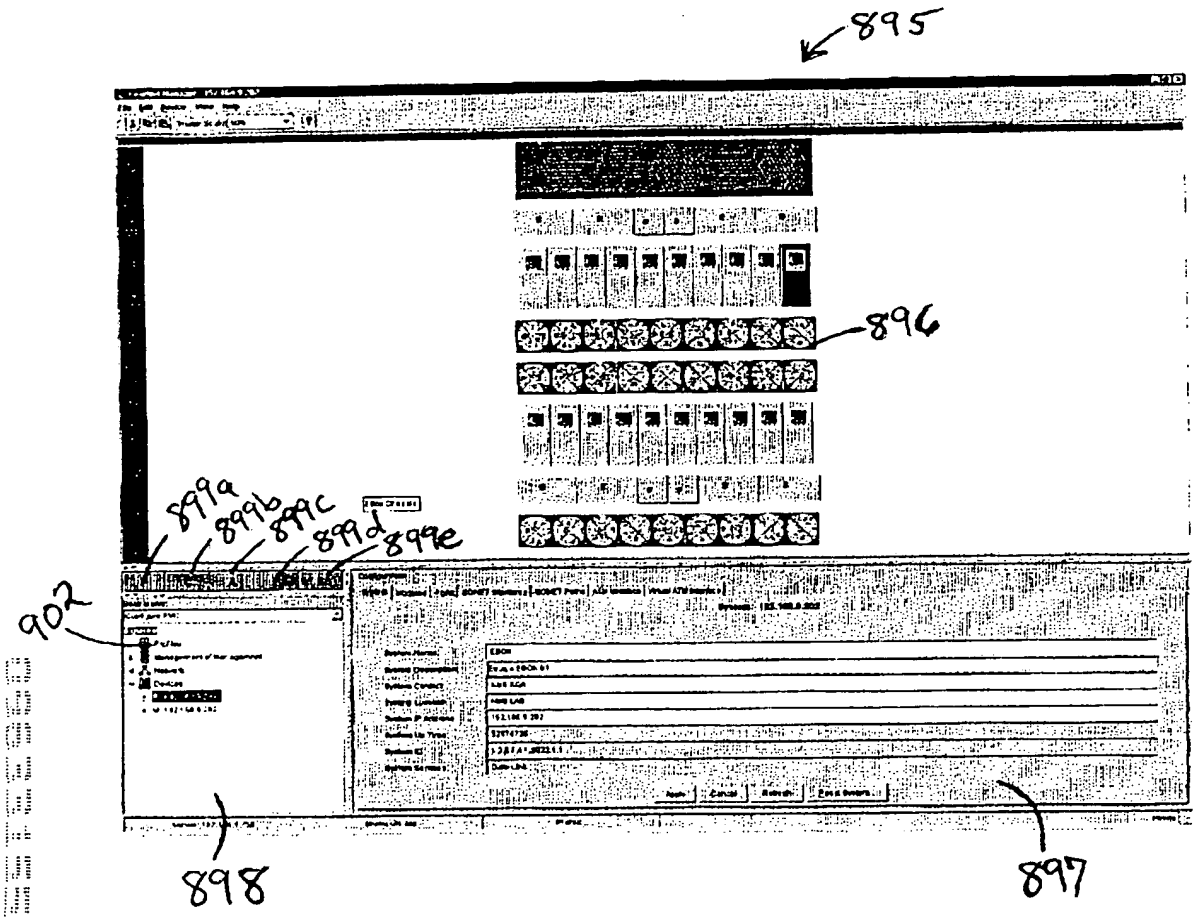


Fig. 4a

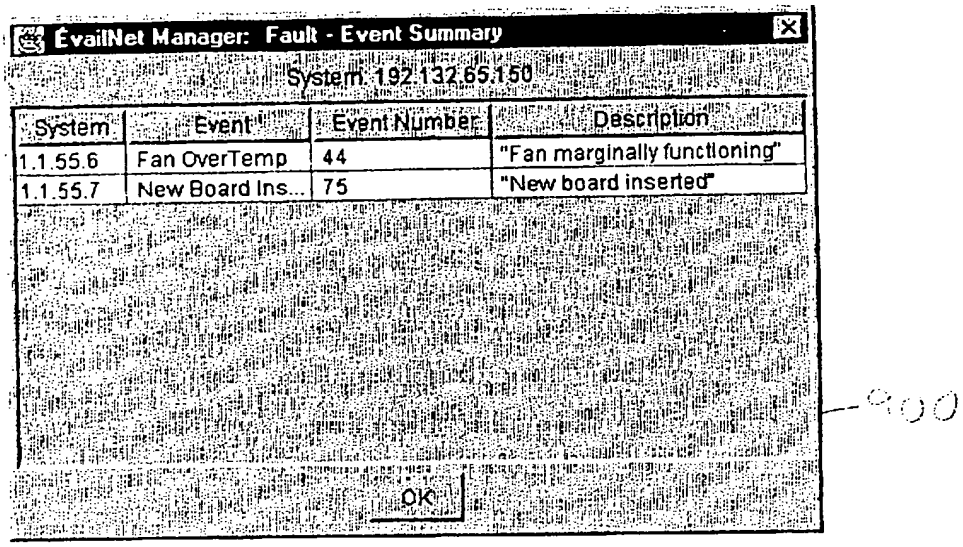

EvailNet Manager: Fault - Event Summary
System: 192.132.65.150
System
Event
Event Number
Description
1.1.55.6
Fan OverTemp
44
"Fan marginally functioning"
1.1.55.7
New Board Ins...
75
"New board inserted"
OK
400

Fig. 4b

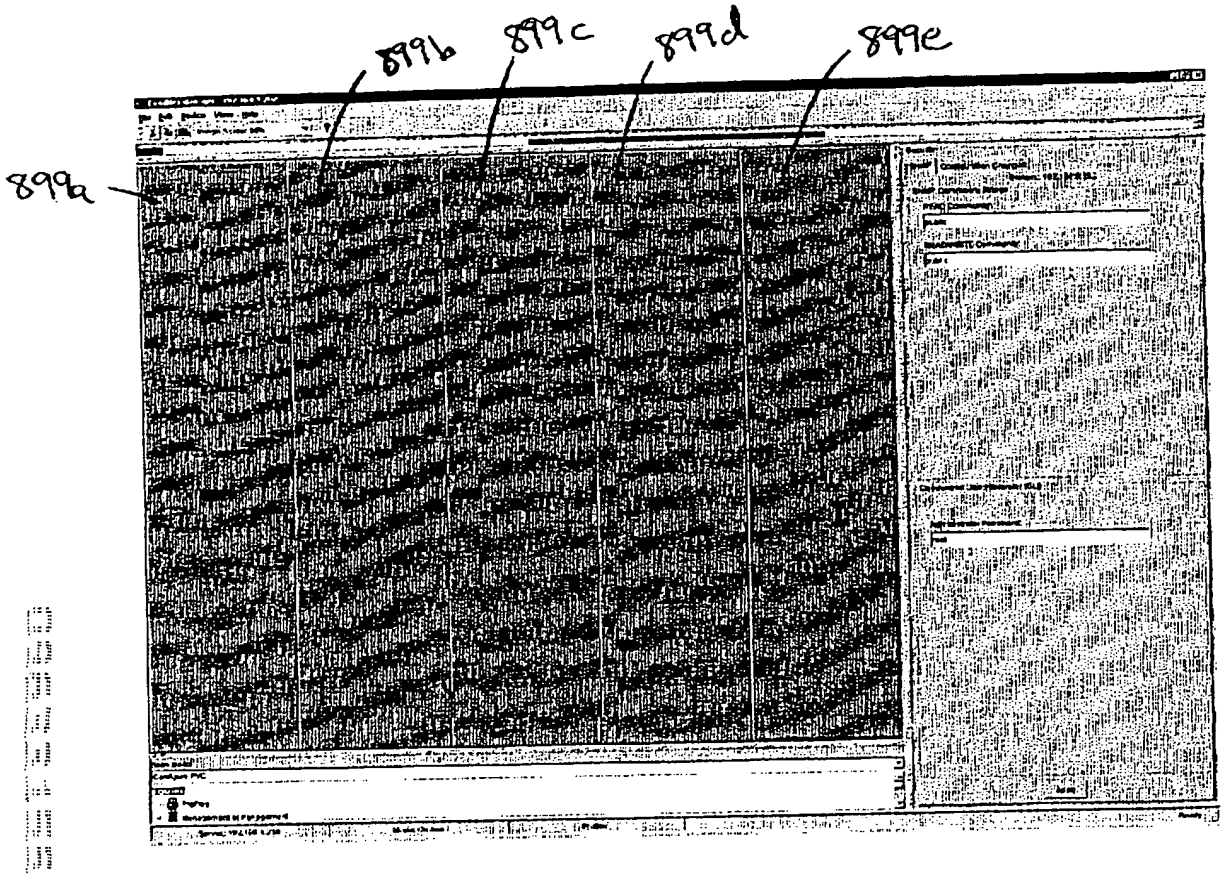


Fig. 4c

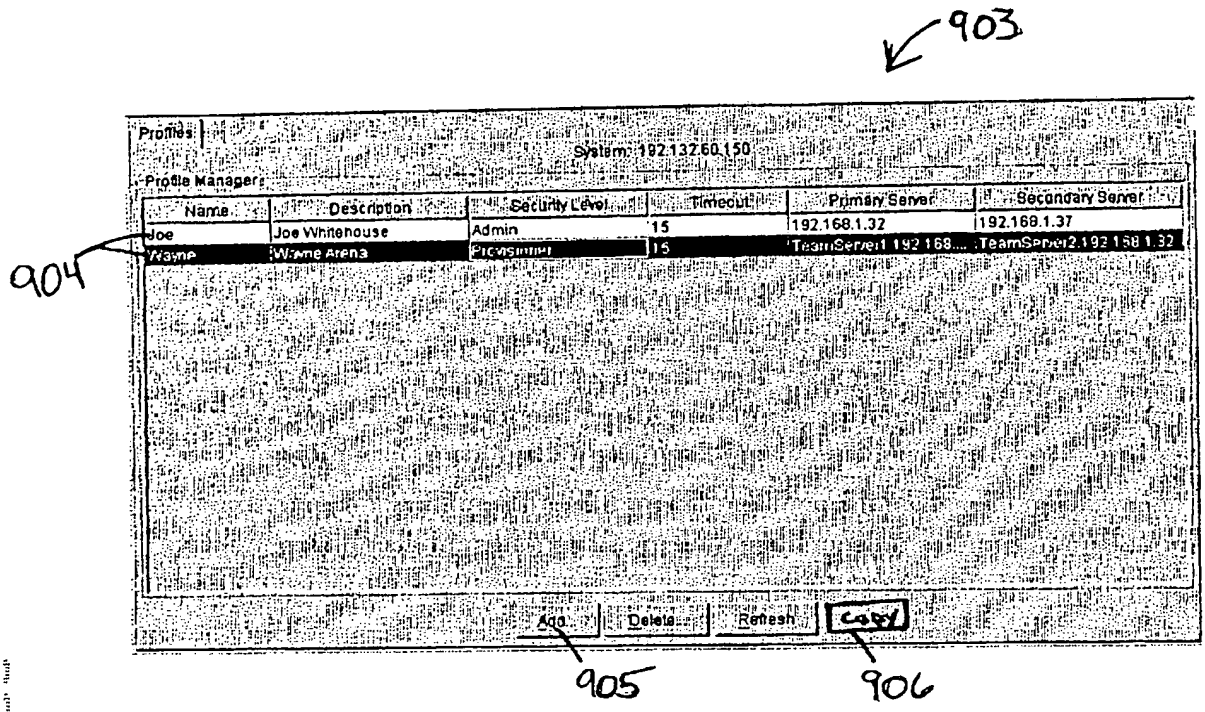

903
Profiles
System: 192.137.60.150
Profile Manager
Name
Description
Security Level
Timeout
Primary Server
Secondary Server
Joe
Joe Whitehouse
Admin
15
192.168.1.32
192.168.1.37
Wayne
Wayne Arena
Provisioner
15
TeamServer1 192.168....
TeamServer2 192.168.1.32
904
Add
Delete
Refresh
Copy
905
906

Fig. 4d

Fig. 4e

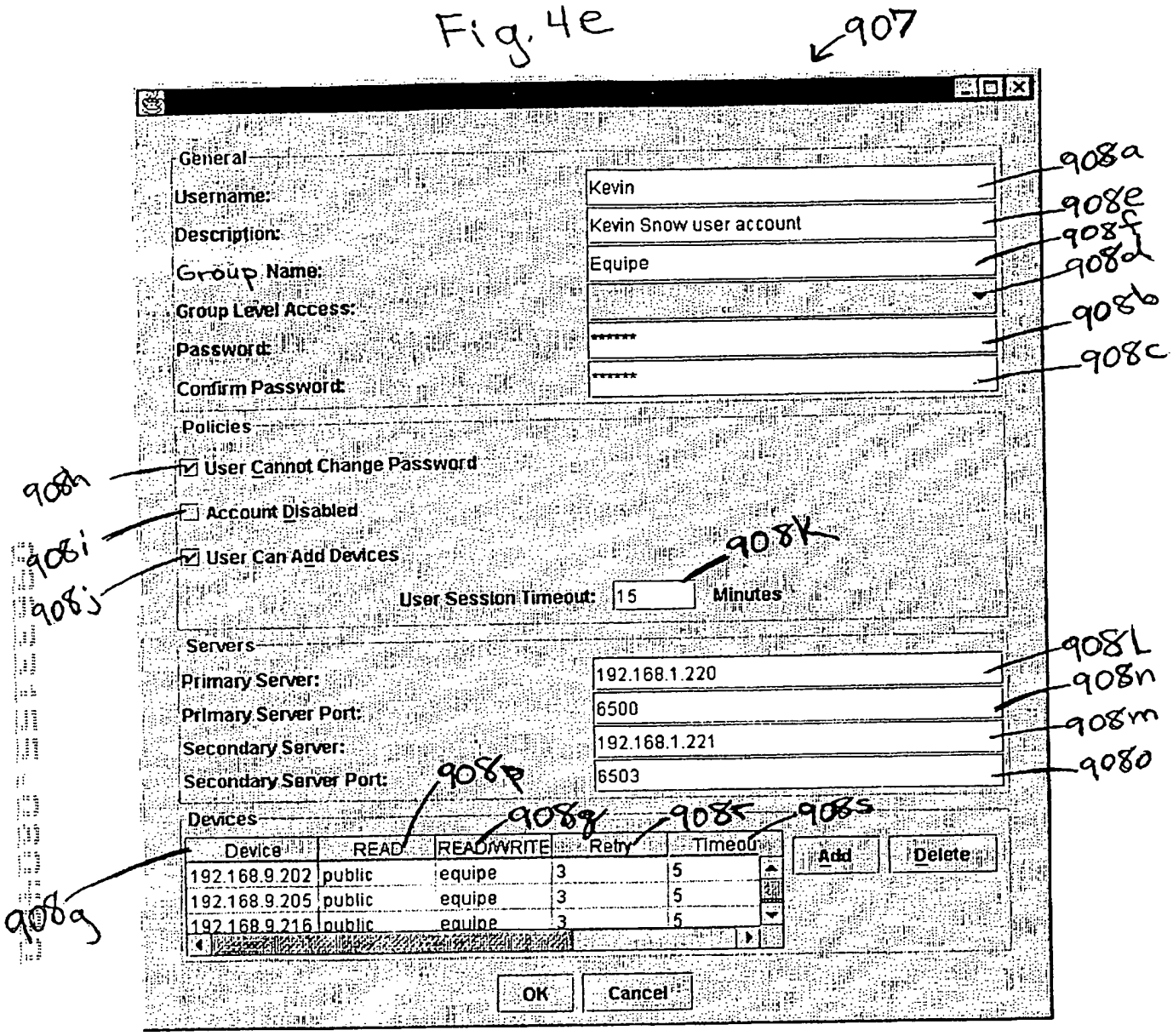
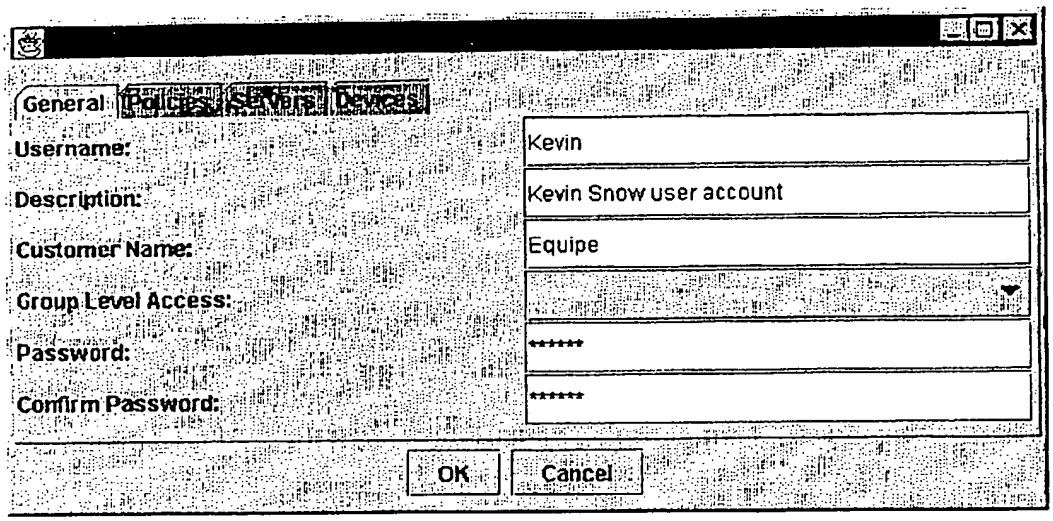

907
General
Username:
Kevin
908a
Description:
Kevin Snow user account
908e
Group Name:
Equipe
908f
Group Level Access:
908d
Password:
908b
Confirm Password:
908c
Policies
User Cannot Change Password
908h
Account Disabled
908i
User Can Add Devices
908j
User Session Timeout:
15
Minutes
908k
Servers
Primary Server:
192.168.1.220
908l
Primary Server Port:
6500
908n
Secondary Server:
192.168.1.221
908m
Secondary Server Port:
6503
908o
Devices
908p
908q
908r
908s
Device
READ
READ/WRITE
Retry
Timeou
Add
Delete
192.168.9.202
public
equipe
3
5
192.168.9.205
public
equipe
3
5
192.168.9.216
public
equipe
3
5
908g
OK
Cancel

Fig.4f


General
Policies
Servers
Devices
Username:
Kevin
Description:
Kevin Snow user account
Customer Name:
Equipe
Group Level Access:
Password:
******
Confirm Password:
******
OK
Cancel

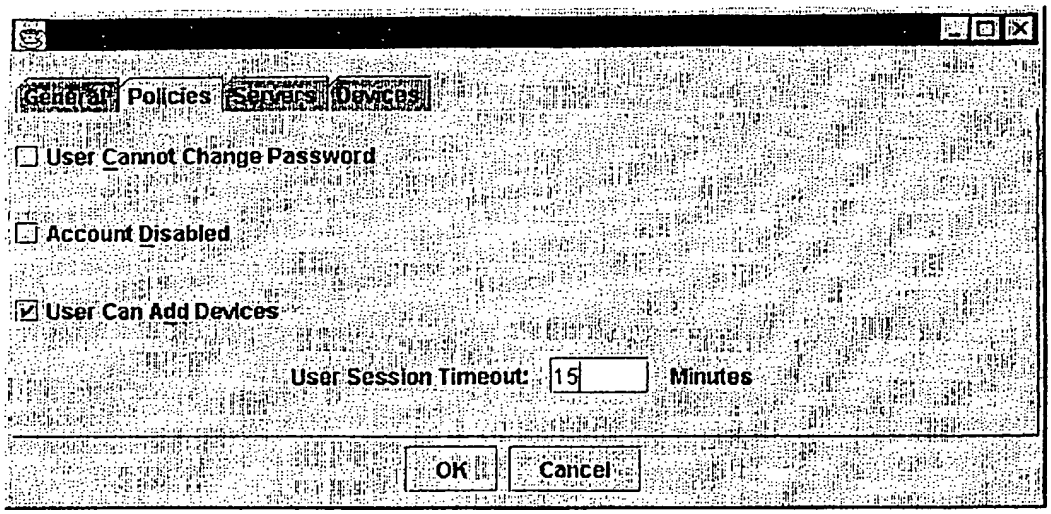
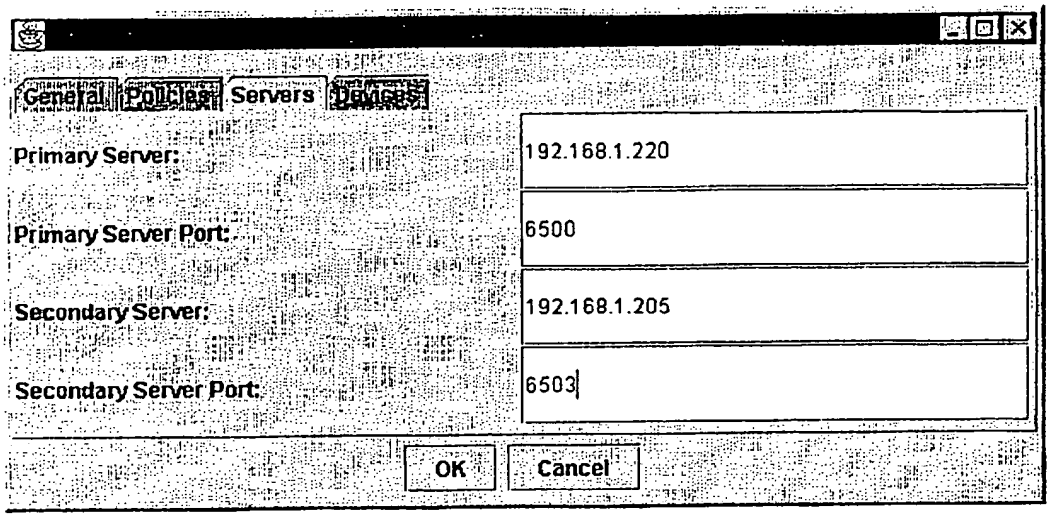

General
Policies
Servers
Devices
User Cannot Change Password
Account Disabled
User Can Add Devices
User Session Timeout:
15
Minutes
OK
Cancel

Fig.4g

Fig. 4h


General
Policies
Servers
Devices
Primary Server:
192.168.1.220
Primary Server Port:
6500
Secondary Server:
192.168.1.205
Secondary Server Port:
6503
OK
Cancel

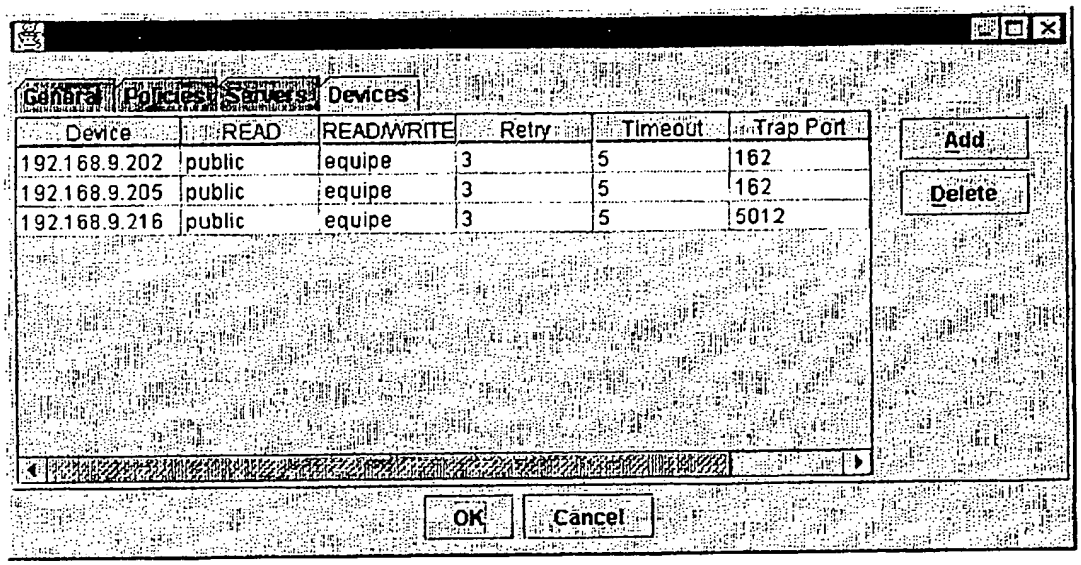

General
Policies
Servers
Devices
Device
READ
READ/WRITE
Retry
Timeout
Trap Port
192.168.9.202
public
equipe
3
5
162
192.168.9.205
public
equipe
3
5
162
192.168.9.216
public
equipe
3
5
5012
Add
Delete
OK
Cancel

Fig. 4i

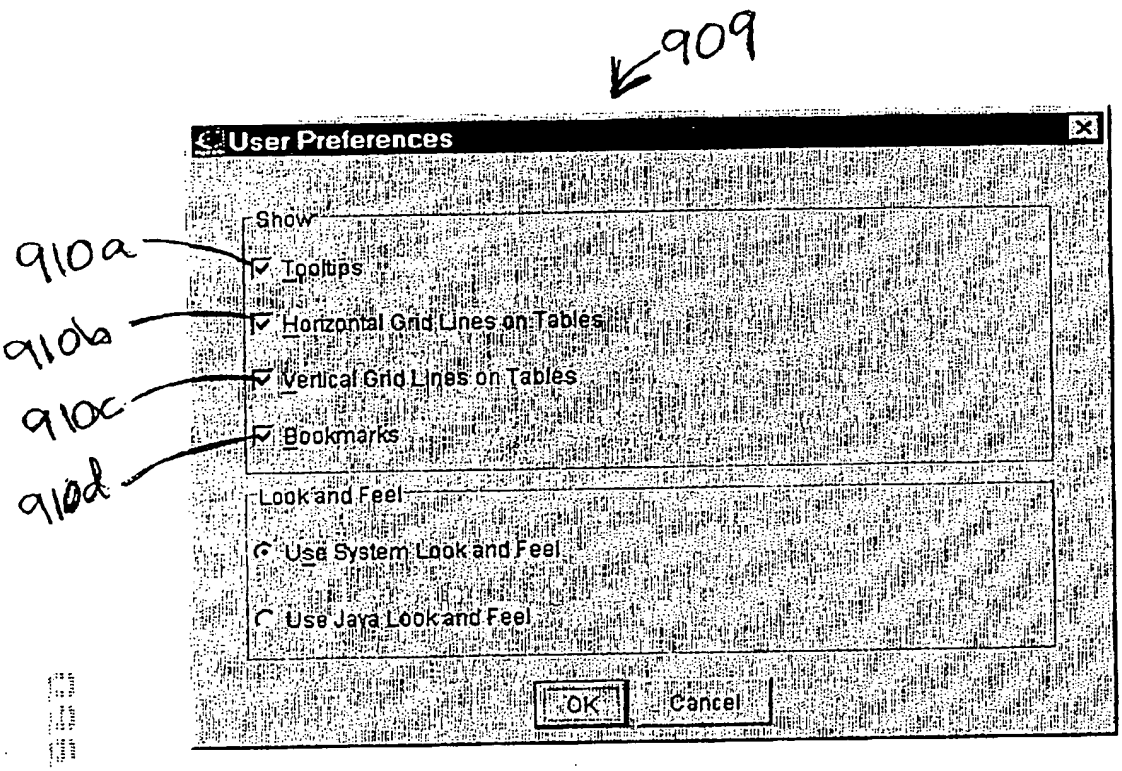
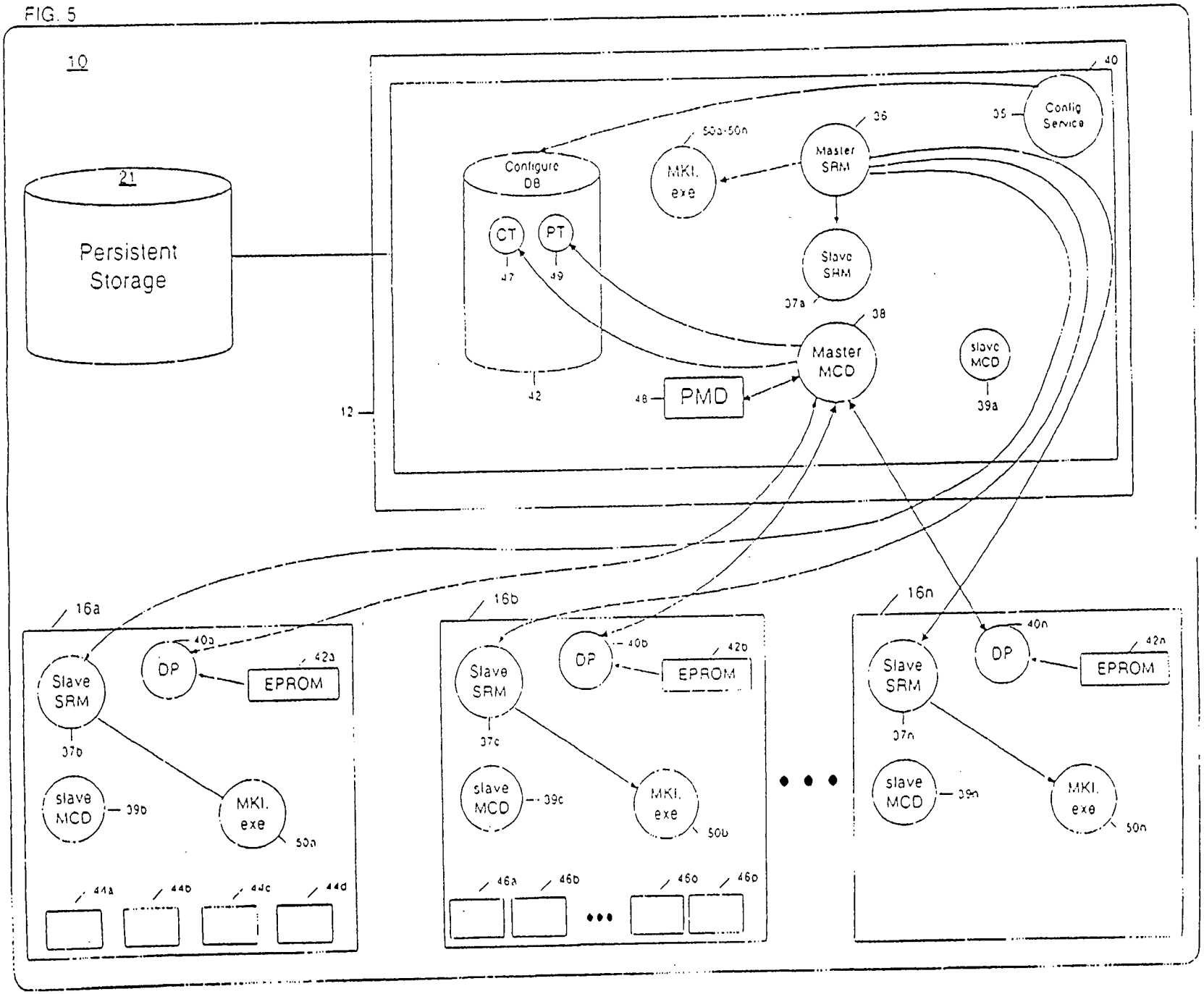

909
User Preferences
Show
Tooltips
Horizontal Grid Lines on Tables
Vertical Grid Lines on Tables
Bookmarks
910a
910b
910c
910d
Look and Feel
Use System Look and Feel
Use Java Look and Feel
OK
Cancel

Fig. 4j

FIG. 5


10
21
Persistent Storage
12
40
Config Service
35
Master SRM
36
MKI. exe
50a-50n
Configure DB
CT
PT
47
49
42
Slave SRM
37a
Master MCD
38
slave MCD
39a
PMD
48
16a
16b
16n
DP
40a
EPROM
42a
Slave SRM
37b
slave MCD
39b
MKI. exe
50n
40b
42b
37c
39c
50b
40n
42n
37n
39n
50n
44a
44b
44c
44d
46a
46b
46c
46d

CARD TABLE

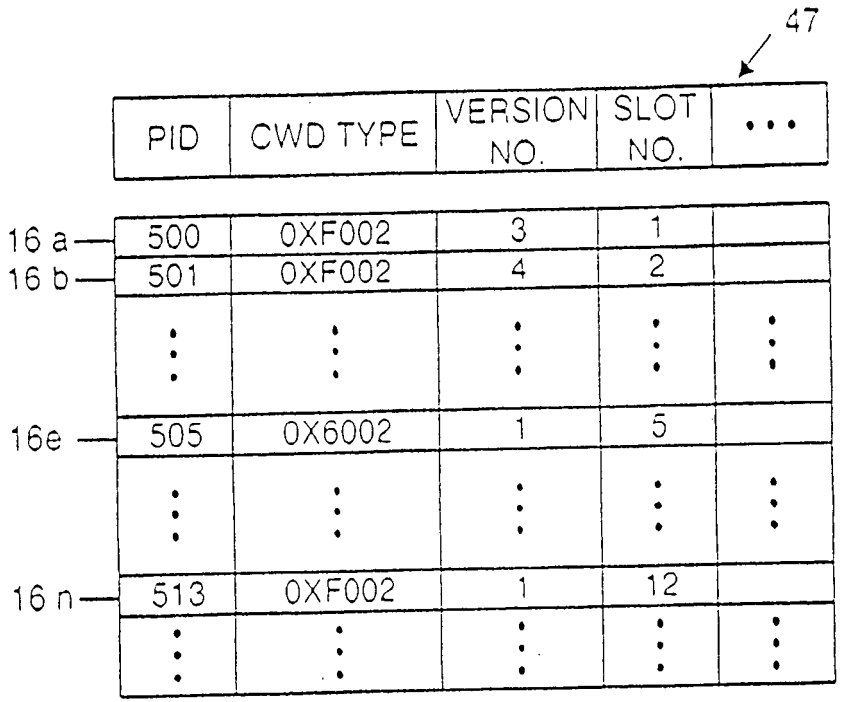


FIG. 6

PORT TABLE

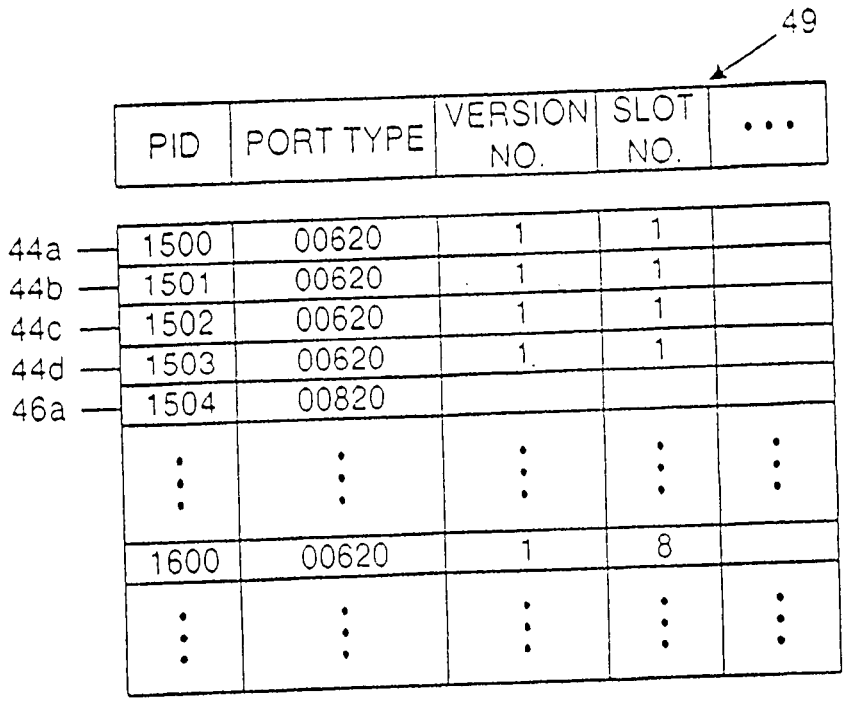


FIG. 7

FIG. 8

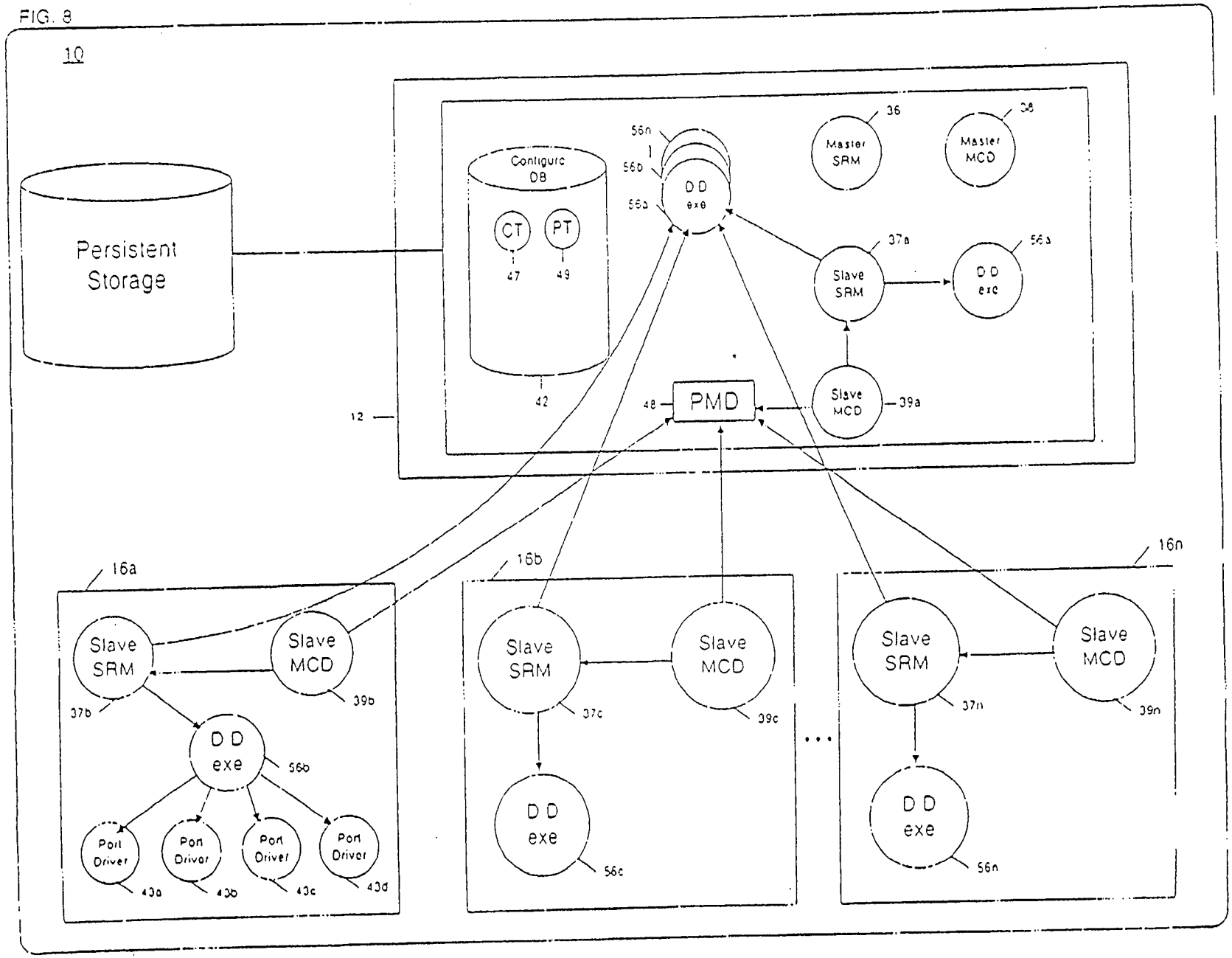


FIG. 9

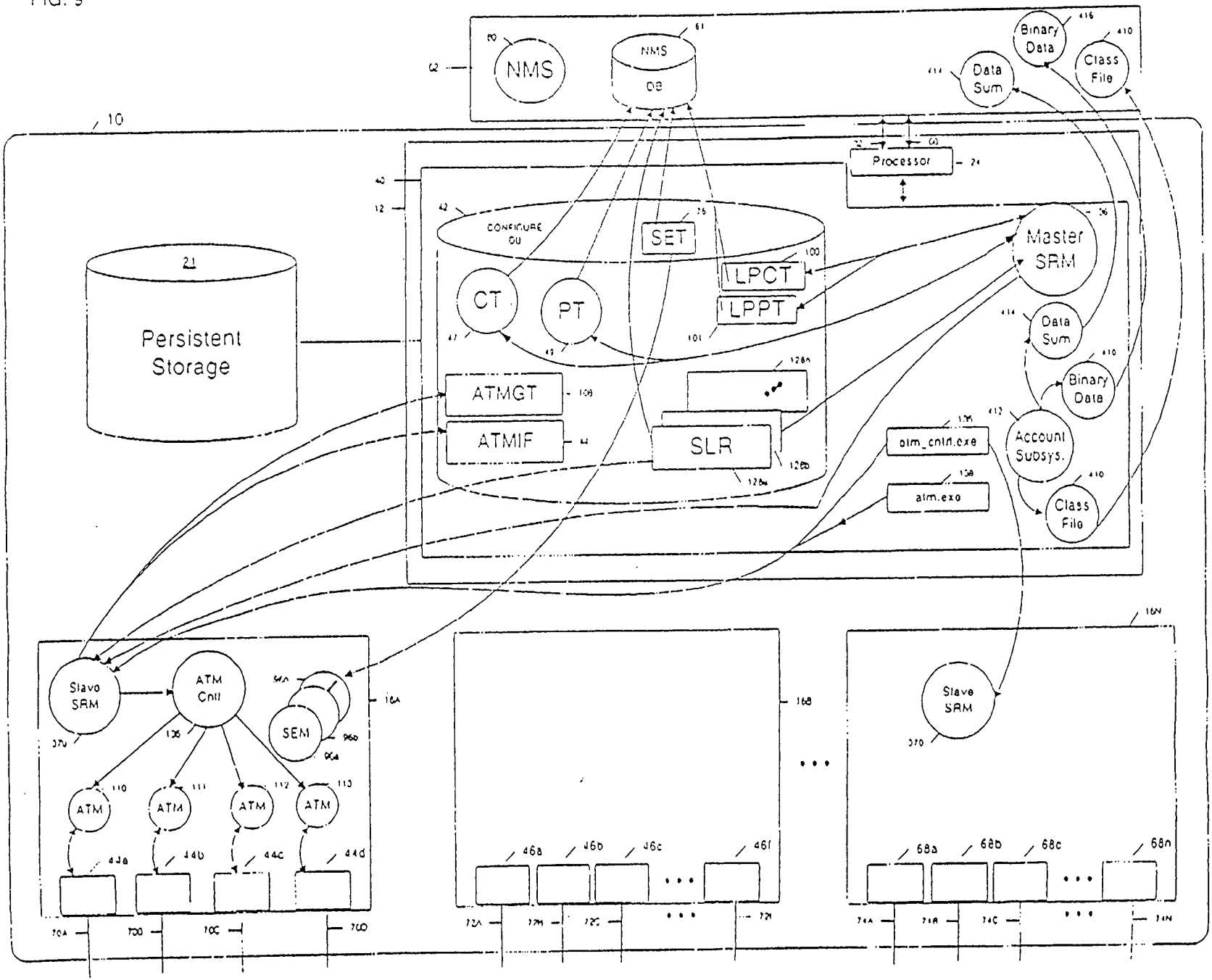

NMS
NMS DB
Binary Data
Class File
Data Sum
10
Processor
Persistent Storage
CONFIGURE DU
SET
LPCT
LPPT
CT
PT
Master SRM
Data Sum
Binary Data
ATMGT
ATMIF
SLR
aim_cnfr.exe
aim.exe
Account Subsys.
Class File
Slave SRM
ATM Cntl
SEM
ATM
ATM
ATM
ATM
Slave SRM

FIG. 10

Service Endpoint Table 76

	Service Endpoint #	Port PID
78	1	1500
80	2	1501
82	3	1501
84	4	1501
86	5	1502
88	6	1502
90	7	1503
92	8	1503
94	9	1503
168	10	1502
	⋮	⋮

FIG. 11a

Logical to Physical Card Table 100

	98 LID	102 Primary PID	104 Back-up PID
106	30	500	513
109	31	501	513
	⋮	⋮	⋮

FIG. 11b

Logical to Physical Port Table 101

	98 LID	102 Primary PID	104 Back-up PID
107	40	1500	1600
	⋮	⋮	⋮

FIG. 12

ATM Group Table 108

Group #	Card LID	...
1	30	
2	30	
3	30	
4	30	

FIG. 13

ATM Interface Table 114

ATM IF	ATM Group	SE	...
1	1	1	
2	1	1	
3	1	1	
4	2	2	
5	2	3	
6	2	4	
⋮	⋮	⋮	⋮
12	3	10	
⋮	⋮	⋮	⋮

170

FIG. 14

Software Load Record 128a

Control Shim	LID
atm-cntl.exe	30

130 134 132

FIG. 15

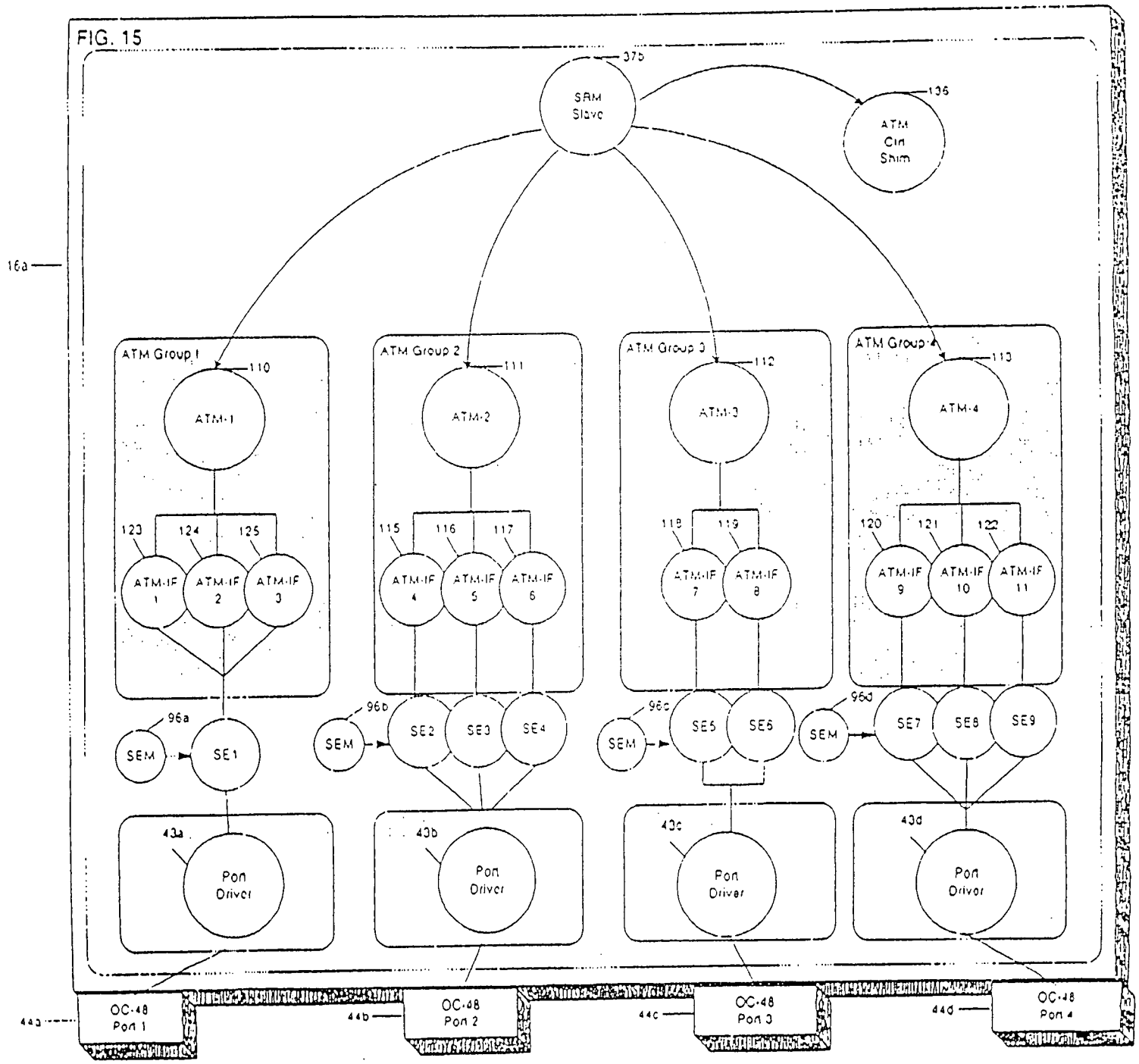

16a
37b
SRM Slave
135
ATM Cln Shim
ATM Group 1
110
ATM-1
123
124
125
ATM-IF 1
ATM-IF 2
ATM-IF 3
96a
SEM
SE1
43a
Pon Driver
ATM Group 2
111
ATM-2
115
116
117
ATM-IF 4
ATM-IF 5
ATM-IF 6
96b
SEM
SE2
SE3
SE4
43b
Pon Driver
ATM Group 3
112
ATM-3
118
119
ATM-IF 7
ATM-IF 8
96c
SEM
SE5
SE6
43c
Pon Driver
ATM Group 4
113
ATM-4
120
121
122
ATM-IF 9
ATM-IF 10
ATM-IF 11
96d
SEM
SE7
SE8
SE9
43d
Pon Driver
44a
OC-48 Pon 1
44b
OC-48 Pon 2
44c
OC-48 Pon 3
44d
OC-48 Pon 4

FIG. 16a

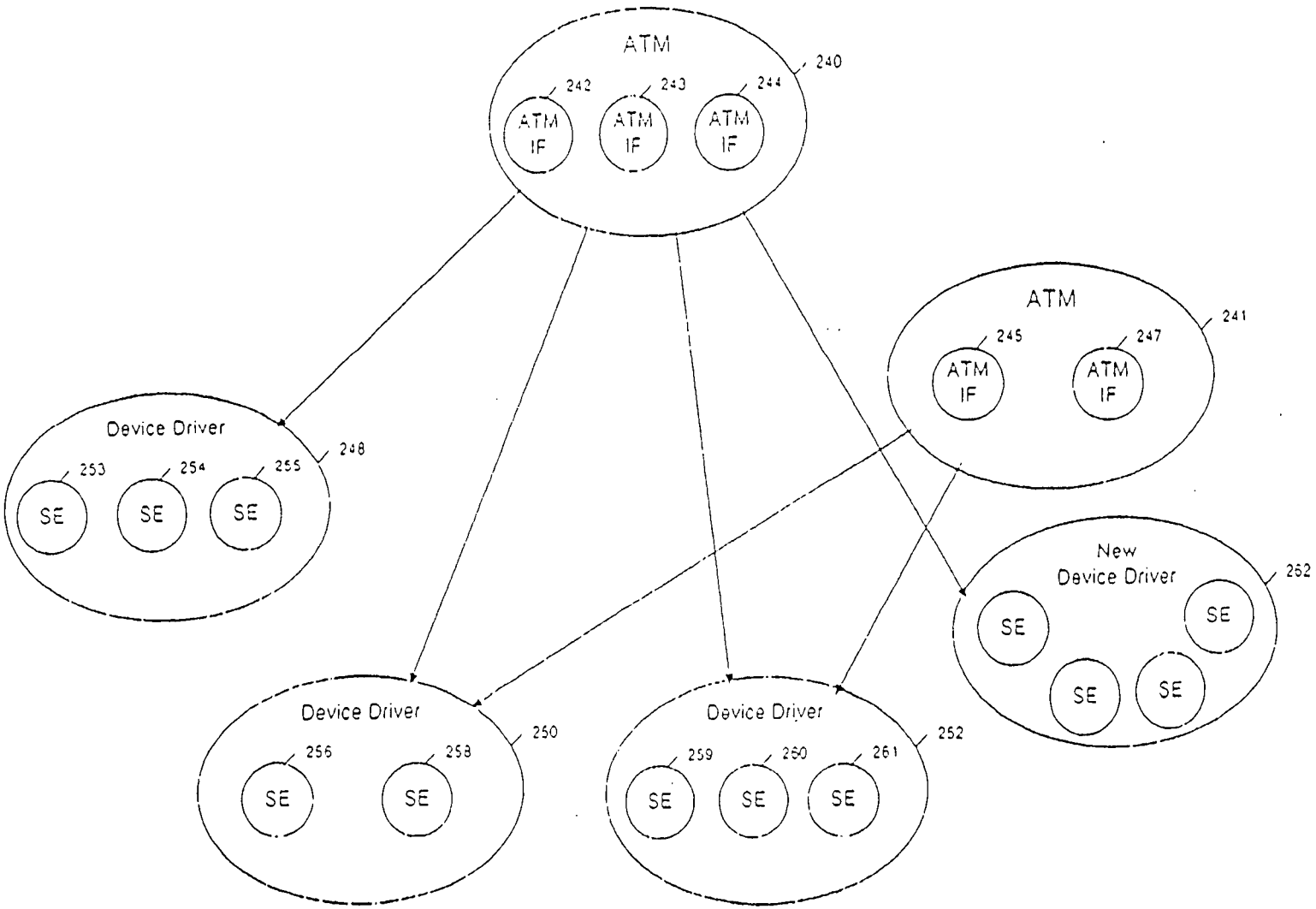

ATM
240
242
243
244
ATM IF
ATM IF
ATM IF
ATM
241
245
247
ATM IF
ATM IF
Device Driver
248
253
254
255
SE
SE
SE
New Device Driver
262
SE
SE
SE
SE
Device Driver
250
256
258
SE
SE
Device Driver
252
259
260
261
SE
SE
SE

FIG. 16b

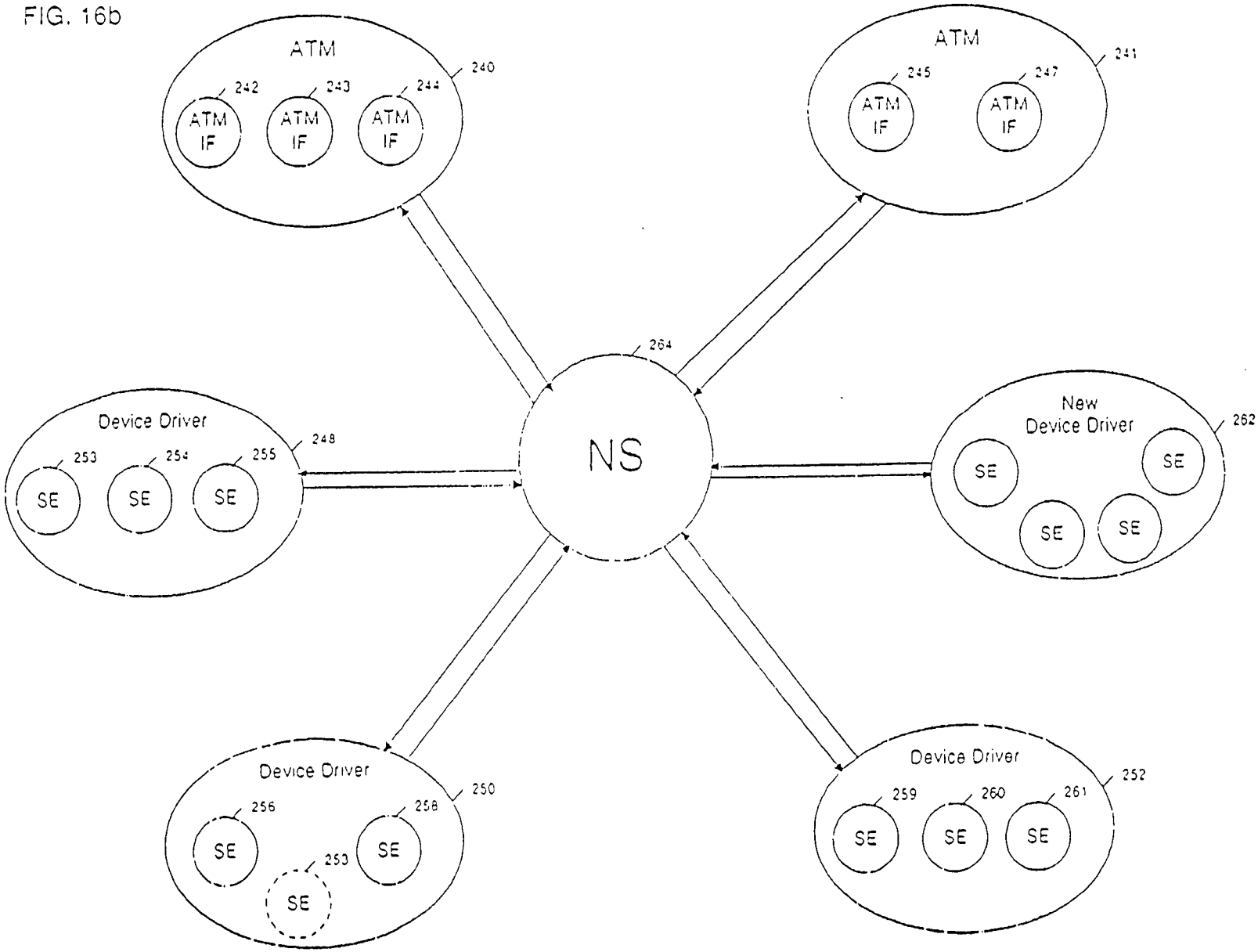

ATM
240
242
243
244
ATM IF
ATM IF
ATM IF
ATM
241
245
247
ATM IF
ATM IF
264
NS
Device Driver
248
253
254
255
SE
SE
SE
New Device Driver
262
SE
SE
SE
SE
Device Driver
250
256
258
253
SE
SE
SE
Device Driver
252
259
260
261
SE
SE
SE

FIG. 16c

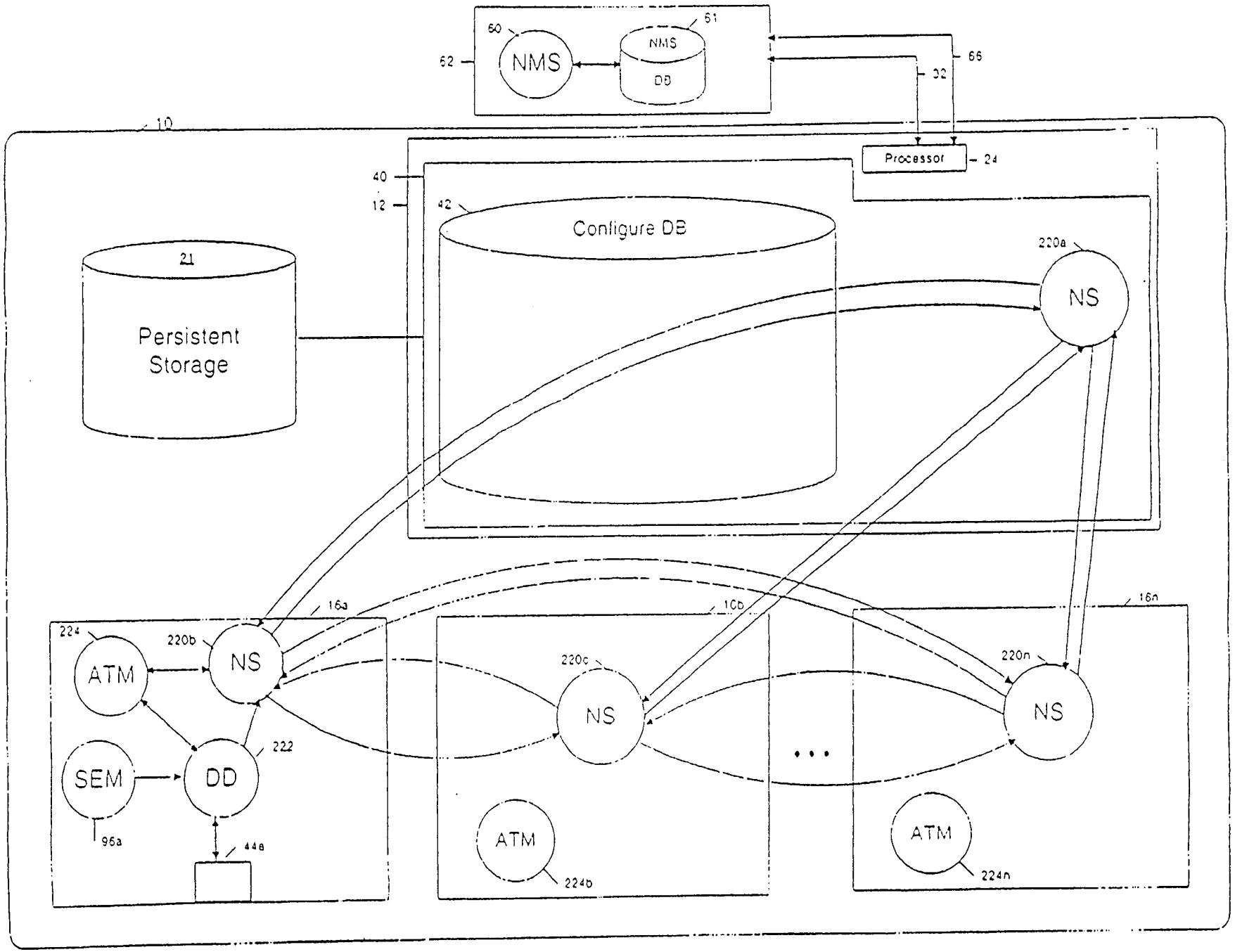

60
NMS
61
NMS
DB
62
66
32
10
Processor
24
40
12
42
Configure DB
21
Persistent
Storage
220a
NS
16a
16b
16n
224
220b
ATM
NS
220c
NS
220n
NS
222
SEM
DD
96a
44a
ATM
224b
ATM
224n

FIG. 16d

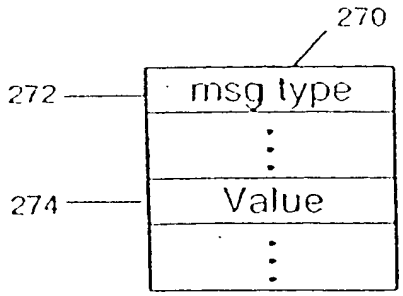


FIG. 17

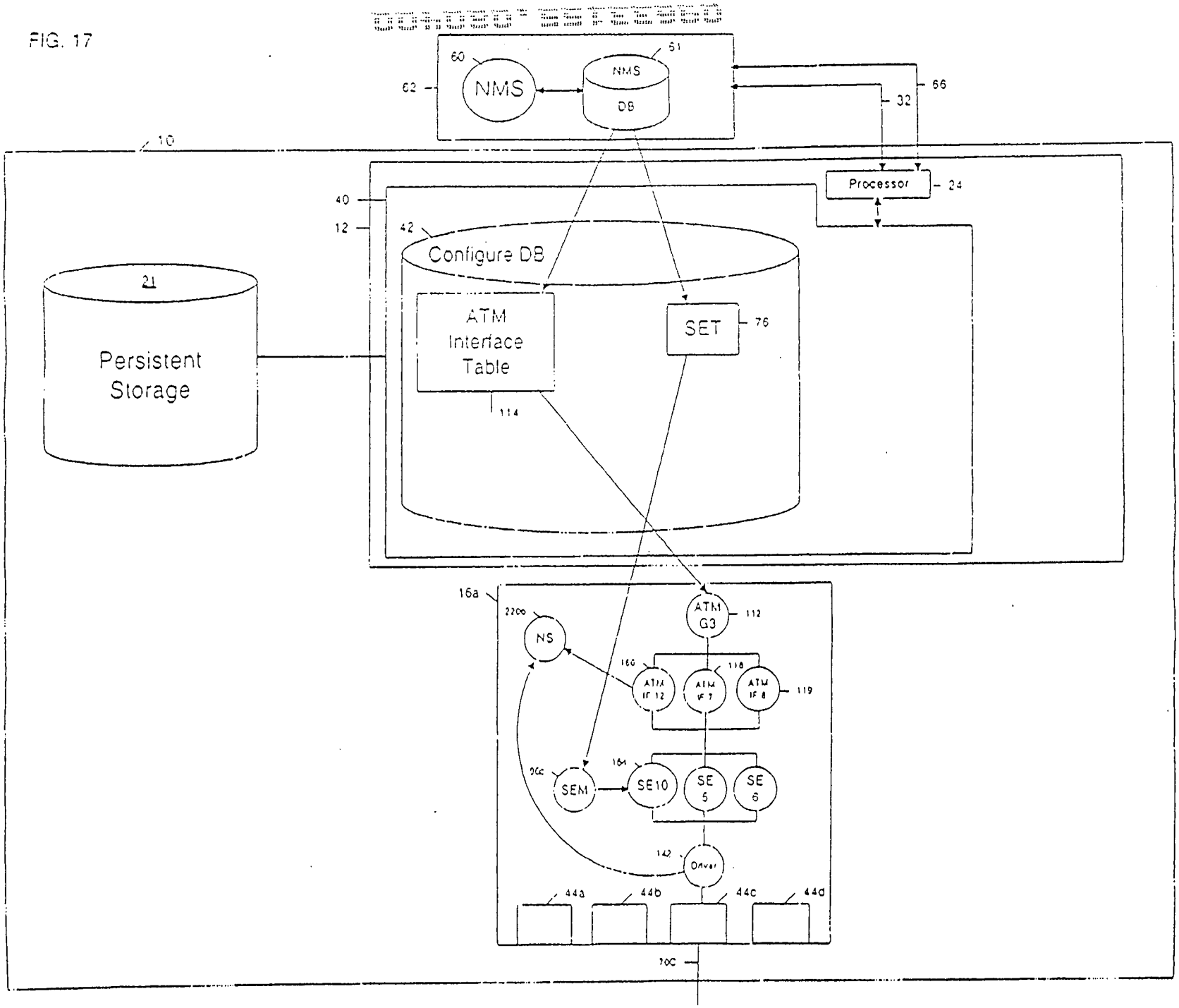

60
NMS
62
51
NMS
DB
66
32
10
Processor
24
40
12
42
Configure DB
21
Persistent Storage
ATM Interface Table
114
SET
76
16a
2200
NS
ATM G3
112
160
118
119
SEM
164
SE10
SE 5
SE 5
142
Driver
44a
44b
44c
44d
70C

FIG. 18

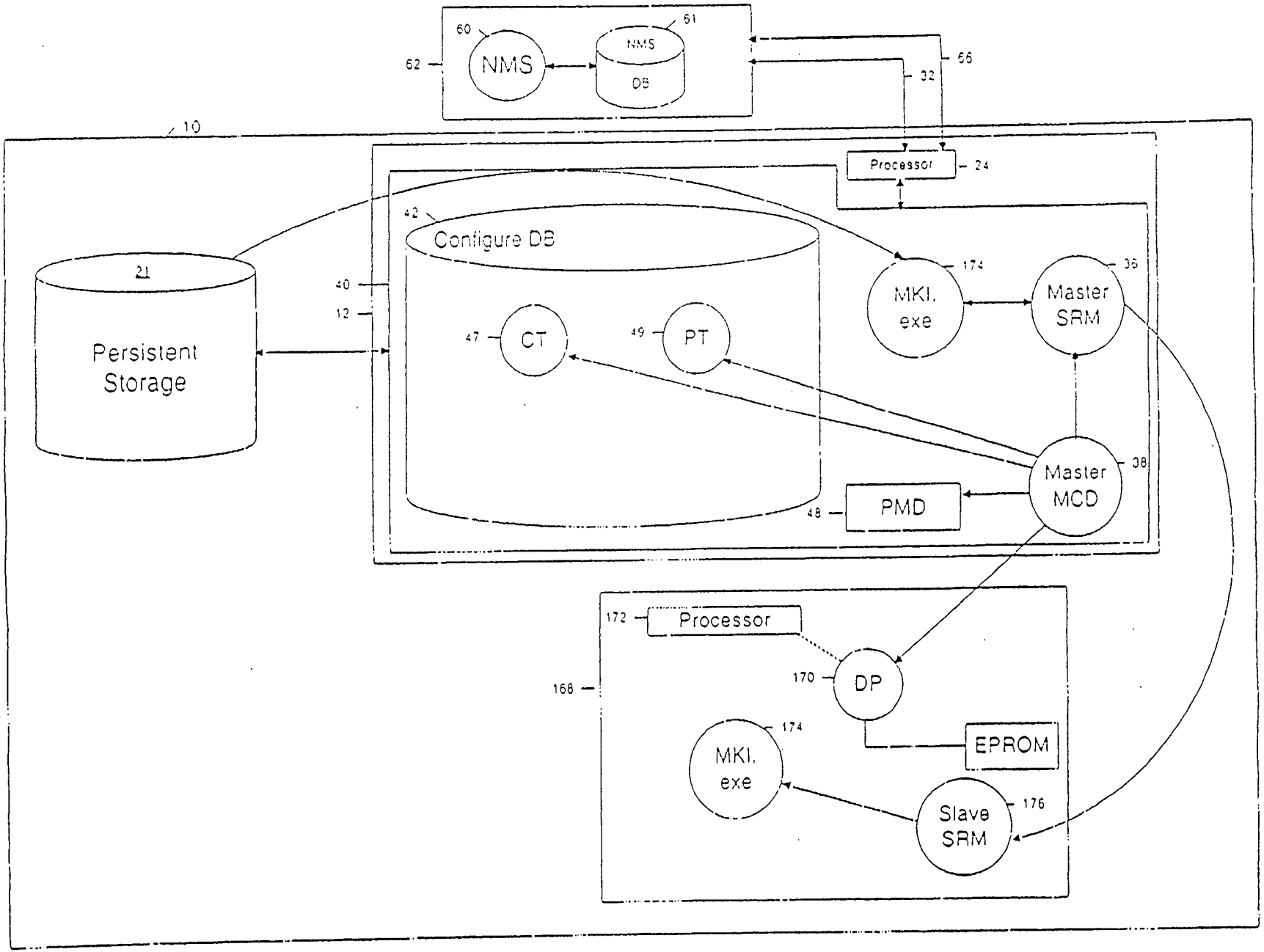

60
NMS
51
NMS
DB
62
66
32
10
Processor
24
42
Configure DB
21
Persistent
Storage
40
12
47
CT
49
PT
174
MKI.
exe
36
Master
SRM
38
Master
MCD
48
PMD
172
Processor
170
DP
168
174
MKI.
exe
EPROM
Slave
SRM
176

FIG. 19

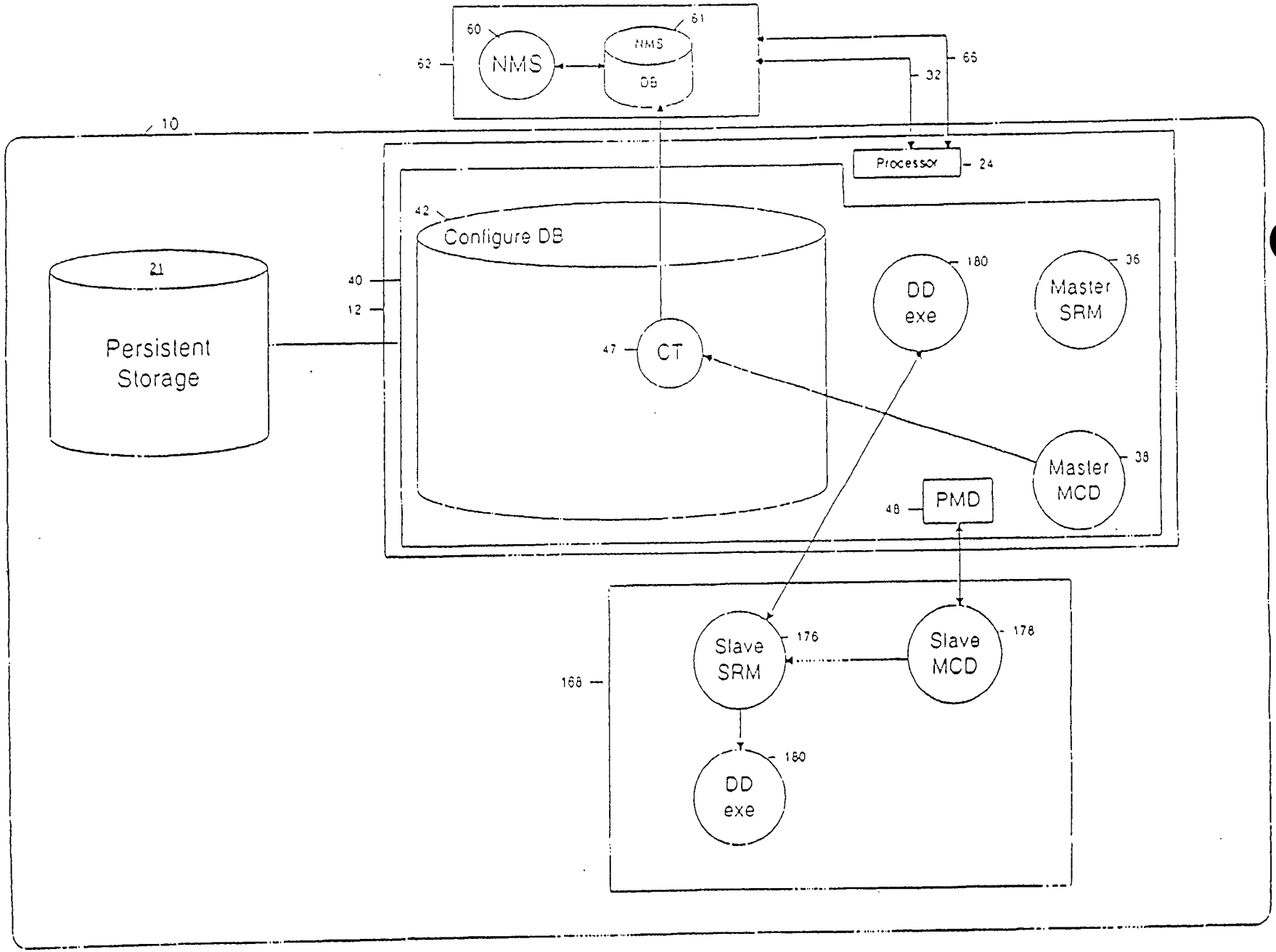

60
NMS
61
NMS
DB
62
65
32
10
Processor
24
42
Configure DB
21
Persistent Storage
40
12
47
CT
180
DD exe
36
Master SRM
38
Master MCD
48
PMD
168
Slave SRM
176
Slave MCD
178
180
DD exe

FIG. 20

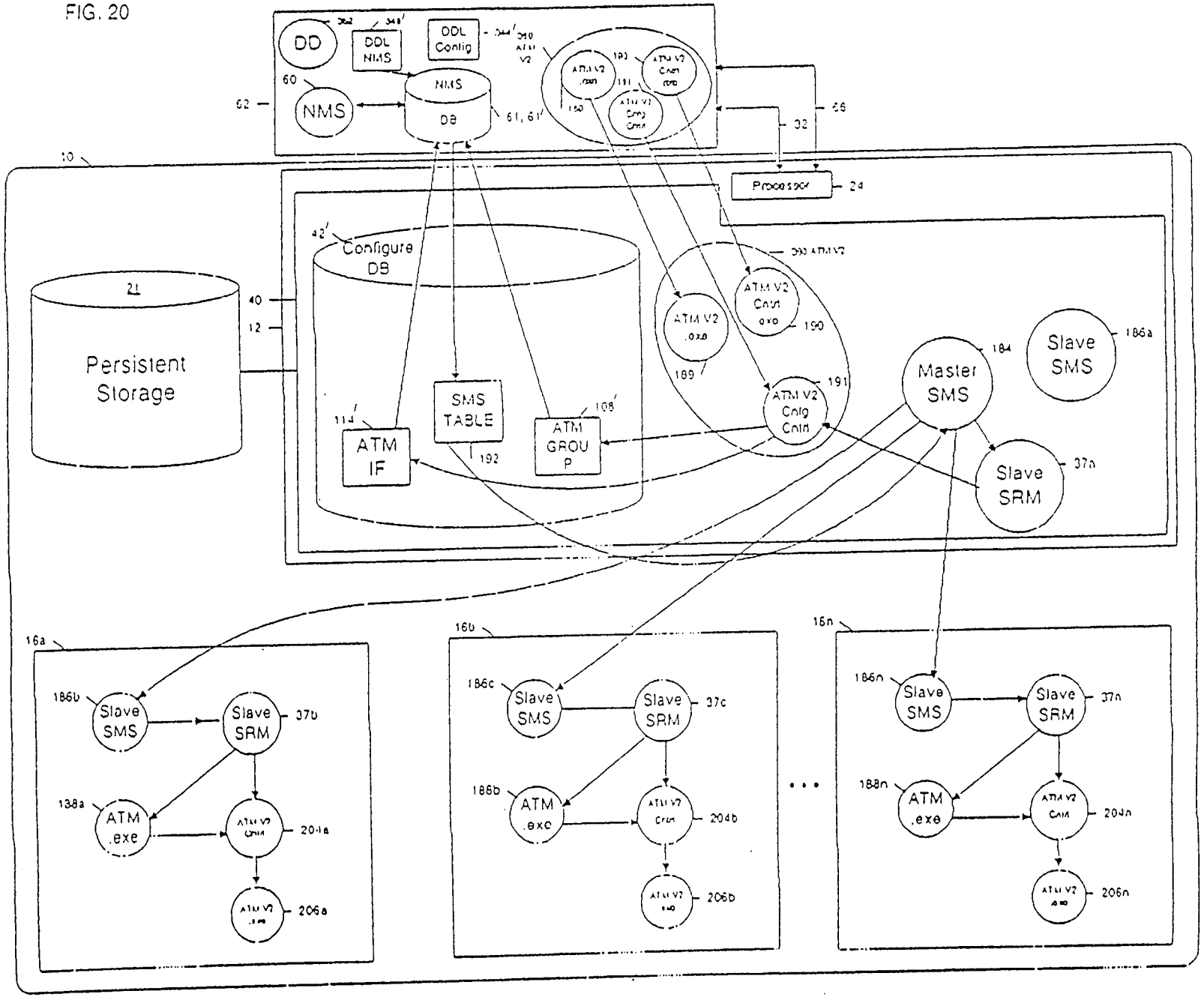

DD
DDL NMS
DDL Config
NMS
NMS
DB
60
62
61, 61'
32
66
10
Processor
24
Configure DB
42'
Persistent Storage
21
40
12
ATM V2 .exe
ATM V2 Config exe
190
189
191
ATM V2 Config Com
SMS TABLE
192
ATM GROU P
108'
ATM IF
114'
Master SMS
184
Slave SMS
186a
Slave SRM
37a
16a
186b
Slave SMS
Slave SRM
37b
138a
ATM .exe
204a
206a
16b
186c
Slave SMS
Slave SRM
37c
186b
ATM .exe
204b
206b
16n
186n
Slave SMS
Slave SRM
37n
188n
ATM .exe
204n
206n

FIG. 21

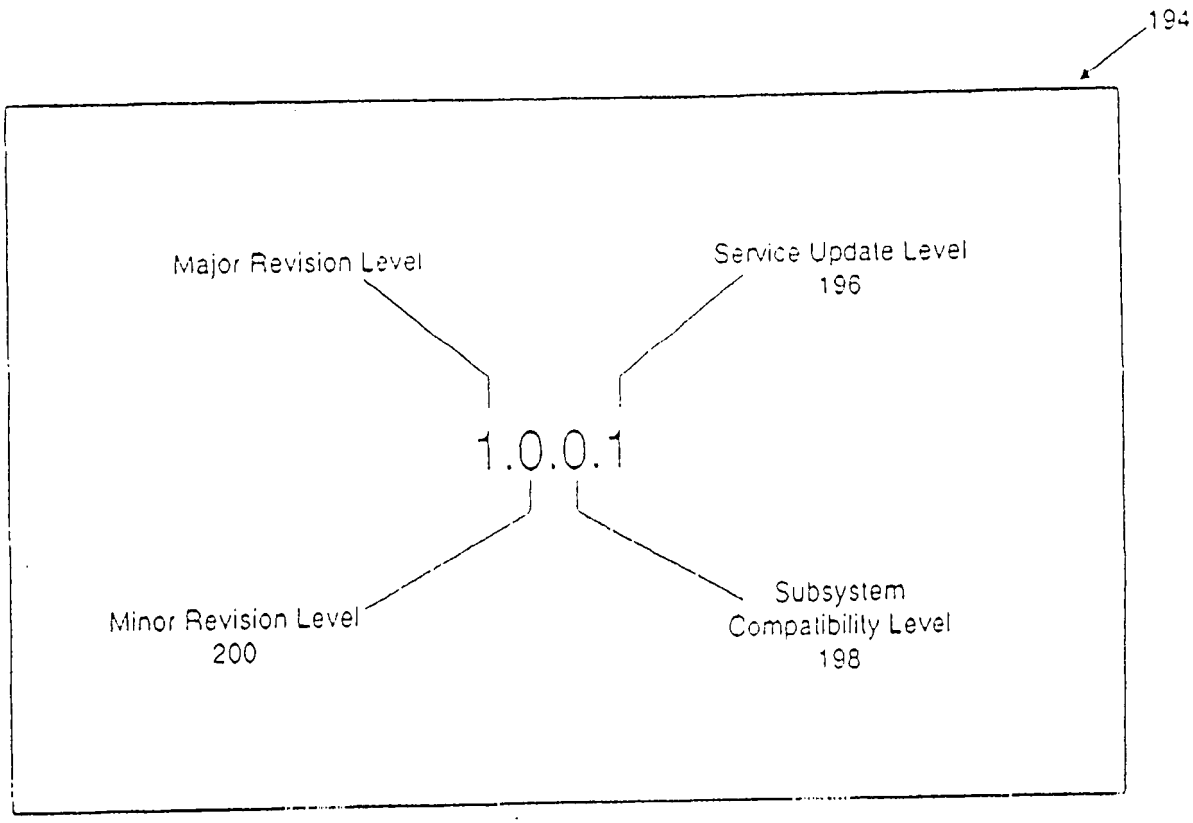

194
Major Revision Level
Service Update Level
196
1.0.0.1
Minor Revision Level
200
Subsystem
Compatibility Level
198

FIG. 22

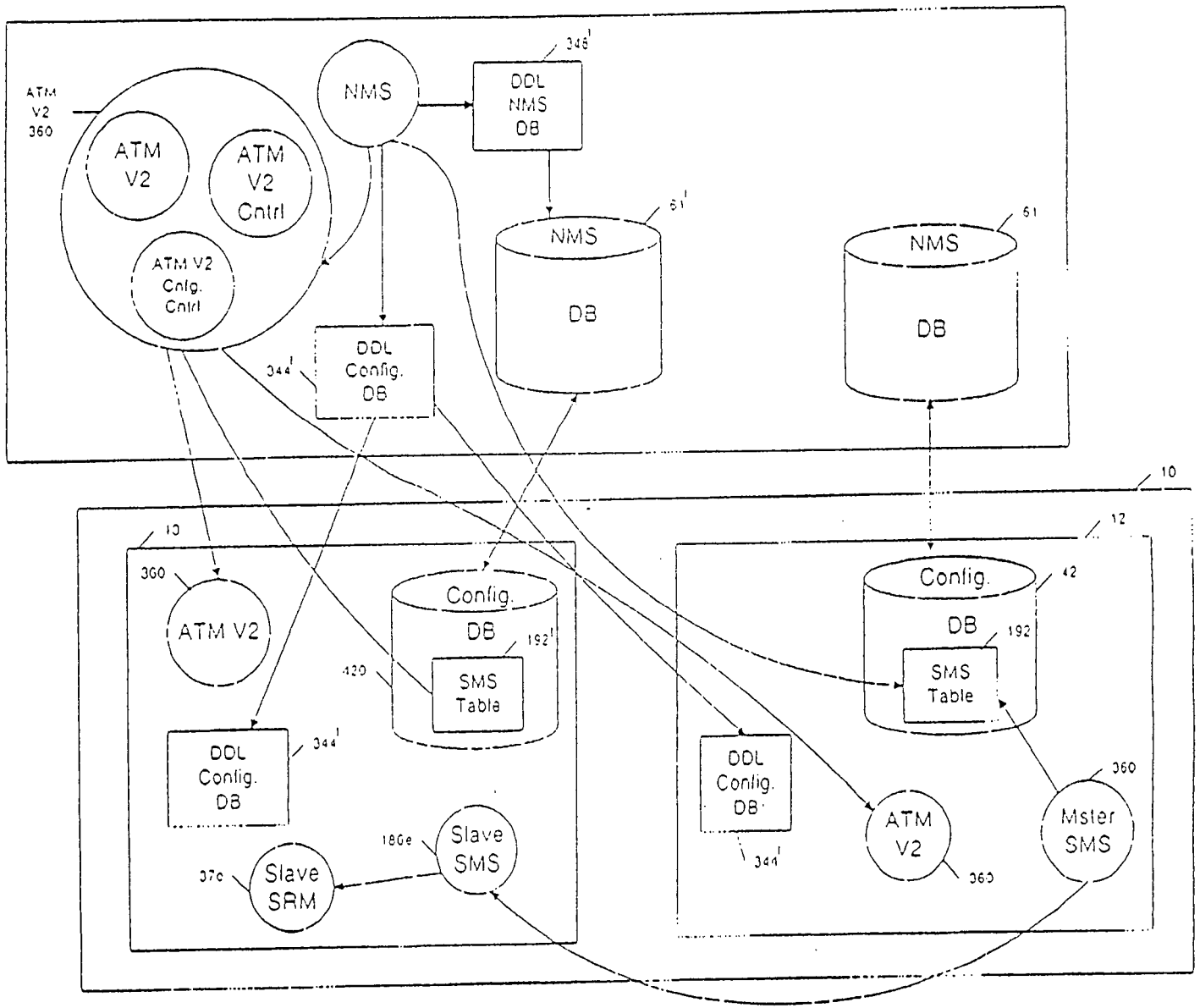

ATM V2 360
ATM V2
ATM V2 Cntrl
ATM V2 Config. Cntrl
NMS
DDL NMS DB
348'
NMS
DB
61'
NMS
DB
61
DDL Config. DB
344'
10
13
360
ATM V2
Config.
DB
192'
420
SMS Table
DDL Config. DB
344'
Slave SMS
186e
Slave SRM
37c
12
Config.
DB
42
192
SMS Table
DDL Config. DB
344'
ATM V2
360
Mster SMS
360

FIG. 23

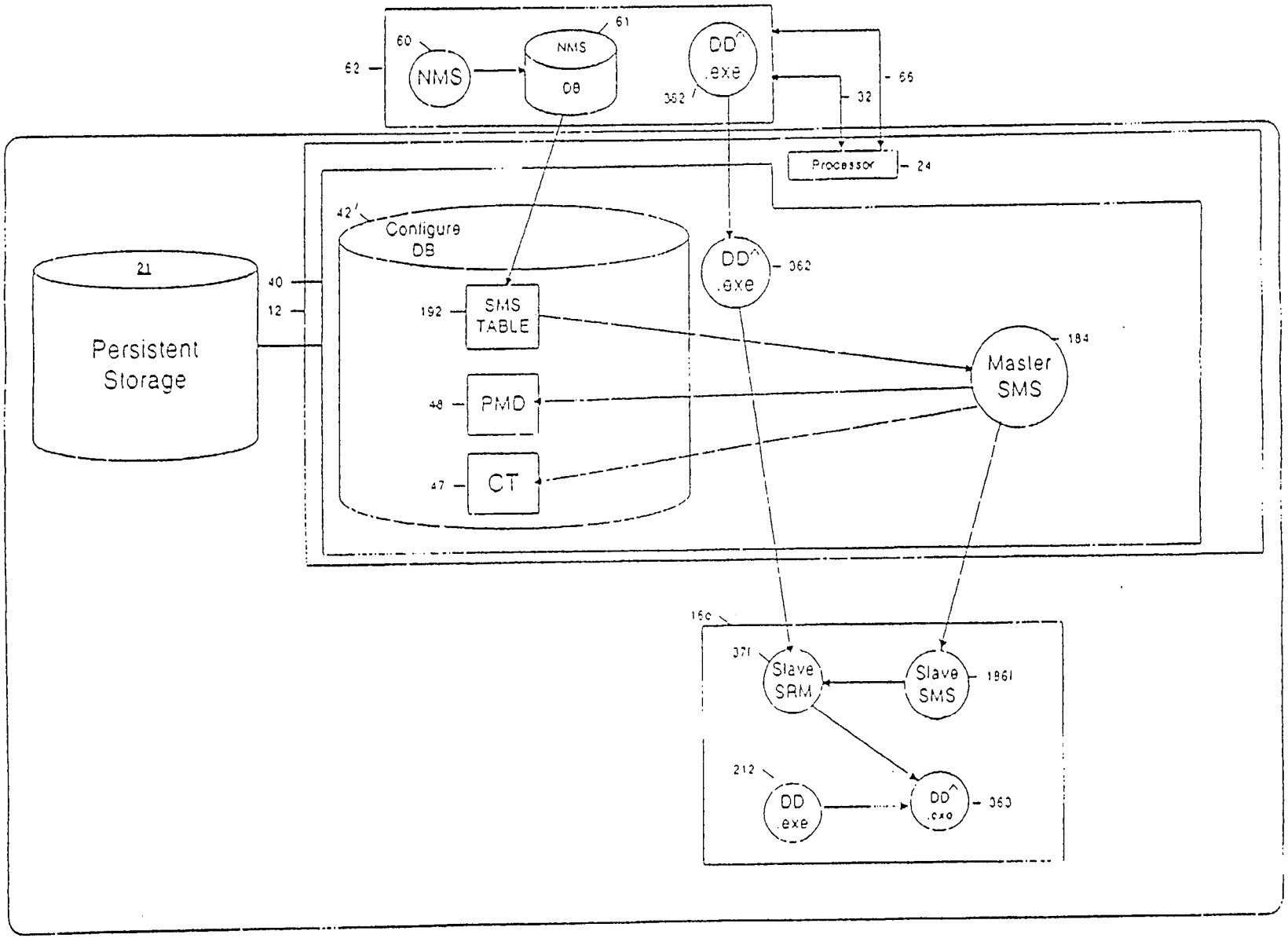

60
NMS
62
NMS
DB
61
DD
.exe
362
66
32
Processor
24
42'
Configure
DB
21
Persistent
Storage
40
12
192
SMS
TABLE
48
PMD
47
CT
DD
.exe
362
184
Master
SMS
16c
371
Slave
SRM
Slave
SMS
196'
212
DD
.exe
DD
.exe
360

FIG. 24

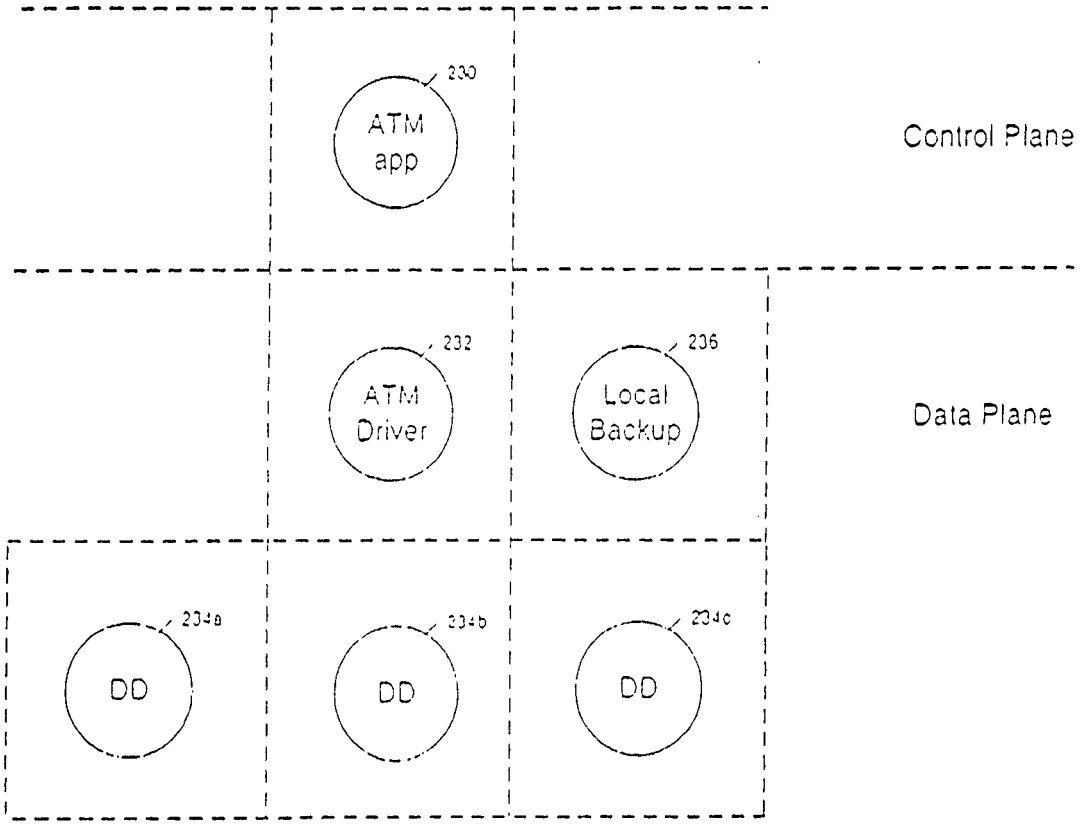

230
ATM app
Control Plane
232
ATM Driver
236
Local Backup
Data Plane
234a
DD
234b
DD
234c
DD

FIG. 25

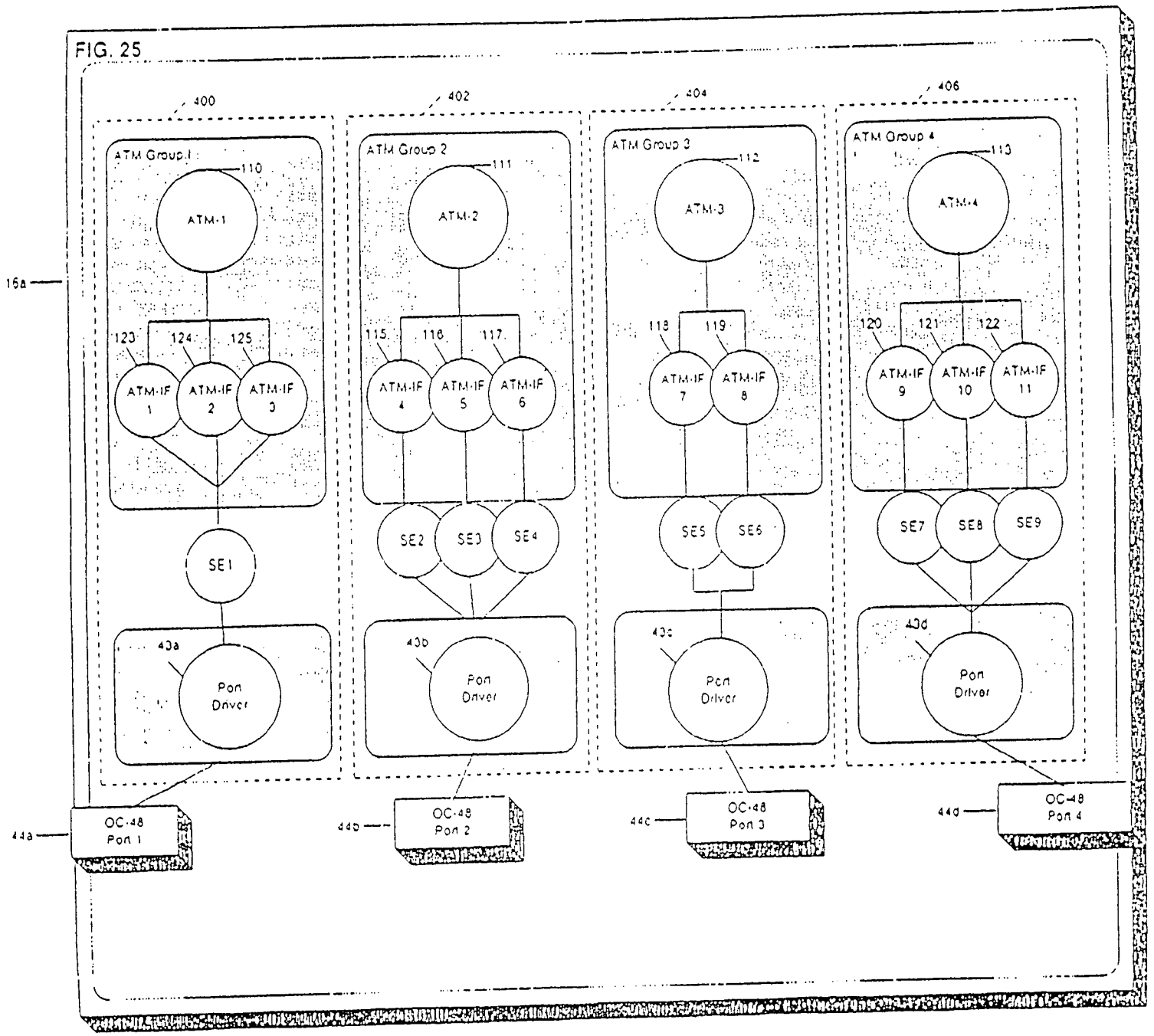

400
402
404
406
ATM Group 1
ATM Group 2
ATM Group 3
ATM Group 4
110
111
112
113
ATM-1
ATM-2
ATM-3
ATM-4
123
124
125
115
116
117
118
119
120
121
122
ATM-IF 1
ATM-IF 2
ATM-IF 3
ATM-IF 4
ATM-IF 5
ATM-IF 6
ATM-IF 7
ATM-IF 8
ATM-IF 9
ATM-IF 10
ATM-IF 11
SE1
SE2
SE3
SE4
SE5
SE6
SE7
SE8
SE9
43a
43b
43c
43d
Pon Driver
Pon Driver
Pon Driver
Pon Driver
16a
44a
OC-48 Pon 1
44b
OC-48 Pon 2
44c
OC-48 Pon 3
44d
OC-48 Pon 4

FIG. 26

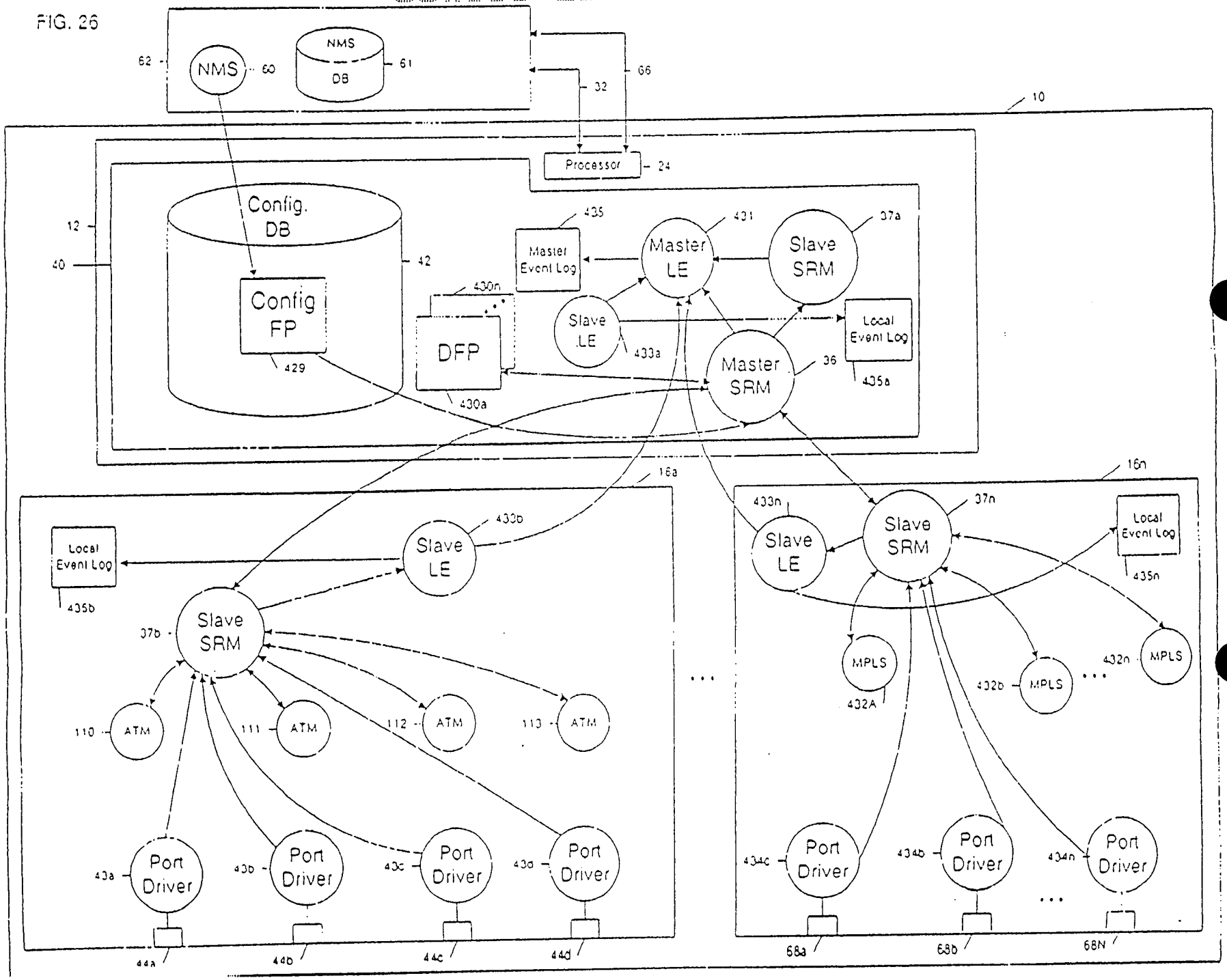

NMS
DB
62
60
61
66
32
10
Processor
24
Config. DB
12
40
42
Config FP
429
DFP
430n
430a
Master Event Log
435
Master LE
431
Slave SRM
37a
Slave LE
433a
Local Event Log
435a
Master SRM
36
16a
Local Event Log
435b
Slave LE
433b
Slave SRM
37b
110
ATM
111
ATM
112
ATM
113
ATM
Port Driver
43a
Port Driver
43b
Port Driver
43c
Port Driver
43d
44a
44b
44c
44d
16n
433n
Slave LE
37n
Slave SRM
Local Event Log
435n
MPLS
432A
MPLS
432b
MPLS
432n
Port Driver
434c
Port Driver
434b
Port Driver
434n
68a
68b
68N

FIG. 27

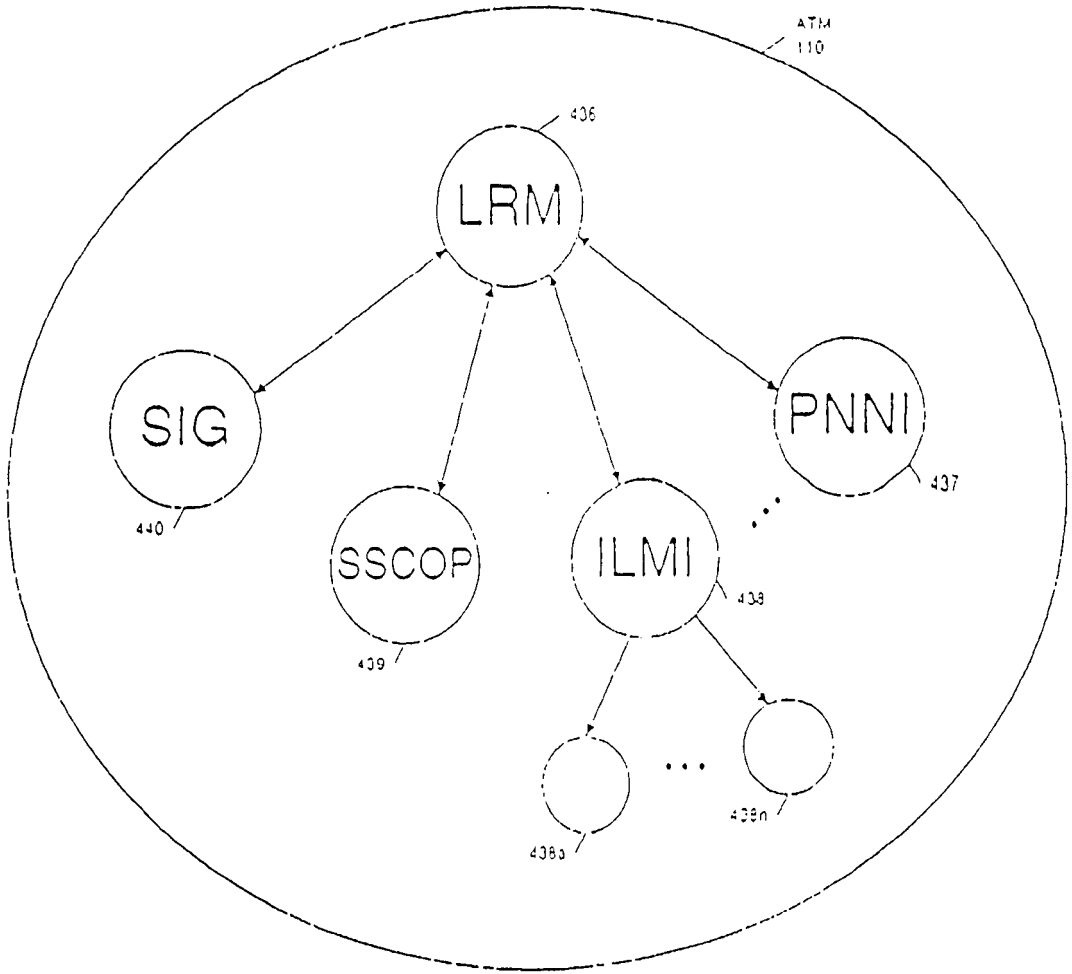


FIG. 28

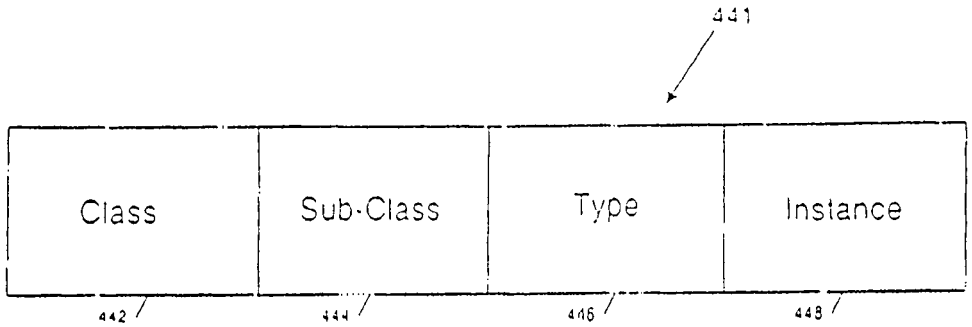


FIG. 29

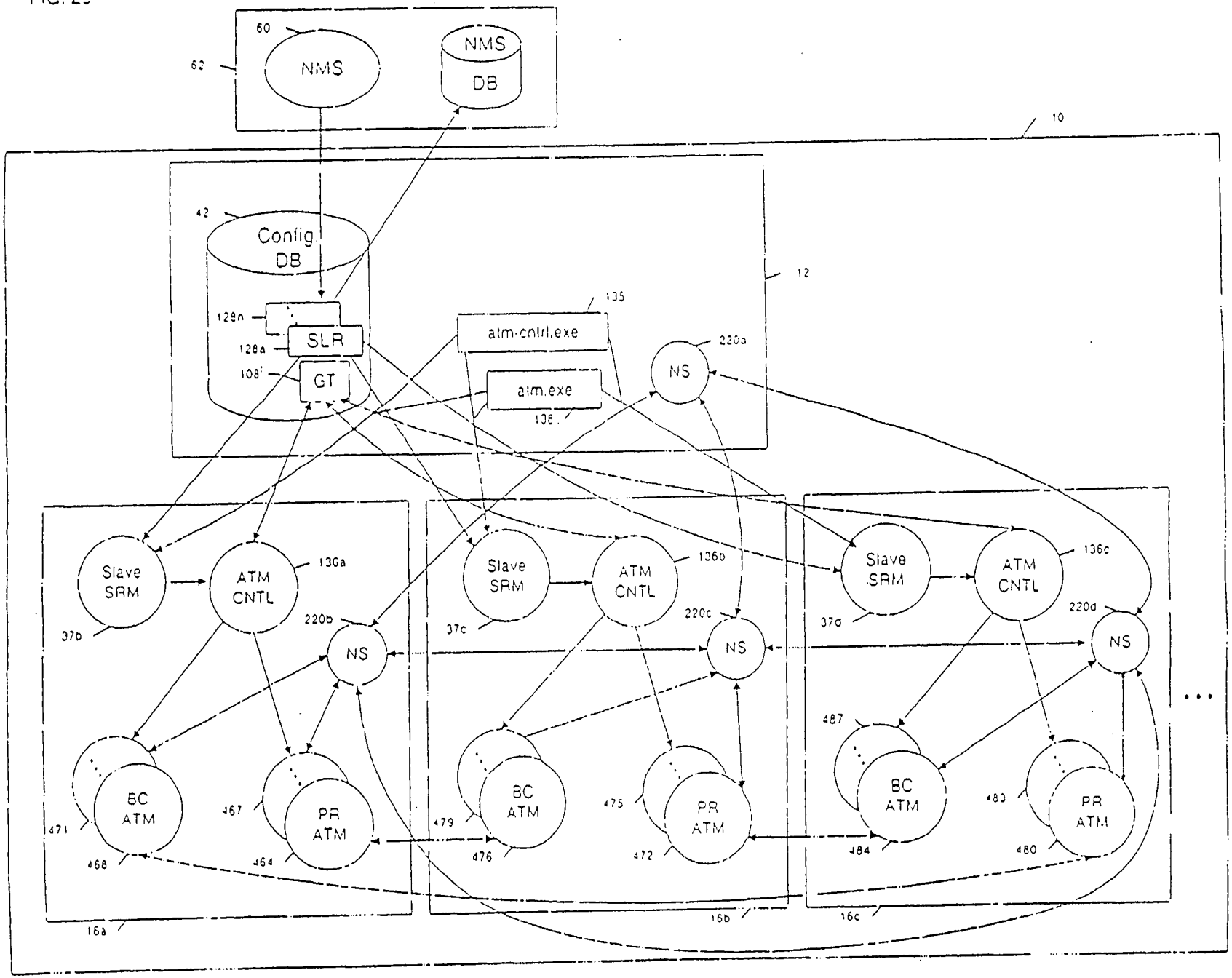

60
NMS
62
NMS
DB
10
12
42
Config.
DB
128n
128a
108'
SLR
GT
135
atm-cntrl.exe
atm.exe
138
220a
NS
Slave
SRM
ATM
CNTL
136a
37b
220b
NS
BC
ATM
471
PR
ATM
467
468
464
16a
Slave
SRM
ATM
CNTL
136b
37c
220c
NS
BC
ATM
479
476
PR
ATM
475
472
16b
Slave
SRM
ATM
CNTL
136c
37d
220d
NS
BC
ATM
487
484
PR
ATM
483
480
16c

FIG.30

Group Table 108'

	Group #	Primary Card LID (447)	Backup Card LID (449)	...
450	1	30	31	
451	2	30	31	
452	3	30	31	
453	4	30	31	
454	5	31	32	
455	6	31	32	
456	7	31	32	
457	8	31	32	
458	9	32	30	
459	10	32	30	
460	11	32	30	
461	12	32	30	
	⋮	⋮	⋮	⋮

Fig. 31a

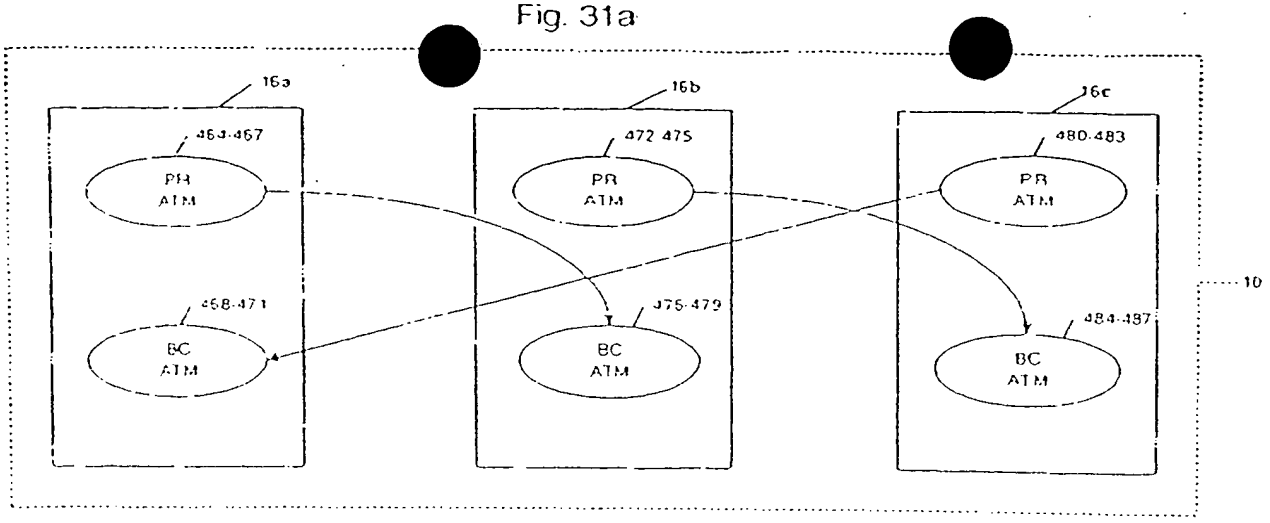


Fig. 31b

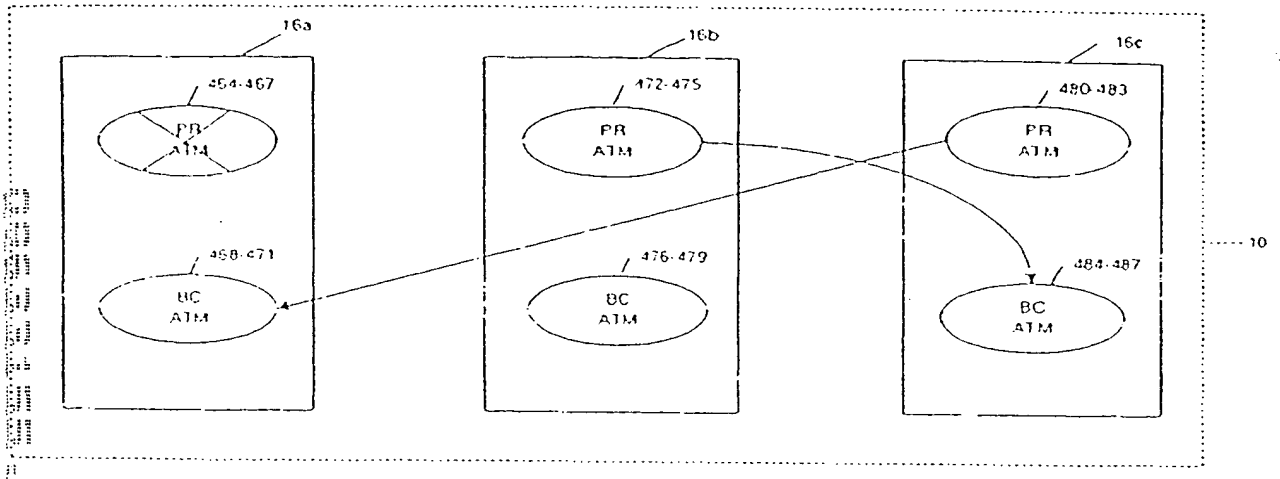


Fig. 31c

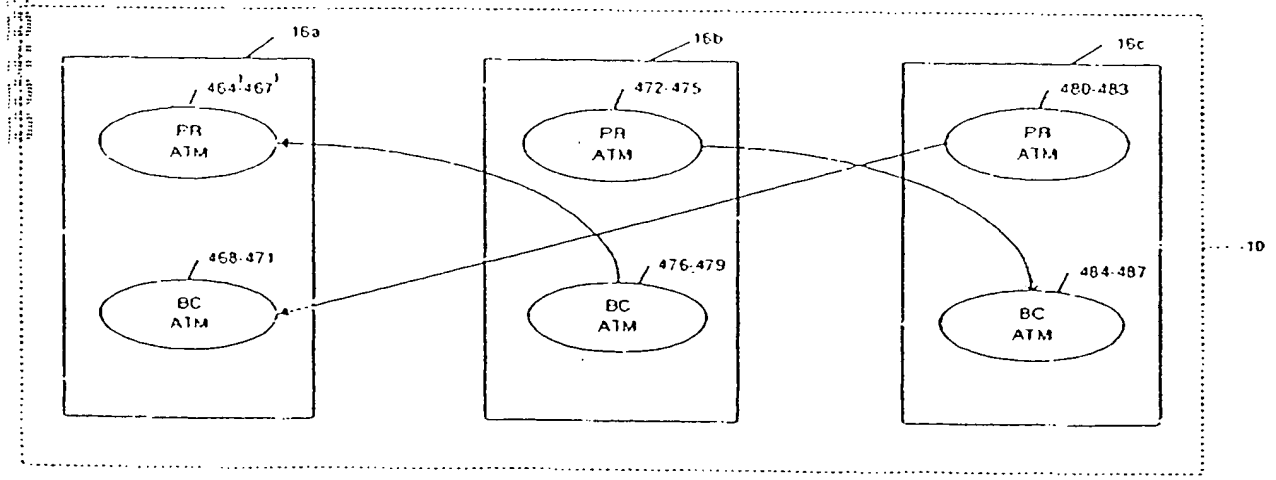


Fig. 32a

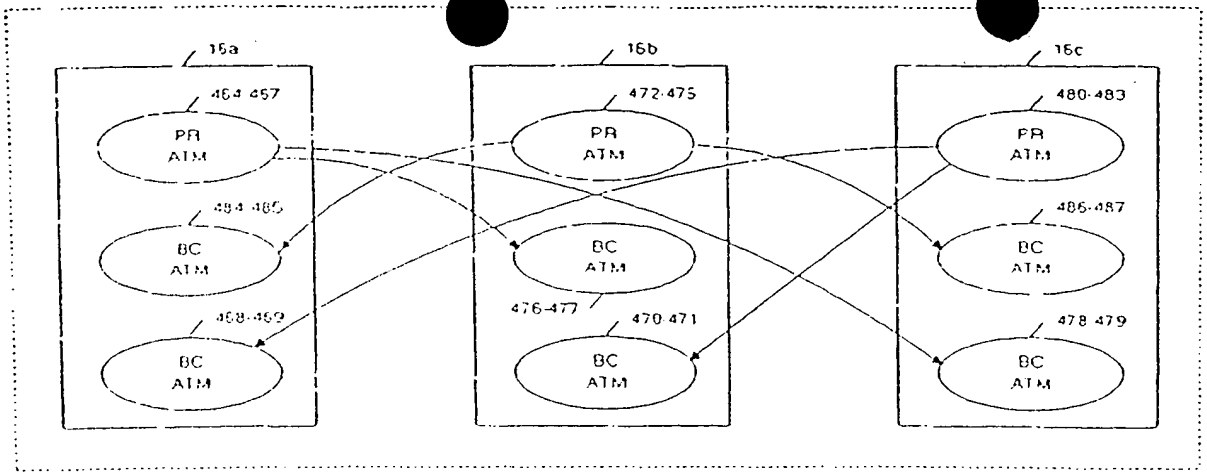


Fig. 32b

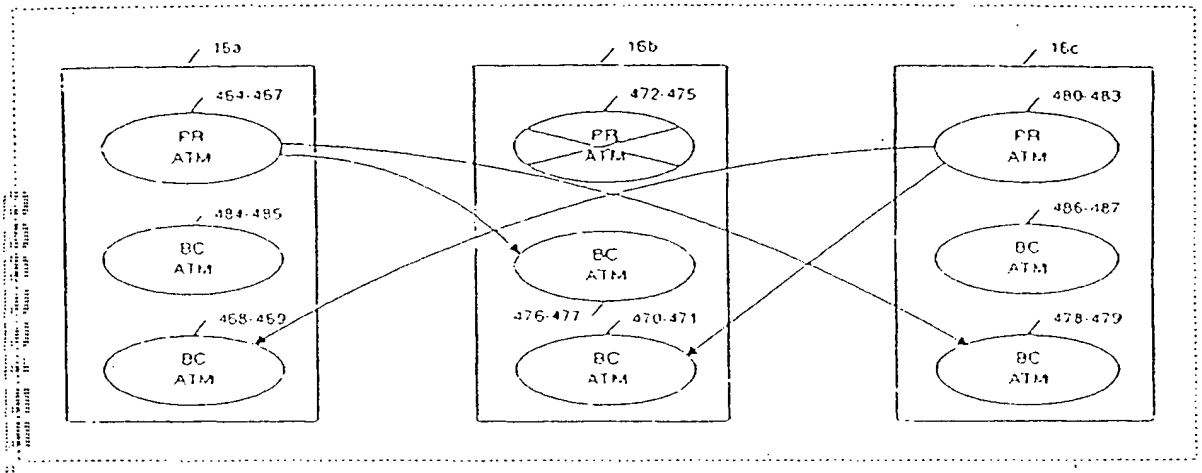


Fig. 32c

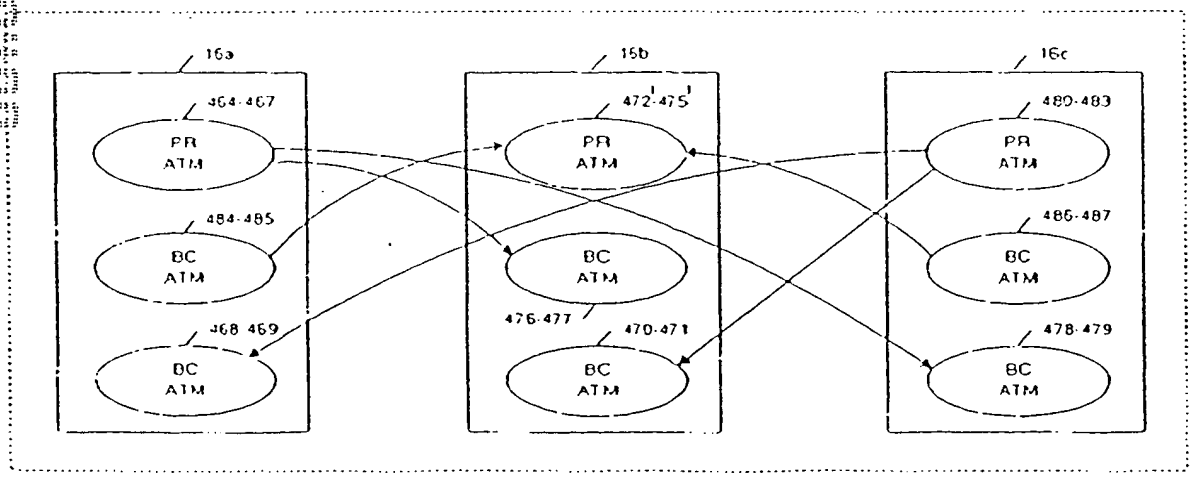


FIG. 33a

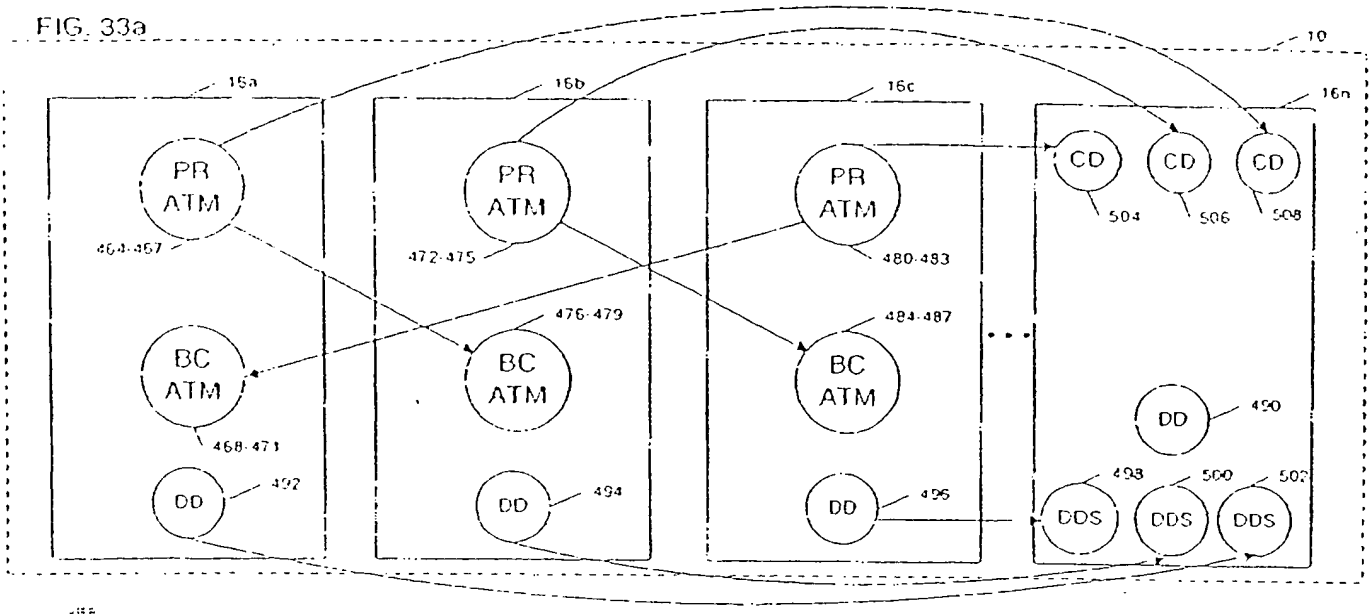


FIG. 33b

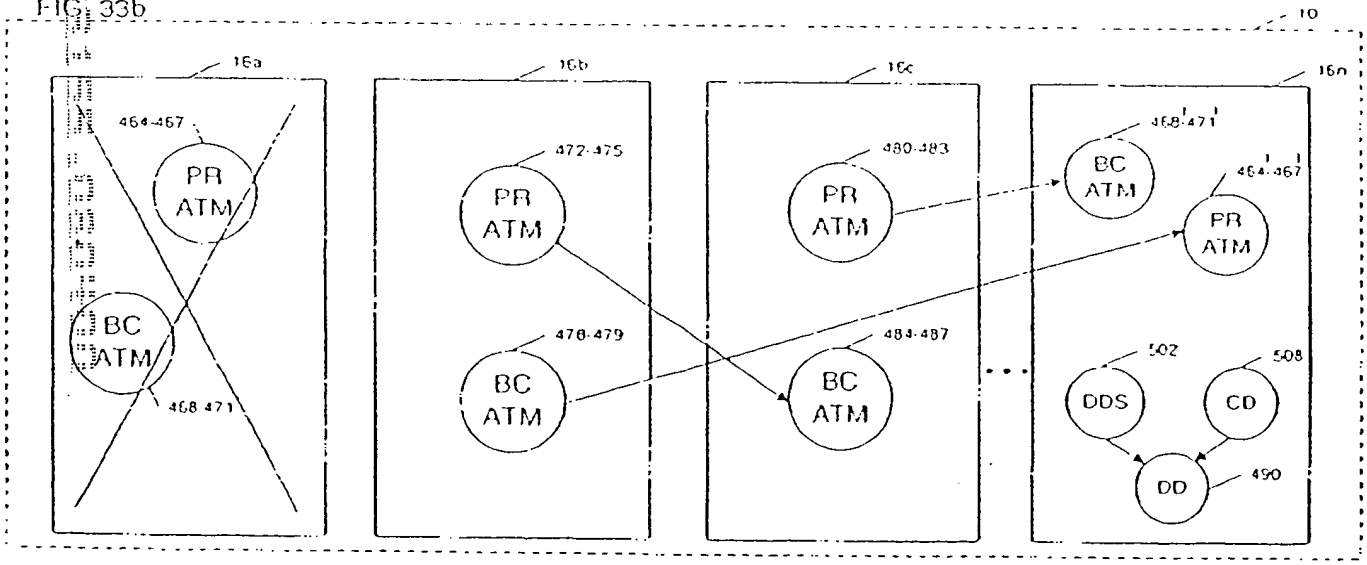


FIG. 33c

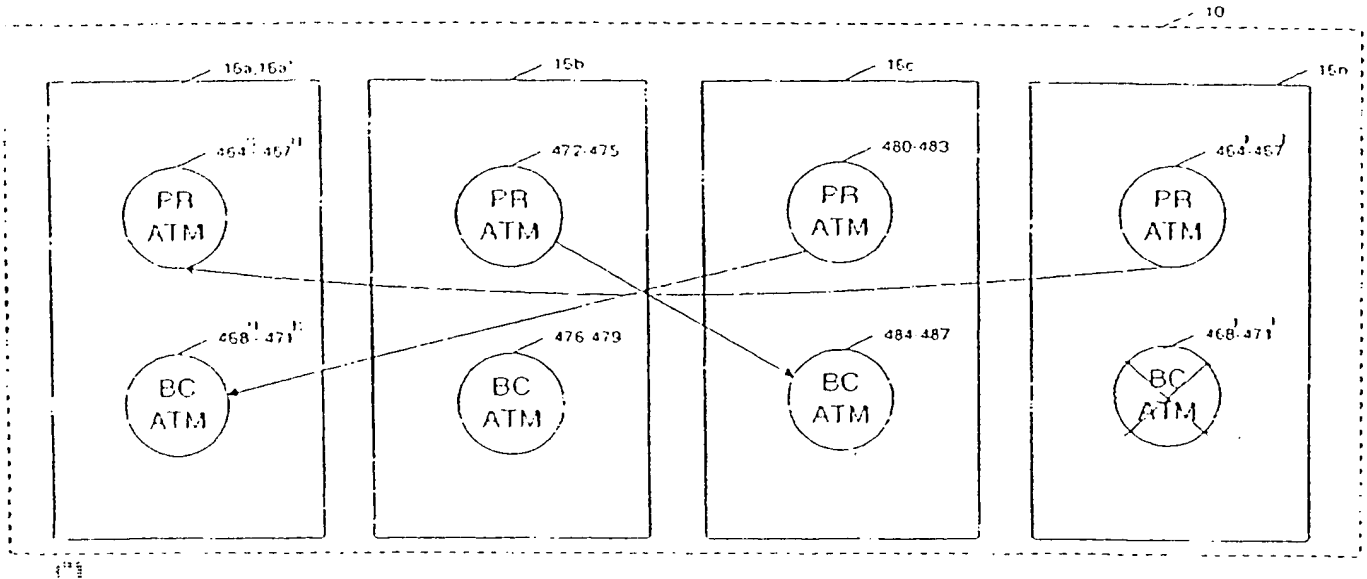


FIG. 33d

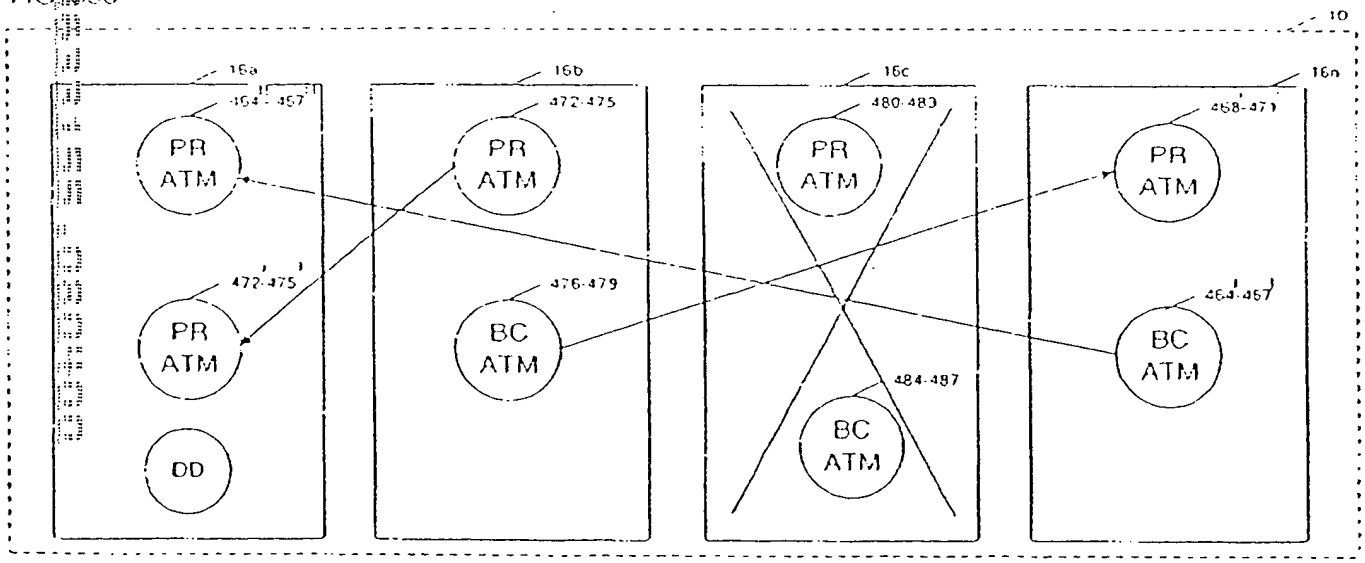


Fig. 34a

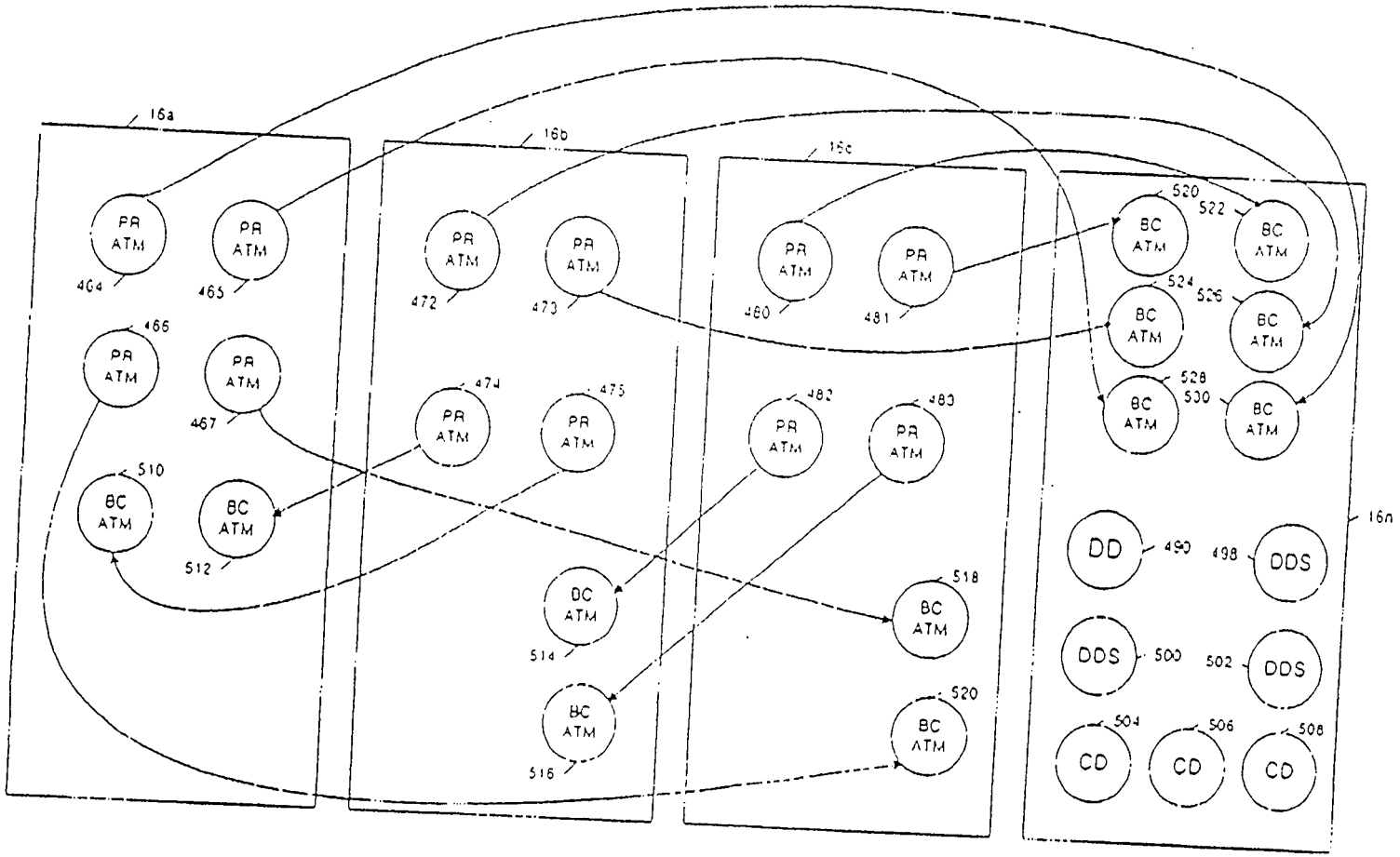

16a
16b
16c
16n
PR ATM
464
465
466
467
472
473
474
475
460
481
482
483
BC ATM
510
512
514
516
518
520
522
524
526
528
530
DD
490
498
DDS
500
502
CD
504
506
508

FIG. 34b

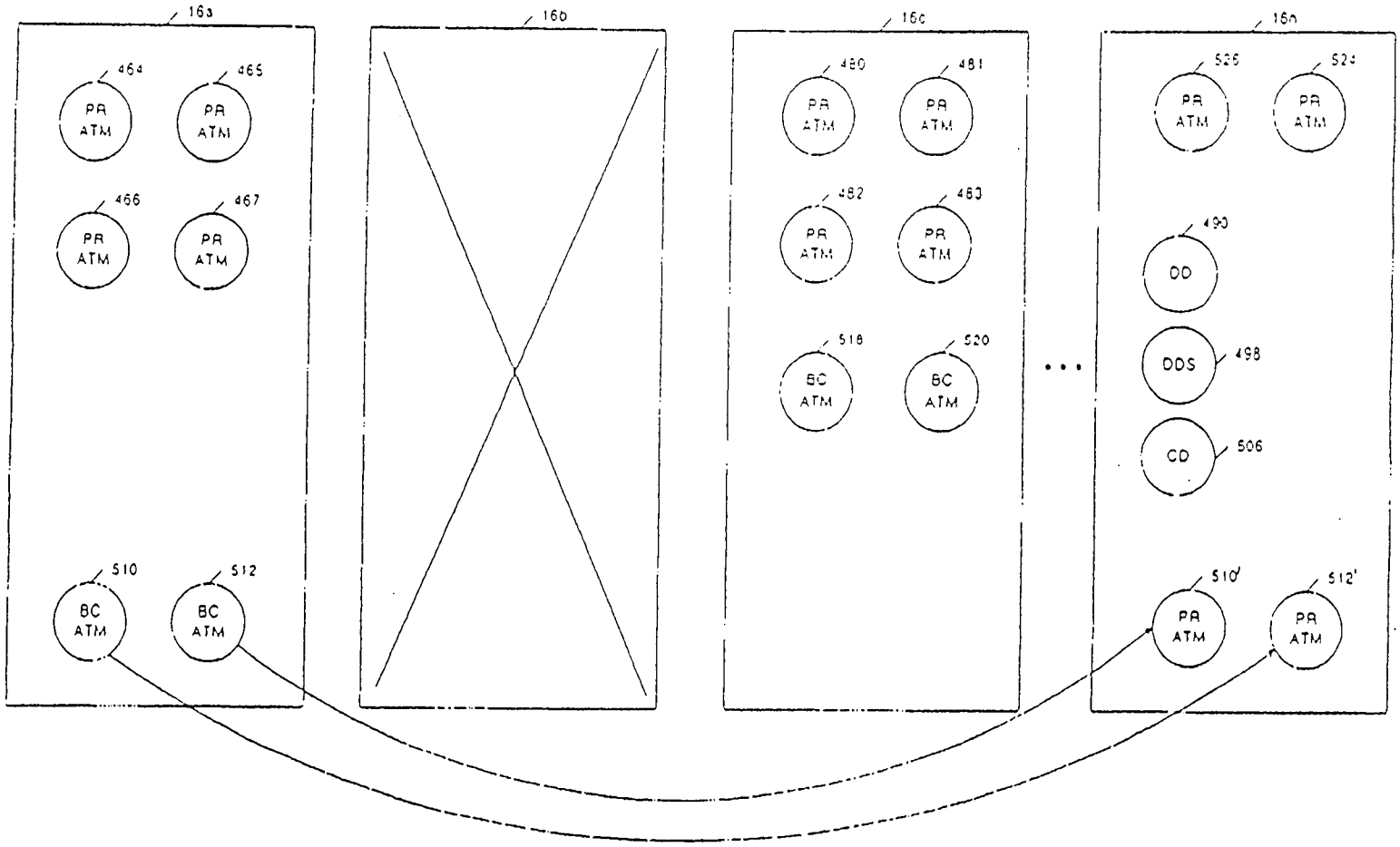

16a
16b
16c
16n
464
PR ATM
465
PR ATM
466
PR ATM
467
PR ATM
510
BC ATM
512
BC ATM
480
PR ATM
481
PR ATM
482
PR ATM
483
PR ATM
518
BC ATM
520
BC ATM
526
PR ATM
528
PR ATM
490
OD
DDS
498
CD
506
510'
PR ATM
512'
PR ATM

FIG. 35

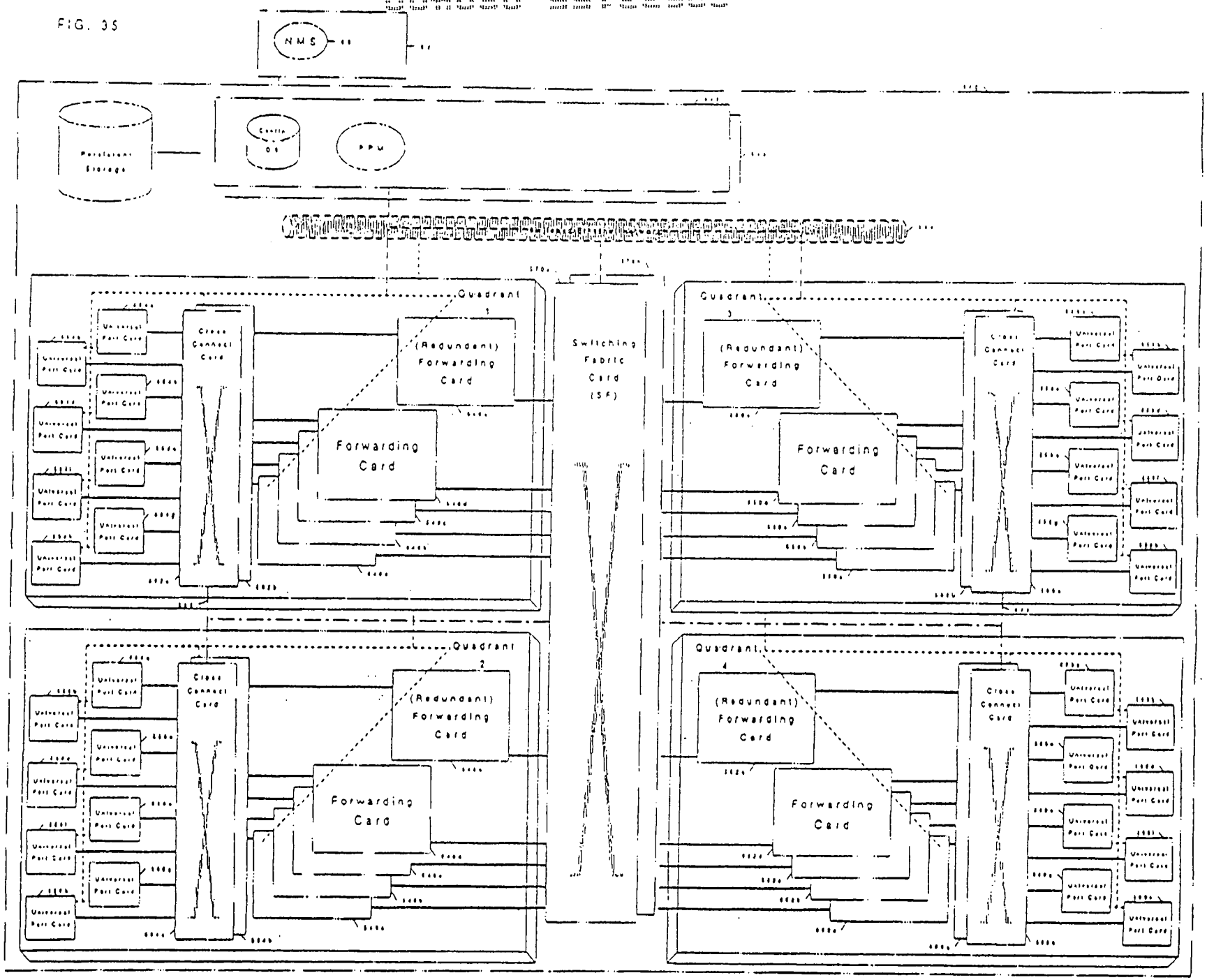

NMS
Persistent Storage
Quadrant 1
Quadrant 2
Quadrant 3
Quadrant 4
Cross Connect Card
(Redundant) Forwarding Card
Forwarding Card
Switching Fabric Card (SF)
Universal Port Card

FIG. 36

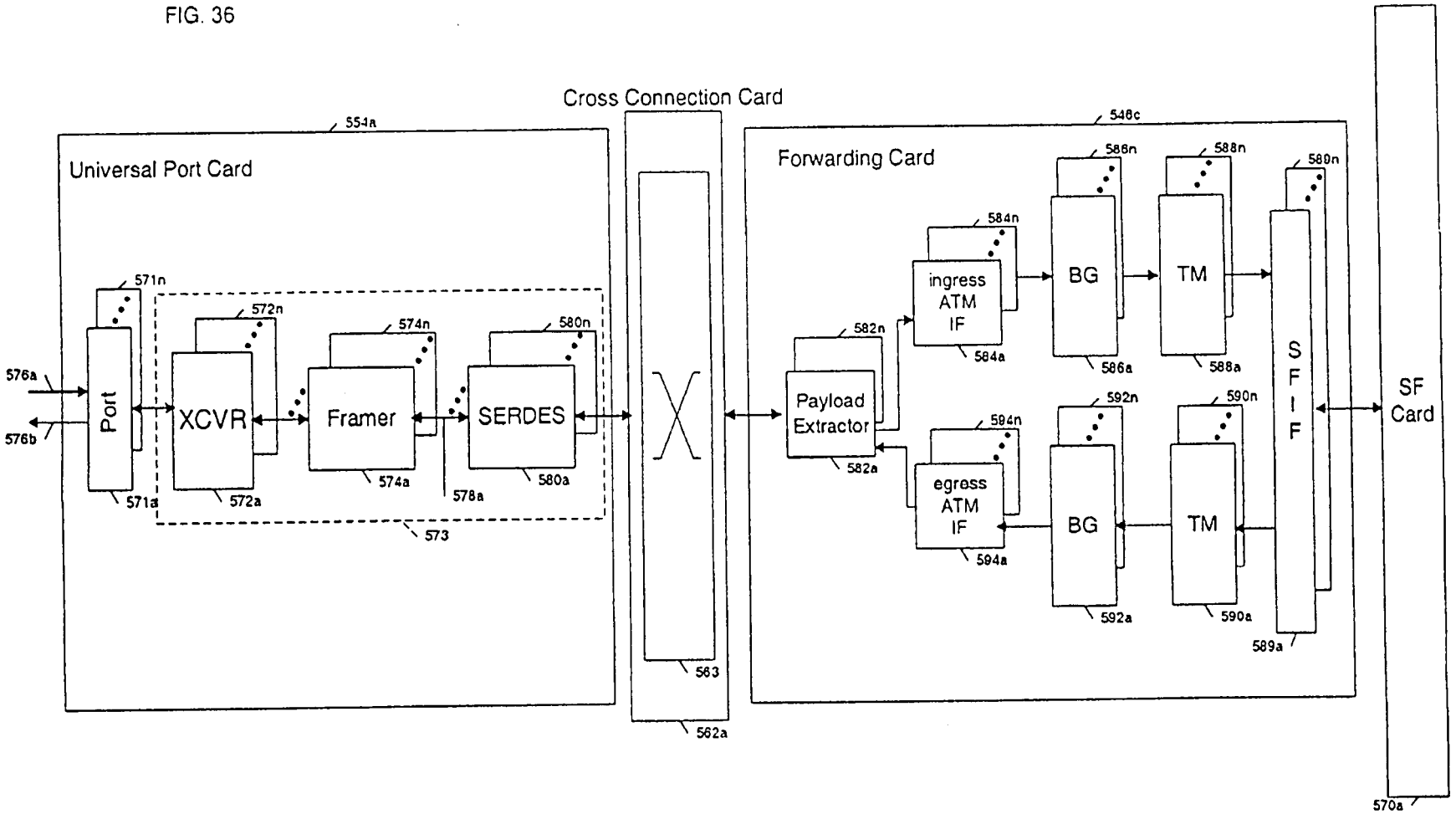

Cross Connection Card
554a
Universal Port Card
571n
572n
574n
580n
576a
576b
Port
XCVR
Framer
SERDES
571a
572a
574a
578a
580a
573
563
562a
546c
Forwarding Card
584n
586n
588n
589n
582n
ingress ATM IF
BG
TM
S F I F
Payload Extractor
584a
586a
588a
582a
592n
590n
594n
egress ATM IF
BG
TM
SF Card
594a
592a
590a
589a
570a

FIG. 37

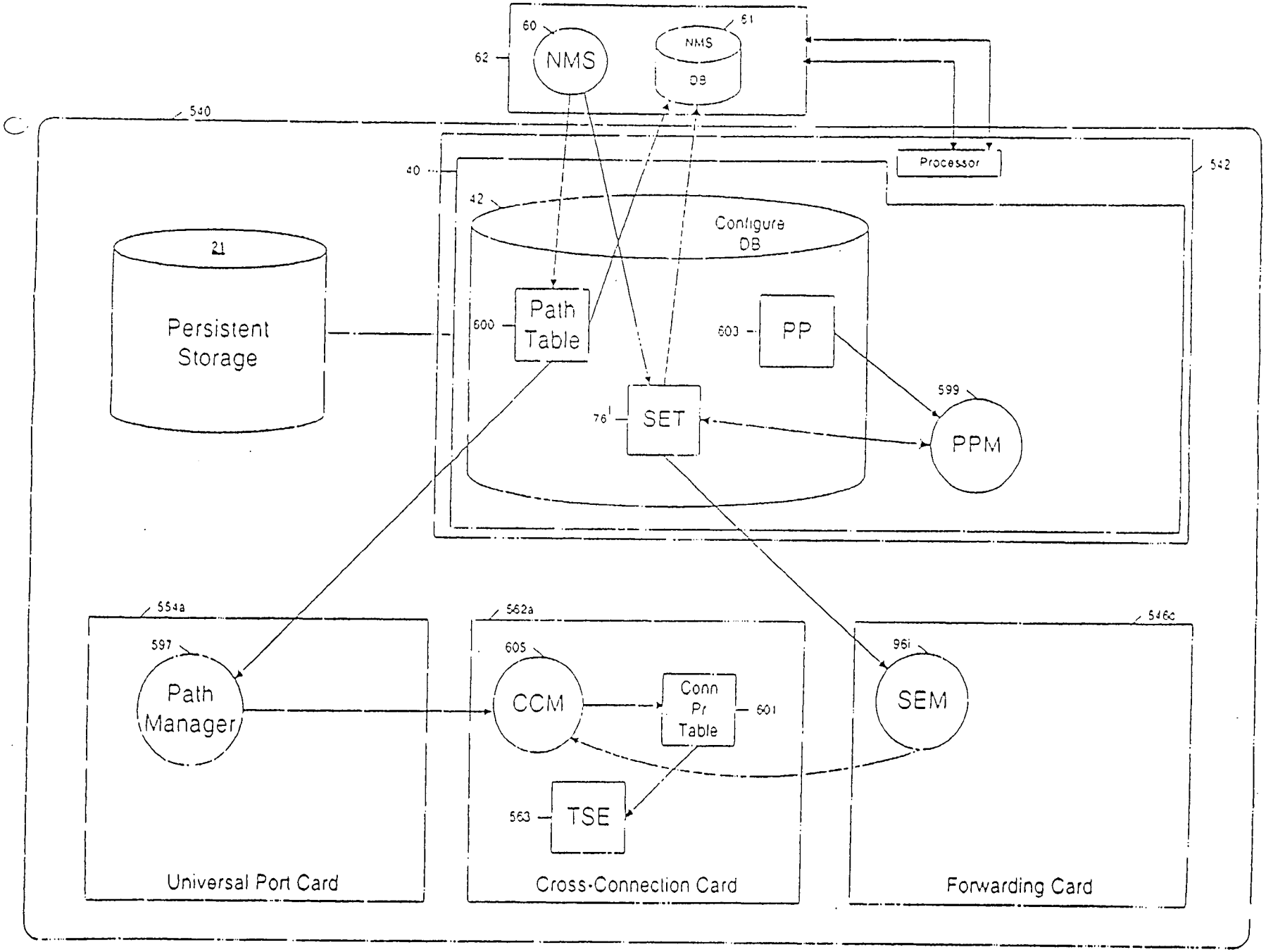

60
NMS
62
61
NMS
DB
540
Processor
542
40
42
Configure DB
21
Persistent Storage
600
Path Table
603
PP
599
76'
SET
PPM
554a
597
Path Manager
562a
605
CCM
Conn Pr Table
601
563
TSE
546c
96i
SEM
Universal Port Card
Cross-Connection Card
Forwarding Card

FIG. 38

Path Table 600

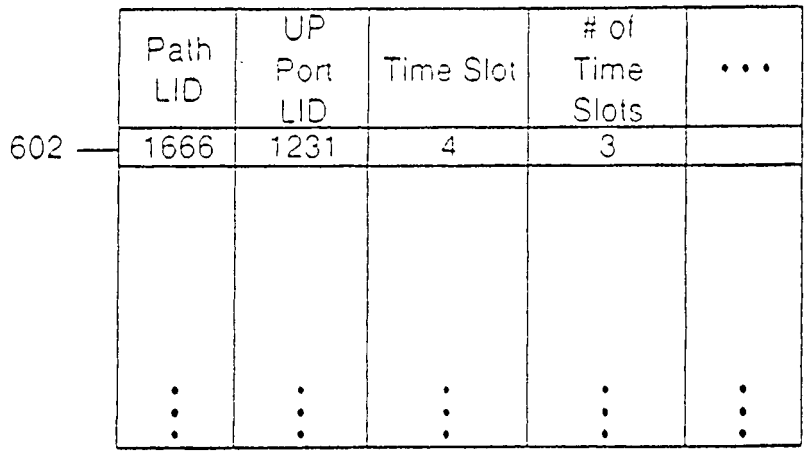
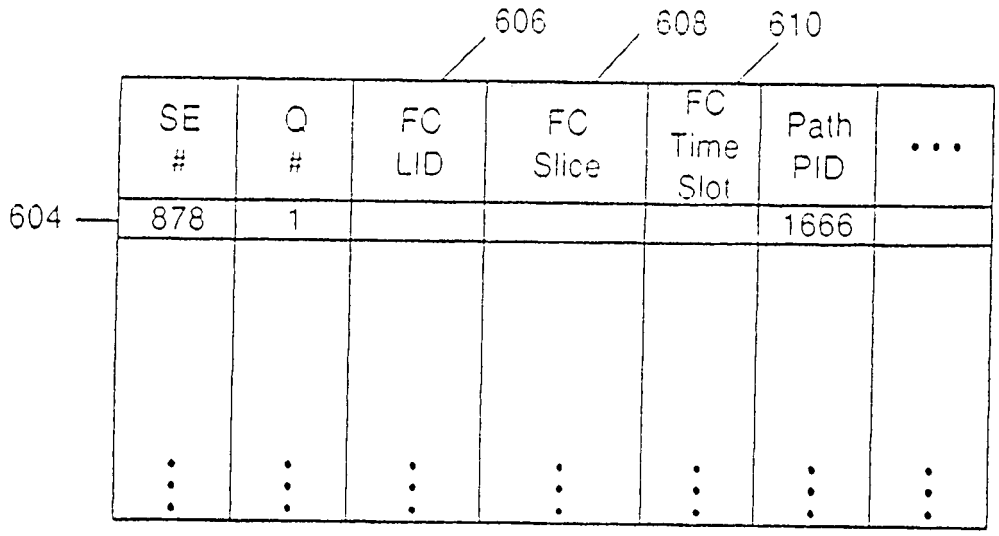


FIG. 39

Service End Point Table 76'



19"
24"
41b
41b
EQUIPE
540
619
546a
546b
546c
546d
546e
548a
548b
548c
548d
548e
547
547
547
550a
550b
550c
550d
550e
552a
552b
552c
552d
552e
7'
41a
41a
41c

Fig. 40

FIG. 41a
Front

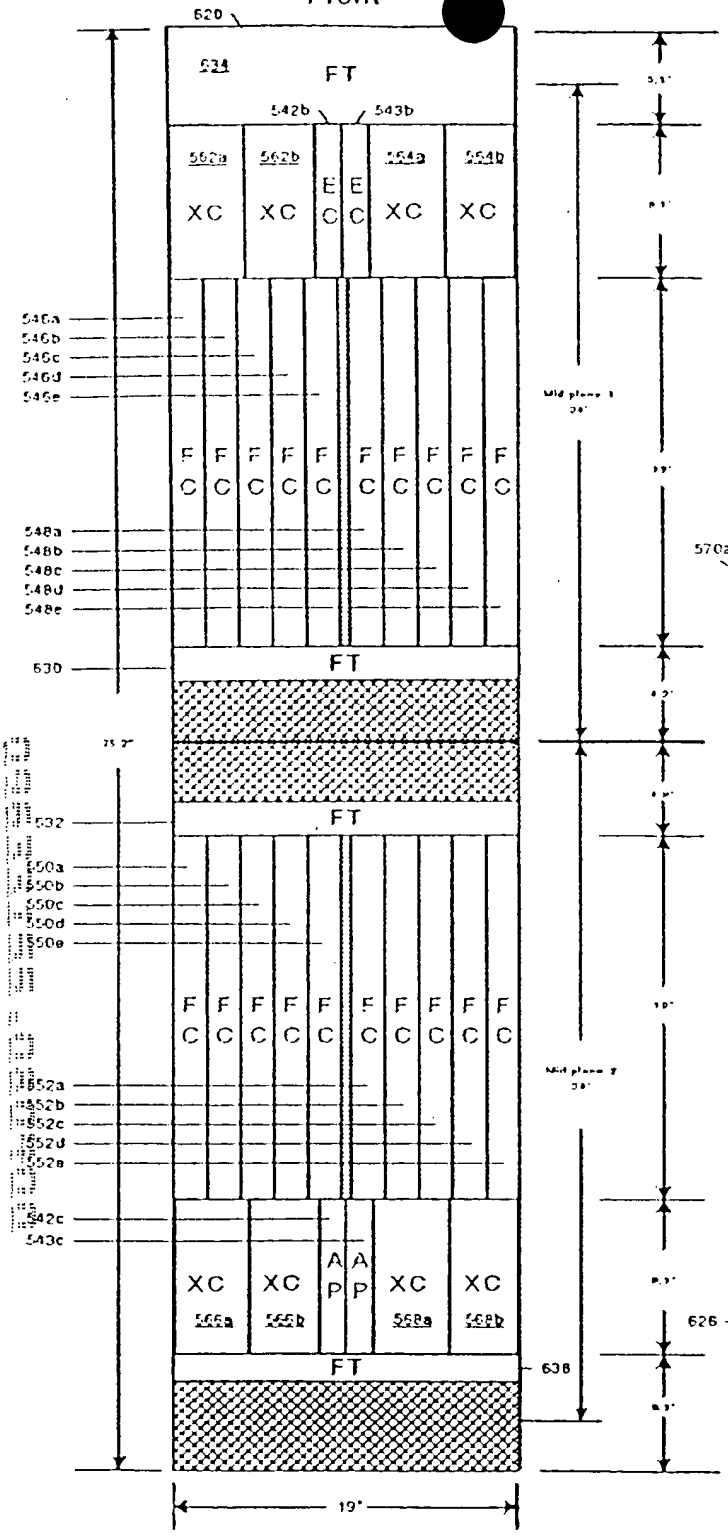


FIG. 41b
Back

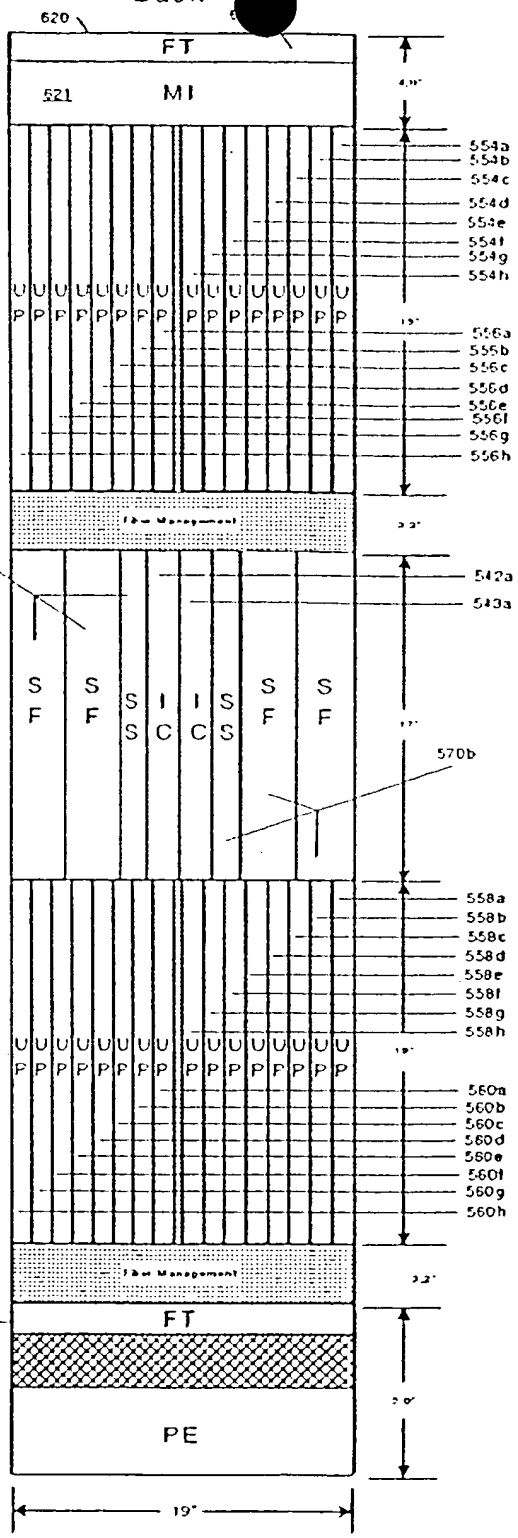


FIG. 4C

Side

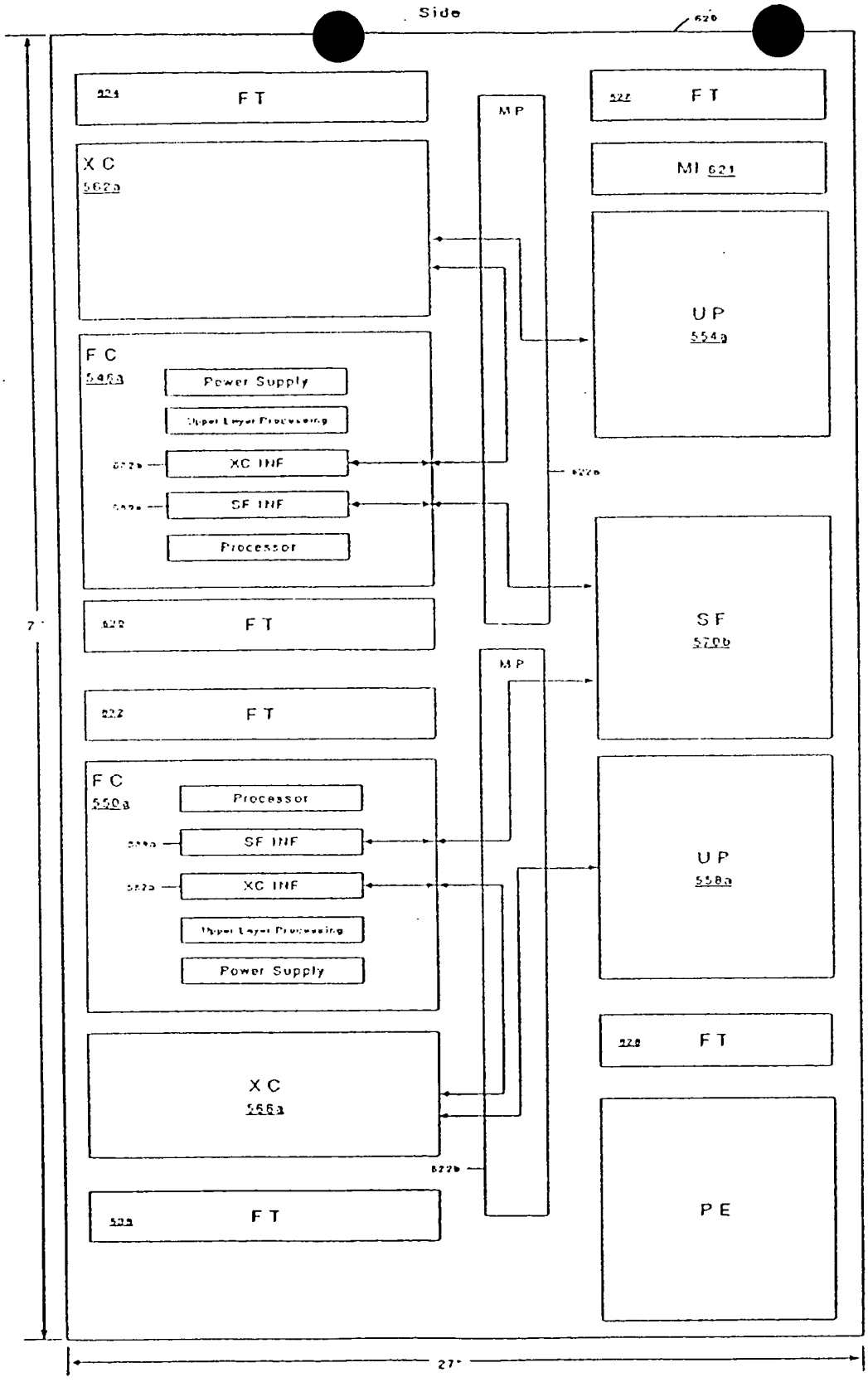

626
FT
MP
FT
XC
562a
MI 621
UP
554a
FC
540a
Power Supply
Upper Layer Processing
XC INF
SF INF
Processor
FT
SF
570b
MP
FT
FC
550a
Processor
SF INF
XC INF
Upper Layer Processing
Power Supply
UP
558a
FT
XC
566a
FT
PE
7'
27'

FIG. 42

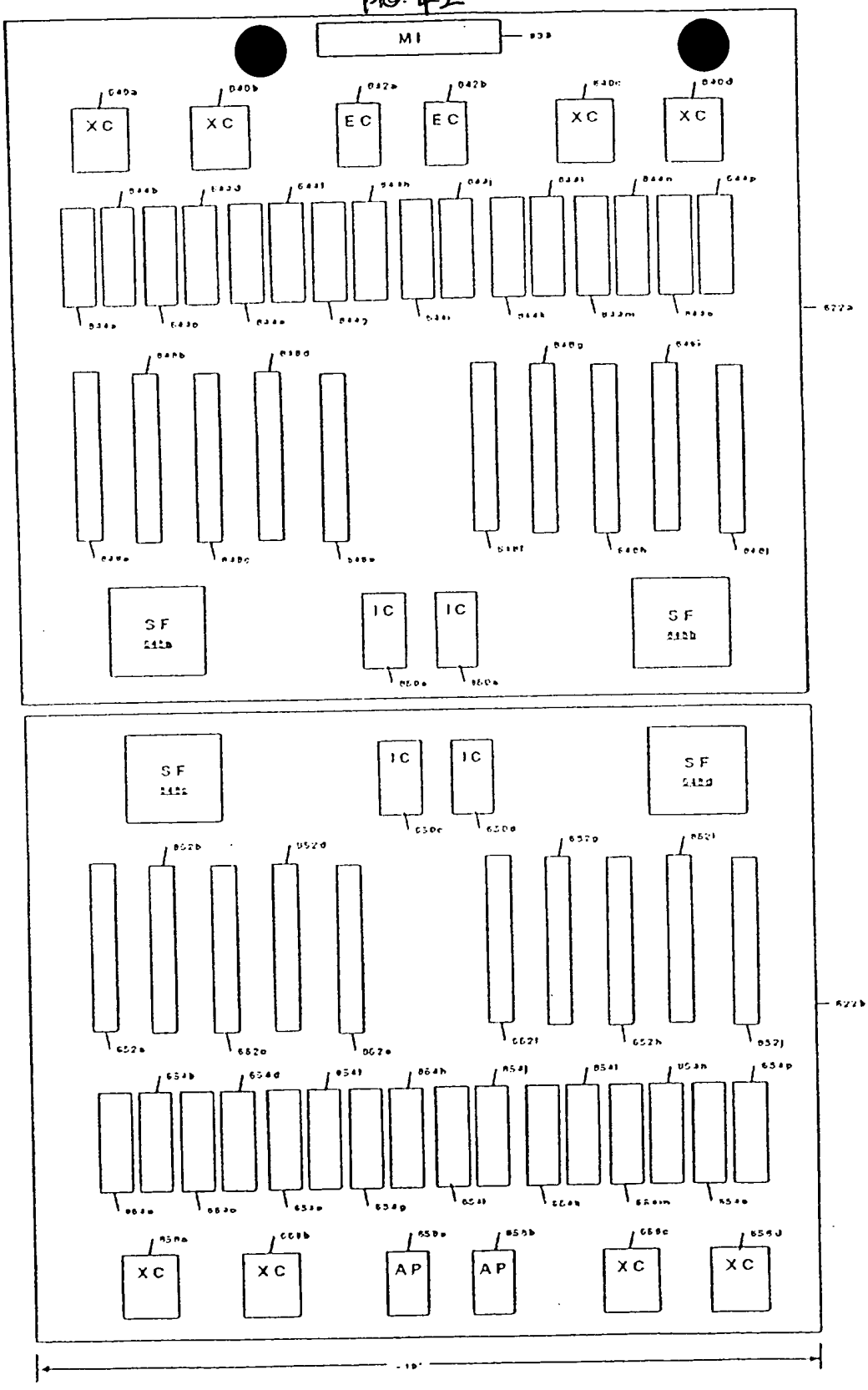
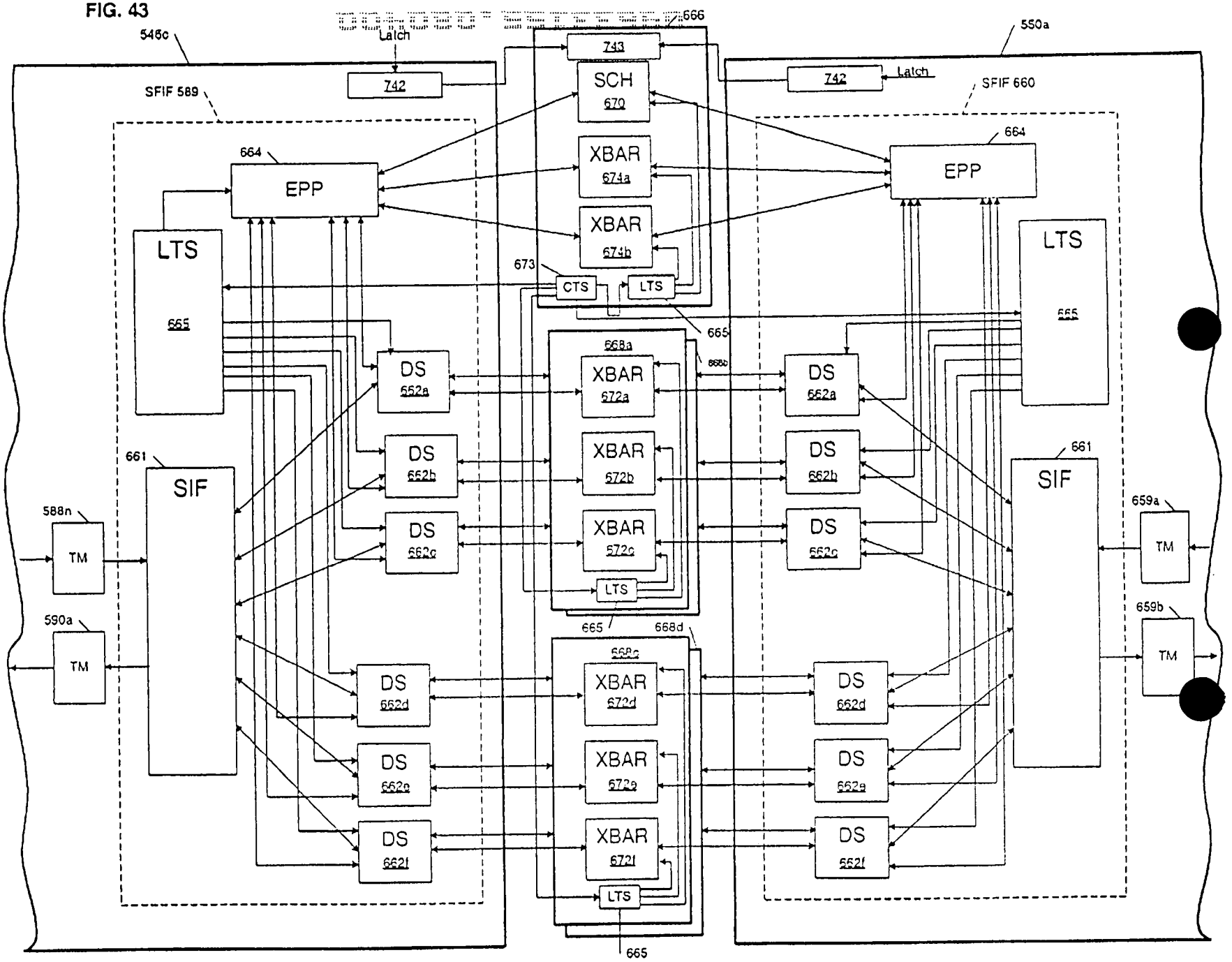


FIG. 43


546c
Latch
666
743
SCH
670
742
Latch
550a
SFIF 589
SFIF 660
664
EPP
XBAR
674a
XBAR
674b
673
CTS
LTS
665
668a
668b
668c
668d
DS
662a
DS
662b
DS
662c
DS
662d
DS
662e
DS
662f
XBAR
672a
XBAR
672b
XBAR
672c
XBAR
672d
XBAR
672e
XBAR
672f
661
SIF
588n
590a
TM
659a
659b

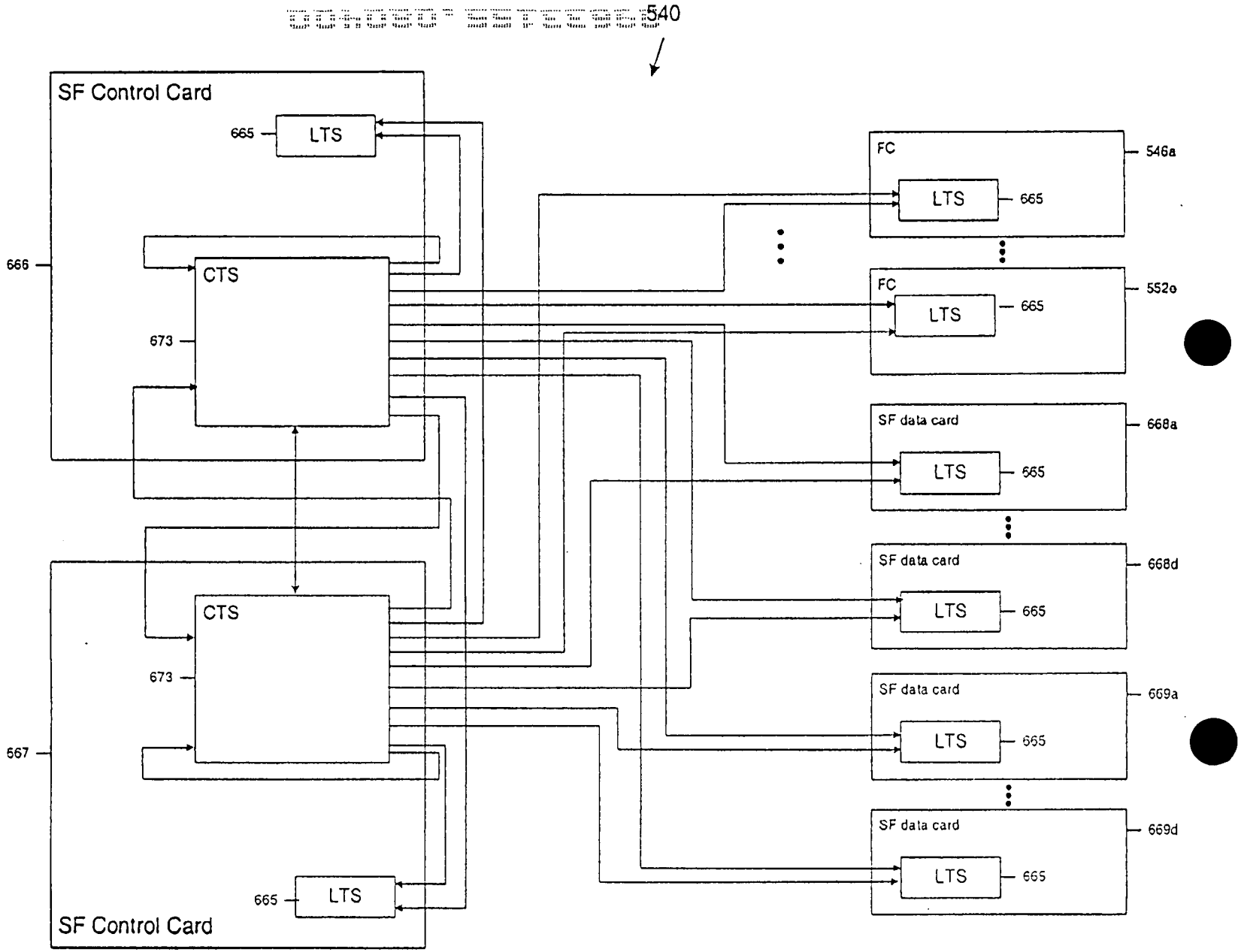
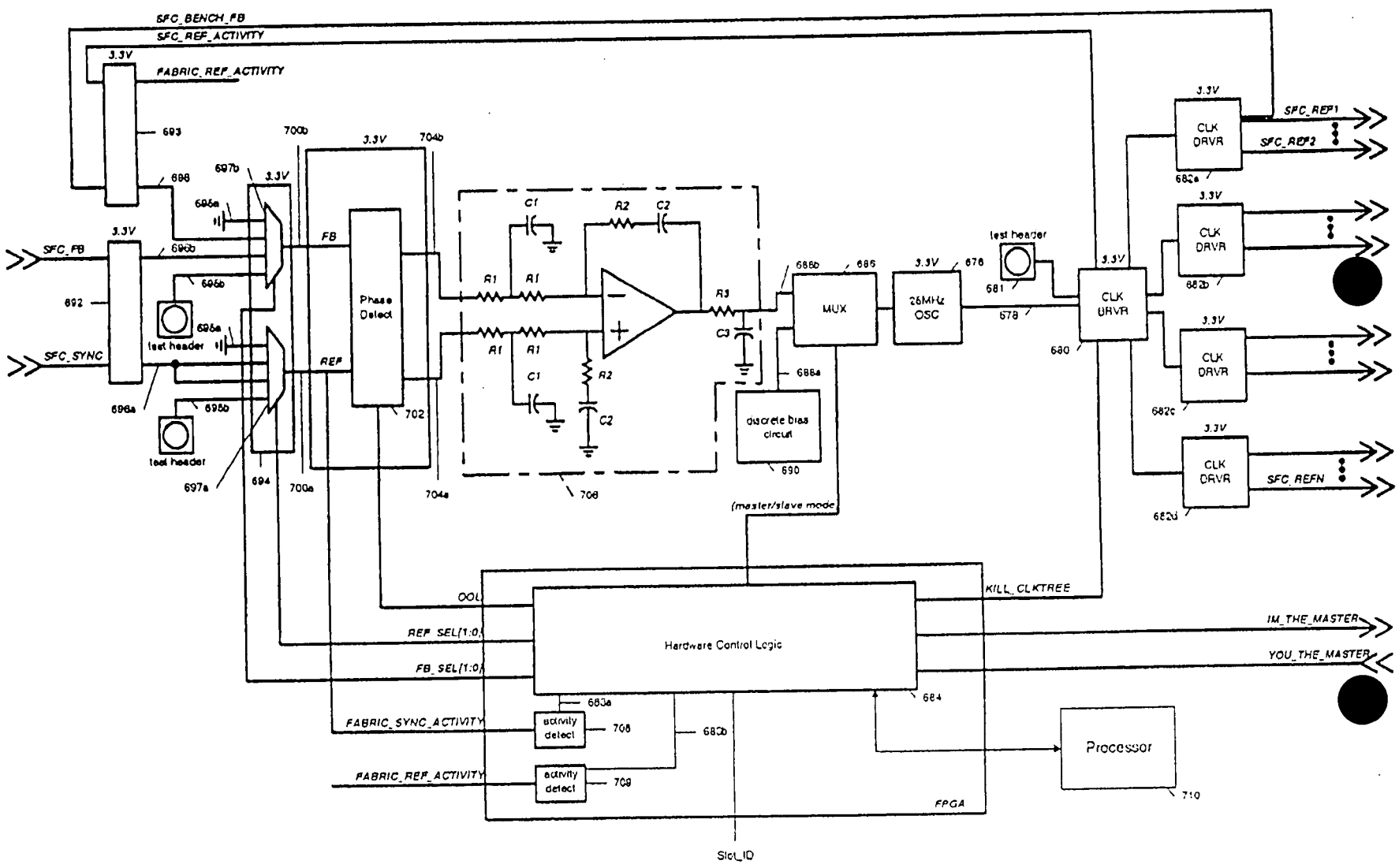


FIG. 44

FIG. 45



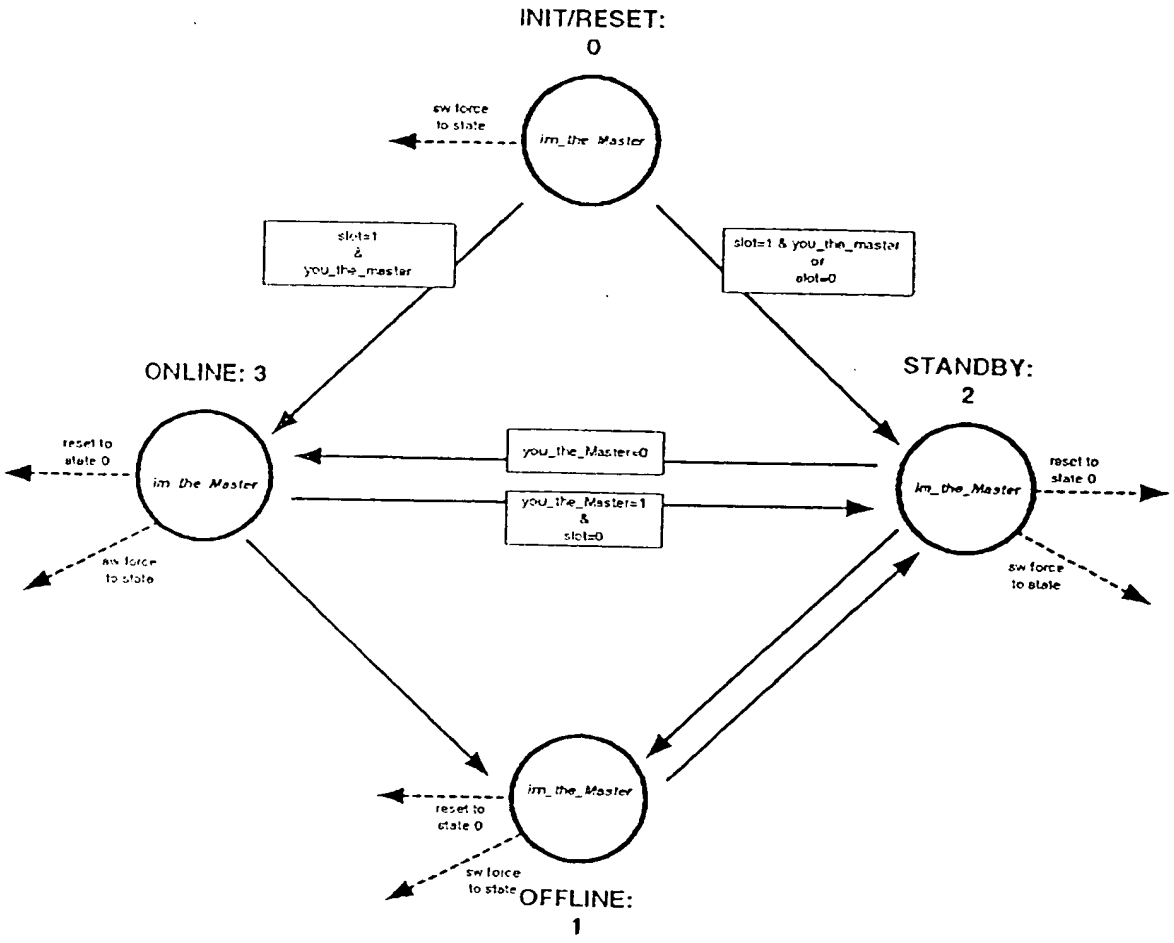

INIT/RESET:
0
sw force
to state
im_the_Master
slot=1
&
you_the_master
slot=1 & you_the_master
or
slot=0
ONLINE: 3
STANDBY:
2
reset to
state 0
im_the_Master
you_the_Master=0
you_the_Master=1
&
slot=0
im_the_Master
reset to
state 0
sw force
to state
sw force
to state
im_the_Master
reset to
state 0
sw force
to state
OFFLINE:
1

FIG. 46

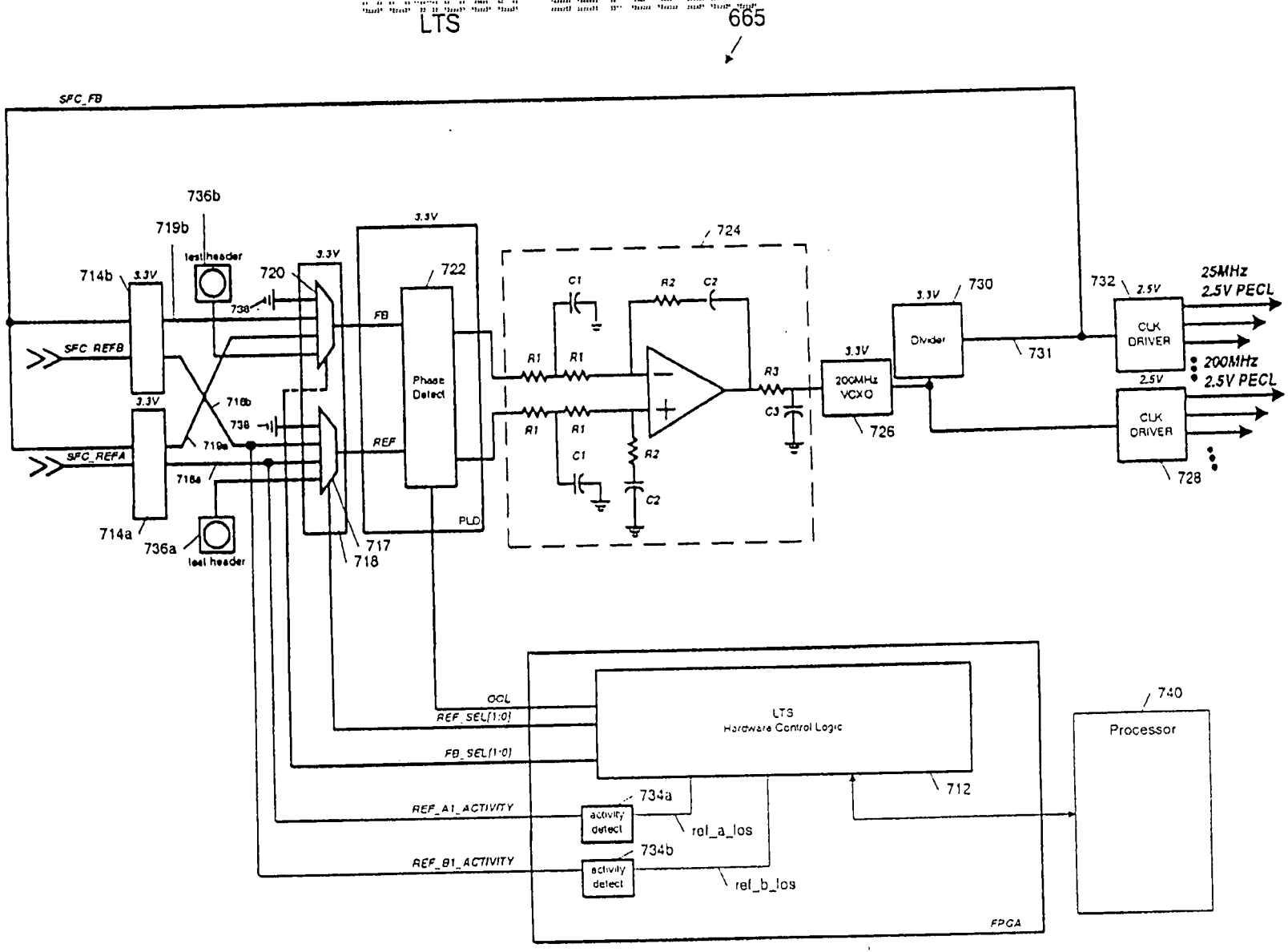

LTS
665
SFC_FB
736b
719b
testheader
714b
3.3V
720
738
SFC_REFB
716b
719a
SFC_REFA
716a
714a
736a
test header
717
718
FB
REF
Phase Detect
PLD
722
724
C1
R2
C2
R1
R3
C3
200MHz VCXO
726
Divider
730
731
732
2.5V
CLK DRIVER
25MHz
2.5V PECL
200MHz
2.5V PECL
728
OCL
REF_SEL[1:0]
FB_SEL[1:0]
LTS
Hardware Control Logic
712
740
Processor
REF_A1_ACTIVITY
REF_B1_ACTIVITY
activity detect
734a
734b
ref_a_los
ref_b_los
FPGA

FIG. 47

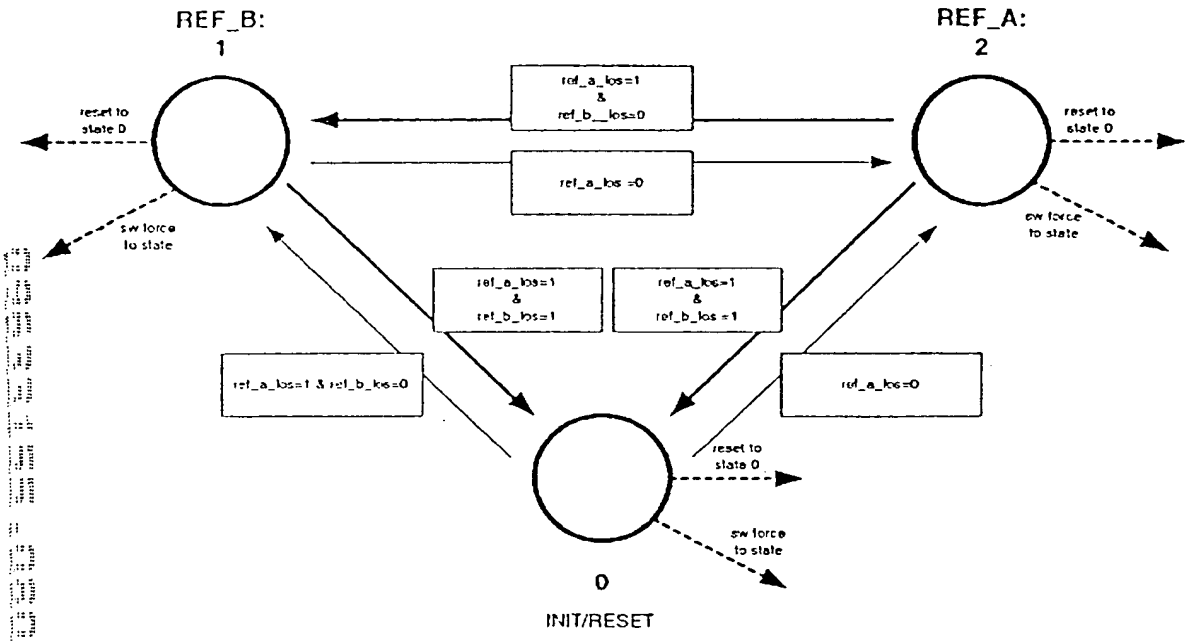

REF_B:
1
REF_A:
2
reset to state 0
sw force to state
ref_a_los=1 & ref_b_los=0
ref_a_los =0
reset to state 0
sw force to state
ref_a_los=1 & ref_b_los=1
ref_a_los=1 & ref_b_los =1
ref_a_los=1 & ref_b_los=0
ref_a_los=0
reset to state 0
sw force to state
0
INIT/RESET

FIG. 48

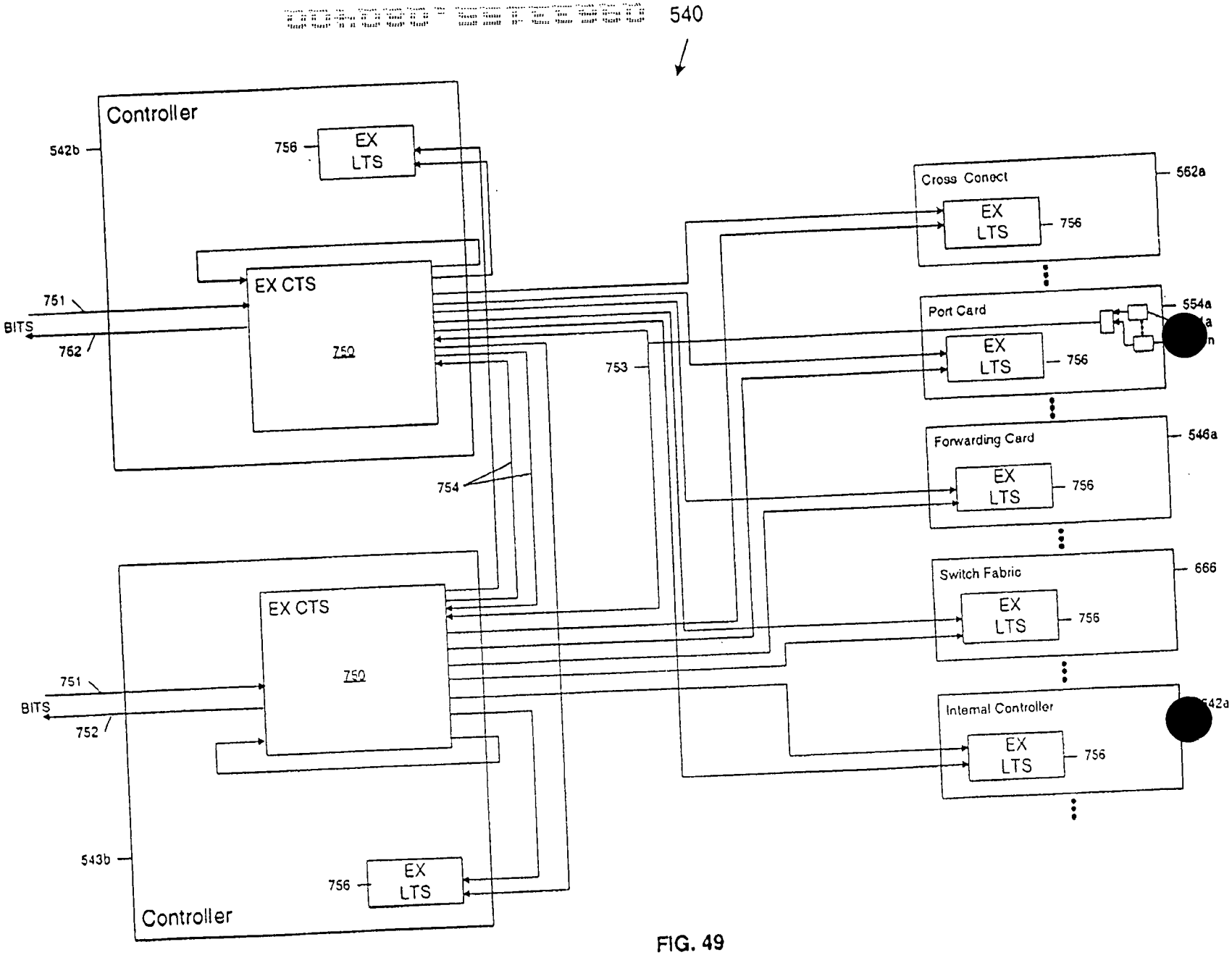


FIG. 49

EX CTS 750

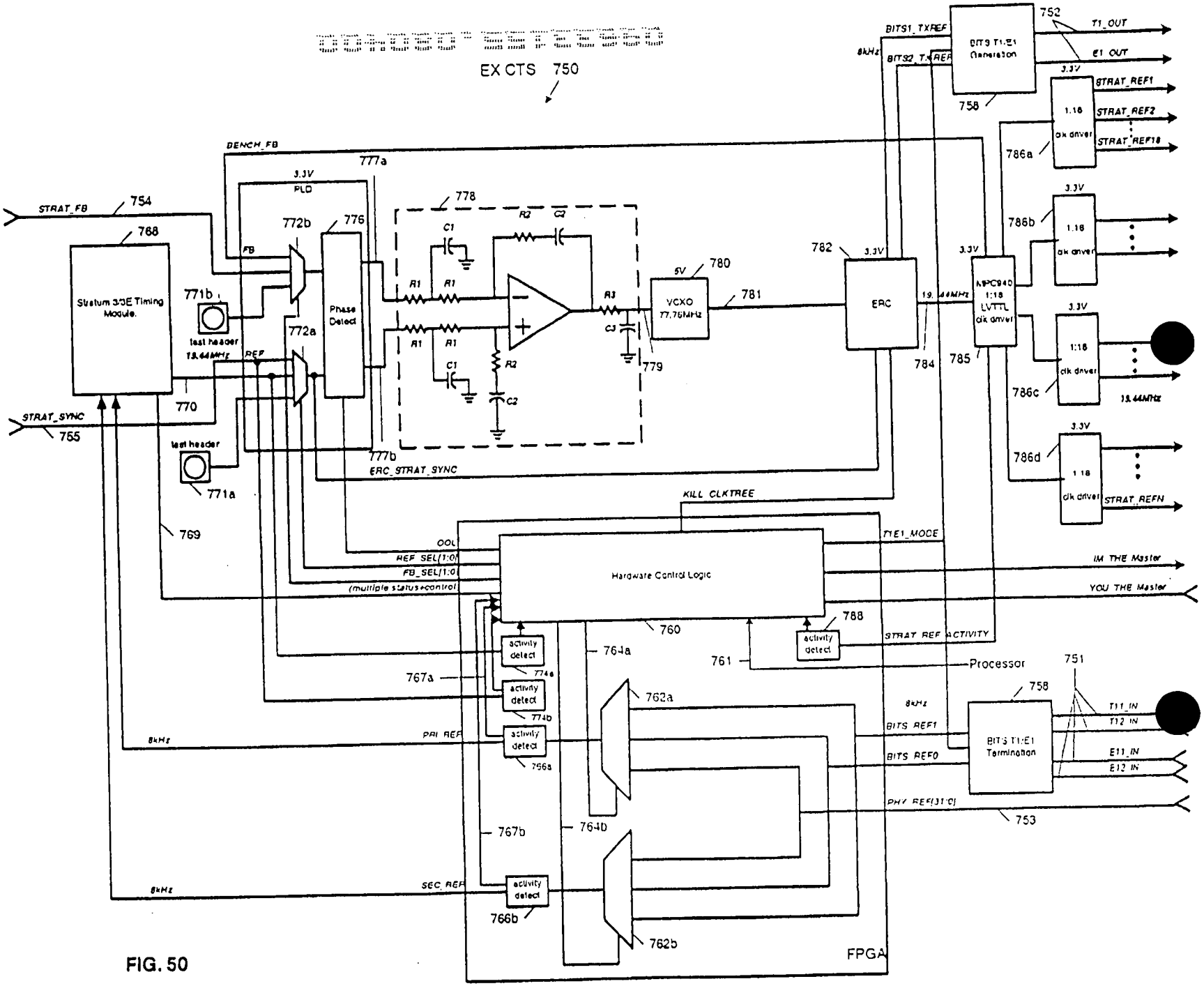


FIG. 50

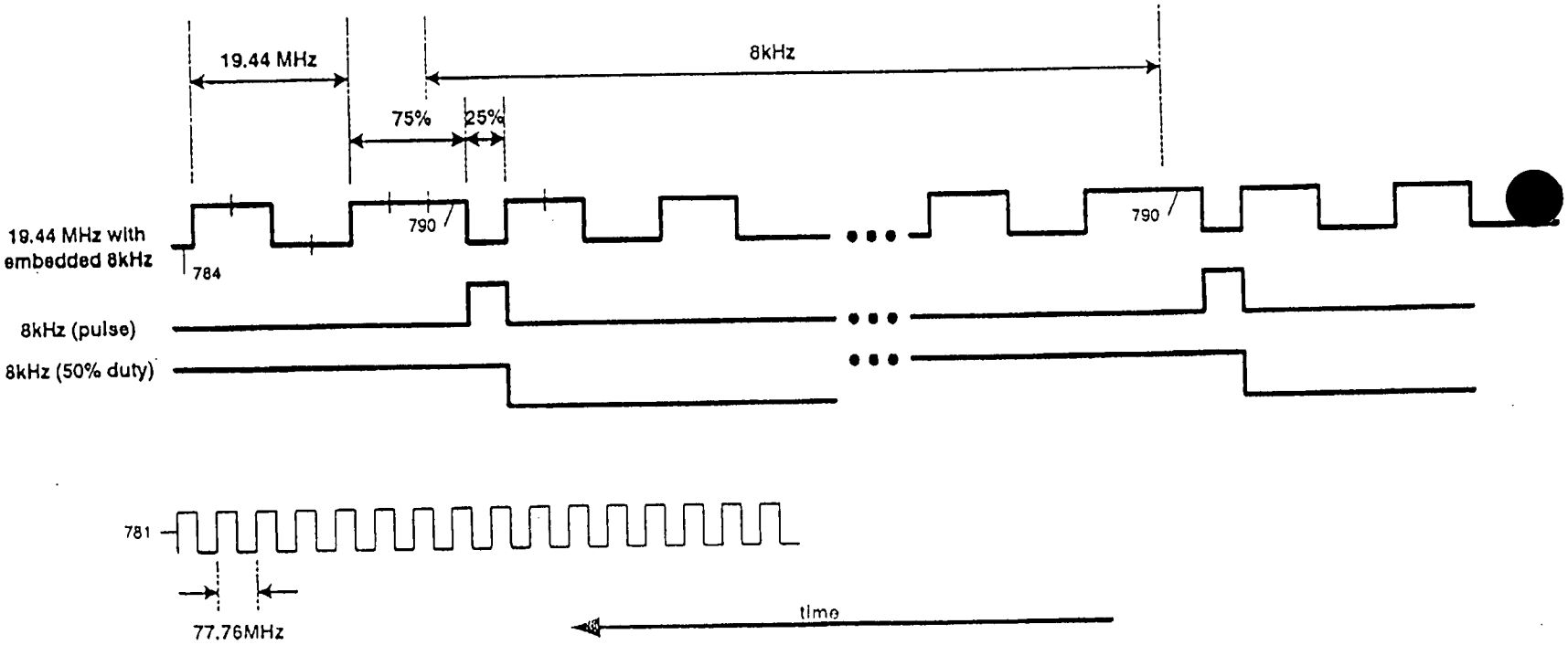

19.44 MHz
8kHz
75%
25%
19.44 MHz with
embedded 8kHz
784
790
790
8kHz (pulse)
8kHz (50% duty)
781
77.76MHz
time

FIG. 51

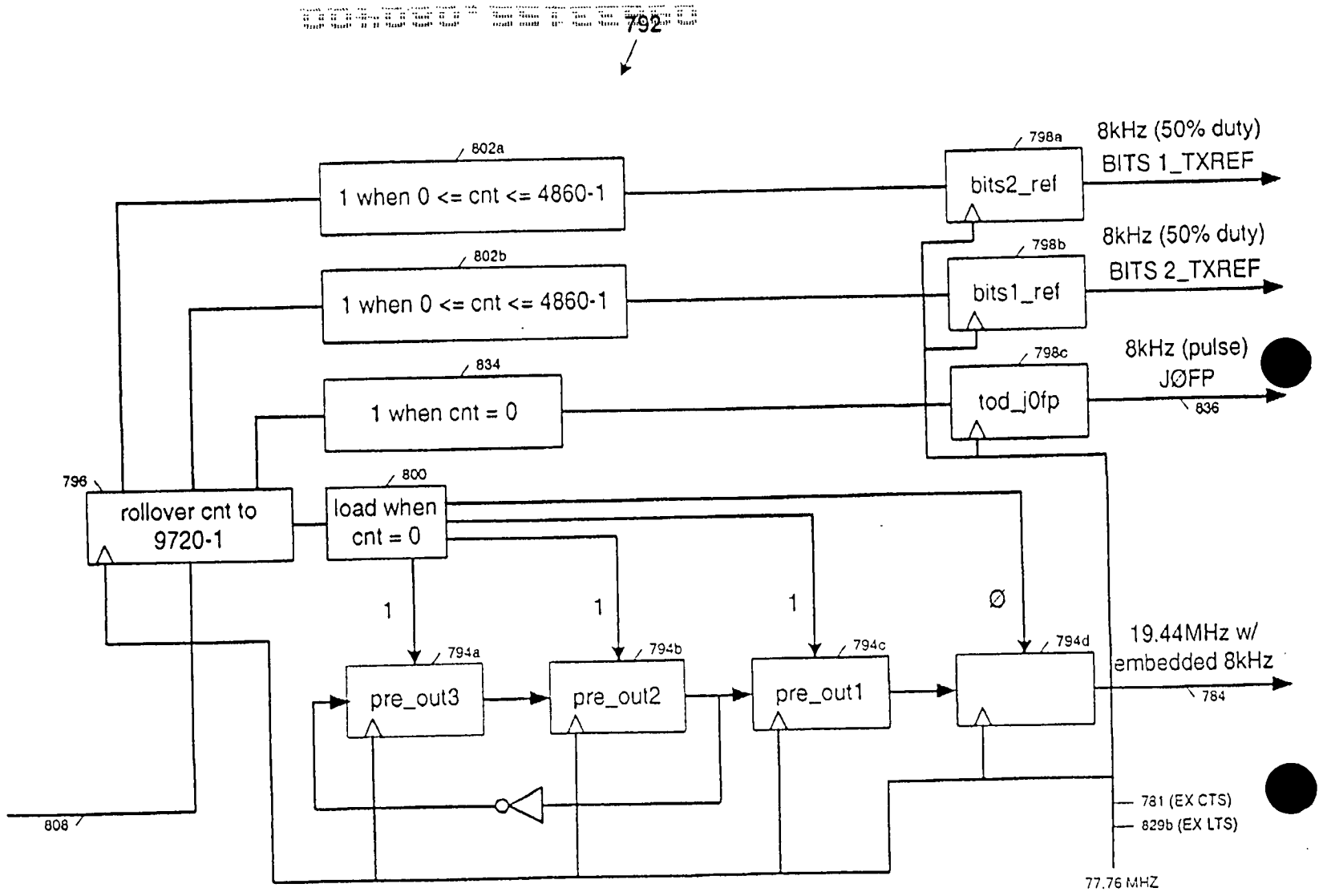
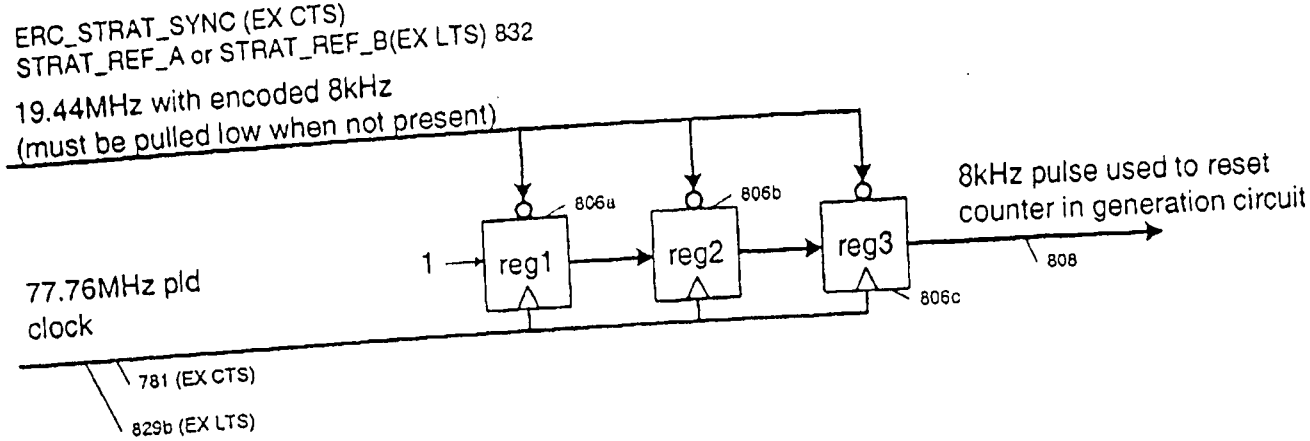

792
802a
1 when 0 <= cnt <= 4860-1
802b
1 when 0 <= cnt <= 4860-1
834
1 when cnt = 0
796
rollover cnt to 9720-1
800
load when cnt = 0
1
1
1
Ø
794a
pre_out3
794b
pre_out2
794c
pre_out1
794d
798a
bits2_ref
798b
bits1_ref
798c
tod_j0fp
8kHz (50% duty) BITS 1_TXREF
8kHz (50% duty) BITS 2_TXREF
8kHz (pulse) JØFP
836
19.44MHz w/ embedded 8kHz
784
781 (EX CTS)
829b (EX LTS)
77.76 MHZ
808

Fig. 52

804



Extractor

FIG. 53

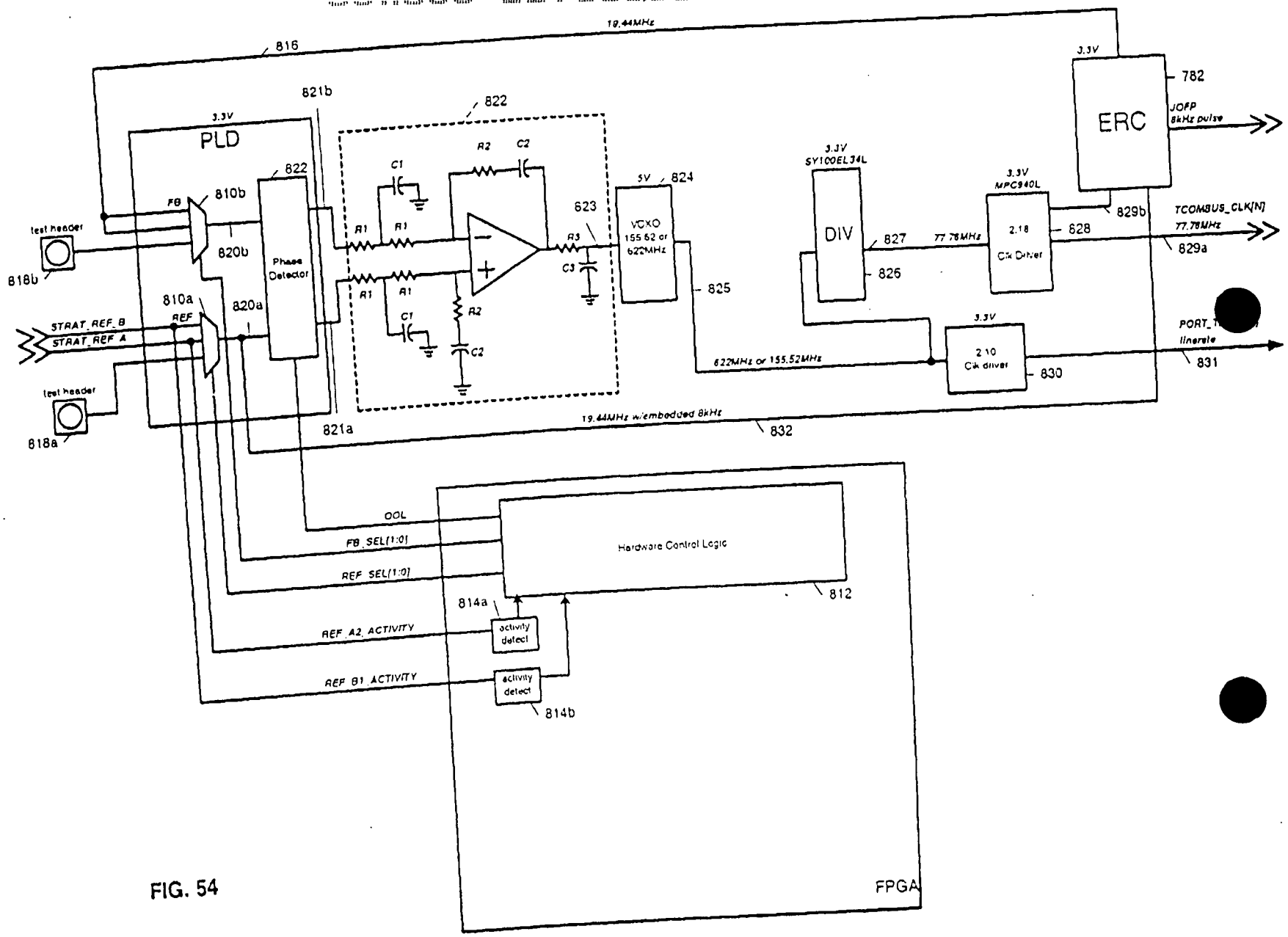


FIG. 54

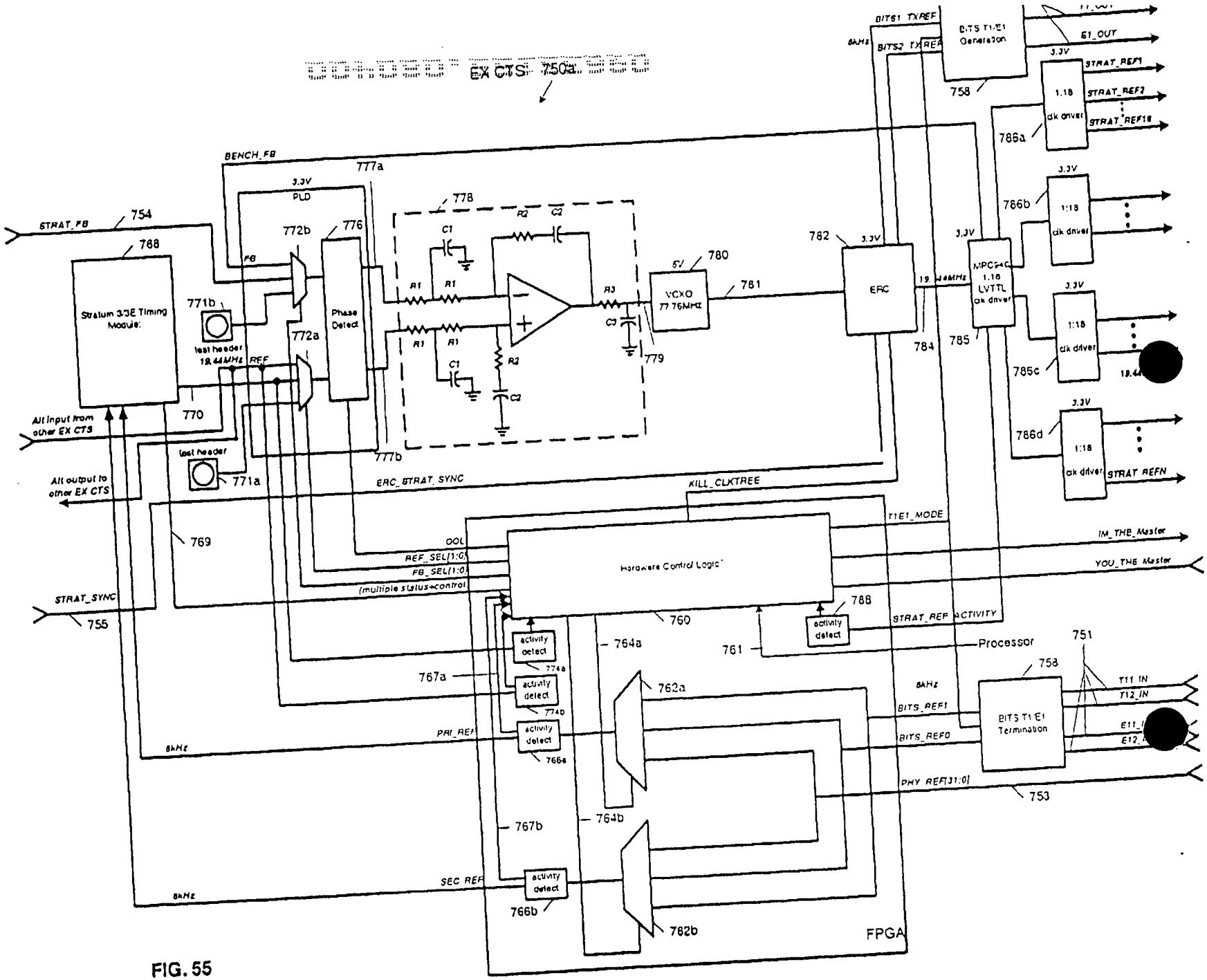

EX CTS 750a
BENCH_FB
777a
3.3V
PLD
778
R2
C2
C1
STRAT_FB
754
772b
776
788
FB
Stratum 3/3E Timing Module
771b
test header 19.44MHz REF
772a
Phase Detect
R1
R3
5V
780
VCXO 77.76MHz
781
779
C3
C2
All input from other EX CTS
770
777b
All output to other EX CTS
test header
771a
ERC_STRAT_SYNC
KILL_CLKTREE
769
T1E1_MODE
IM_THE_Master
YOU_THE Master
REF_SEL[1:0]
FB_SEL[1:0]
(multiple status+control)
Hardware Control Logic
760
STRAT_SYNC
755
activity detect
774a
774b
774c
764a
767a
762a
761
STRAT_REF ACTIVITY
Processor
751
758
8kHz
BITS_REF1
BITS_REF0
BITS T1/E1 Termination
T11 IN
T12_IN
PRI_REF
766a
PHY REF[31:0]
753
767b
764b
SEC REF
766b
762b
FPGA
782
ERC
784
785
MPCS4C 1.1B LVTTL clk driver
786a
786b
785c
786d
1:18 clk driver
STRAT_REF1
STRAT_REF2
STRAT_REF18
STRAT_REFN
BITS1 TX/REF
BITS2 TX/REF
BITS T1/E1 Generation
E1_OUT
19.44MHz

FIG. 55

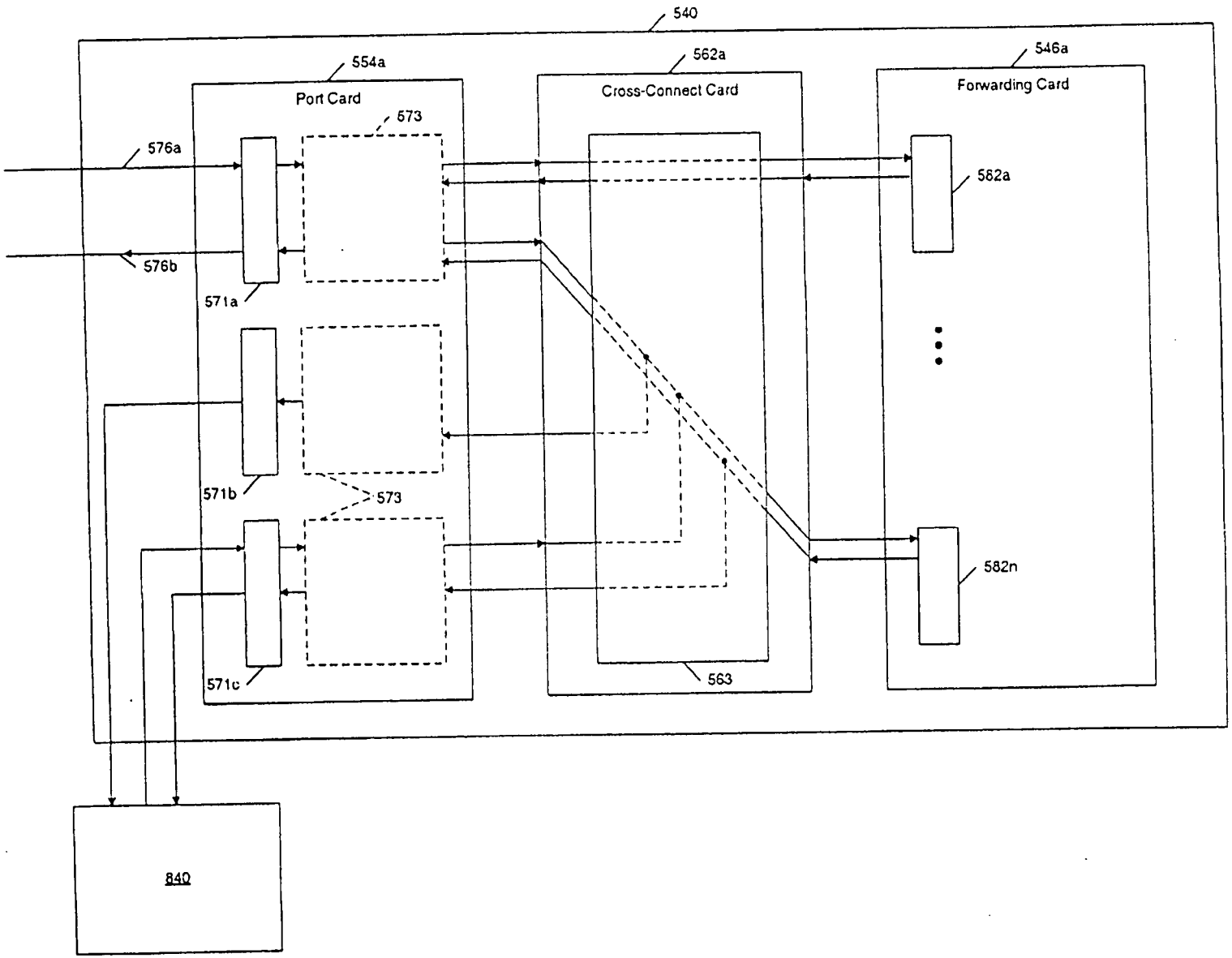

540
554a
Port Card
562a
Cross-Connect Card
546a
Forwarding Card
573
576a
576b
571a
582a
571b
573
582n
571c
563
840

Fig. 56

FIG.57

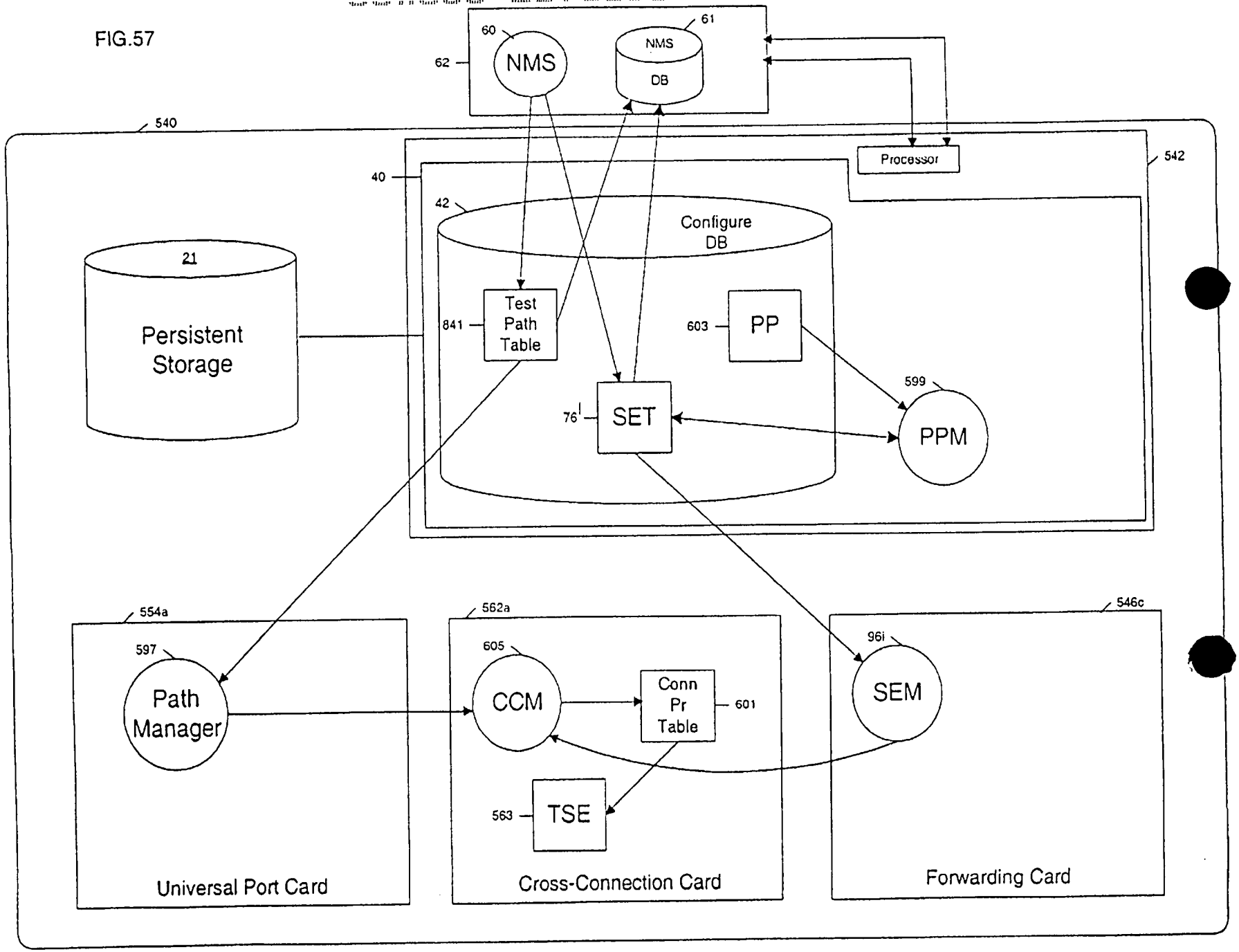

60
NMS
61
NMS
DB
62
540
542
Processor
40
42
Configure
DB
21
Persistent
Storage
841
Test
Path
Table
603
PP
599
76
SET
PPM
554a
597
Path
Manager
562a
605
CCM
Conn
Pr
Table
601
563
TSE
546c
96i
SEM
Universal Port Card
Cross-Connection Card
Forwarding Card

FIG. 58

Test Path Table 841

	Path LID	UP Port LID	Time Slot	# of Time Slots	Monitor (844)	Enable Port Receiver (845)	...
842	1666	1232	4	3	Ingress	No	
843	1666	1233	4	3	Egress	No	
844	1666	1233	4	3	Ingress	Yes	
	⋮	⋮	⋮	⋮			⋮